

T171018t1R303-M001.txt

Successfully removed tdl files

Successfully started file /tmp/advantest/_default/hardware.tdl

* DIAGNOSIS VERSION R6.06-1(for viewpoint R8.00 or later) *

* SYSTEM NAME T6671E

***4

*

* DIAGNOSIS STARTED AT 2017/11/15

09:34:50

*

----- SYSTEM INFORMATION BEGIN

SYSTEM : T6671E MEC4
MAIN CABINET PIN : 1 - 256
TH1 PIN : 1 - 256
DPU DPS CH : 1 2 3 4 5 6 7 8
UDC CH : 1 2 3 4
MDC CH : 1 2 3 4 5 6 7 8
LINE FREQUENCY : 60 Hz
DMM MODEL : R6581T
MAX STE : 0X01000000 (16MW)
PARALLEL MODE : AVAILABLE
OPTIONS : HRB OFF SCPG ON ALPG ON MIXED
ON
PDS : 1 - 256
FMUX : 1 - 256
MAX SCPG(A, B) : 2Gbit, 2Gbit
MAX AFM : 128KW
MIXED OPTION : BASICS[CAL EM CM1] CM2 DCAP AFE
: AFG 1 2 3 4
: AFD 1 2 3 4
: VFG 1 2
: VFD 1 2
HSCLK : NONE

----- SYSTEM INFORMATION END

----- DIAGNOSIS INFORMATION

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DIAGNOSIS TYPE           : SPECIFIED
DIAGNOSIS SPEC           : 100.00
DIAGNOSIS REPEAT COUNT  : 1
Test pins  MAIN FRAME    : 1 - 256
              TH1         : 1 - 256
Target test DPU  DPS CH  : 1 2 3 4 5 6 7 8
                   UDC CH : 1 2 3 4
                   MDC CH  : 1 2 3 4 5 6 7 8
Target test OPTIONS
PDS                   : 1 - 256
FMUX                  : 1 - 256
MIXED OPTION         : BASICS[CAL EM CM1] CM2 DCAP AFE
                   : AFG 1 2 3 4
                   : AFD 1 2 3 4
                   : VFG 1 2
                   : VFD 1 2
HSCLK                 : NONE
TARGET TEST NUMBERS  : ALL
----- DIAGNOSIS INFORMATION END
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```

Now initializing STE memory. Please wait.
Done.

Diagnosis Performance Board : 384pin + Mixed (BLS-024333)

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TEST 141000 TH1 PIN/CW/PCON PB CONNECTION CHECK
TEST 141000 RESULT -----
09:41:52 --> ***PASS
TEST 102003 DPU IFL GPIB          FUNCTION    CHECK
TEST 102003 RESULT -----
09:41:53 --> ***PASS
TEST 102108 DPU IFA  DIAG LOAD    MEASURE   & CHECK
TEST 102108 RESULT -----
09:41:58 --> ***PASS
TEST 102113 DPU IFA  DIAG REF.    INITIALIZE
TEST 102113 RESULT -----

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```
09:41:59 --> ***PASS
TEST 102118 DPU IFA   DIAG ADC   INITIALIZE
TEST 102118 RESULT -----
09:42:03 --> ***PASS
TEST 102328 DPU UDC   VSIM VS    INITIALIZE
TEST 102328 RESULT -----
09:42:24 --> ***PASS
TEST 102333 DPU UDC   VSIM IM    INITIALIZE
TEST 102333 RESULT -----
09:42:49 --> ***PASS
TEST 102338 DPU UDC   VSIM CLP D/A INITIALIZE
TEST 102338 RESULT -----
09:42:50 --> ***PASS
TEST 102343 DPU UDC   ISVM IS    INITIALIZE
TEST 102343 RESULT -----
09:43:34 --> ***PASS
TEST 102348 DPU UDC   ISVM VM    INITIALIZE
TEST 102348 RESULT -----
09:43:51 --> ***PASS
TEST 102528 DPU MDC   VSIM VS    INITIALIZE
TEST 102528 RESULT -----
09:44:13 --> ***PASS
TEST 102533 DPU MDC   VSIM IM    INITIALIZE
TEST 102533 RESULT -----
09:44:44 --> ***PASS
TEST 102543 DPU MDC   ISVM IS    INITIALIZE
TEST 102543 RESULT -----
09:46:24 --> ***PASS
TEST 102548 DPU MDC   ISVM VM    INITIALIZE
TEST 102548 RESULT -----
09:46:39 --> ***PASS
TEST 102728 DPU DPS   VSIM VS    INITIALIZE
TEST 102728 RESULT -----
09:46:50 --> ***PASS
TEST 102733 DPU DPS   VSIM IM    INITIALIZE
TEST 102733 RESULT -----
09:47:49 --> ***PASS
TEST 102763 DPU DPS   VSVM VM    INITIALIZE
```

TEST 102763 RESULT -----
09:47:57 --> ***PASS
TEST 103328 DPU UDC VSIM VS ACCURACY CHECK
TEST 103328 RESULT -----
09:48:01 --> ***PASS
TEST 103333 DPU UDC VSIM IM ACCURACY CHECK
TEST 103333 RESULT -----
09:48:30 --> ***PASS
TEST 103338 DPU UDC VSIM CLAMP ACCURACY CHECK
TEST 103338 RESULT -----
09:48:43 --> ***PASS
TEST 103343 DPU UDC ISVM IS ACCURACY CHECK
TEST 103343 RESULT -----
09:49:17 --> ***PASS
TEST 103348 DPU UDC ISVM VM ACCURACY CHECK
TEST 103348 RESULT -----
09:49:25 --> ***PASS
TEST 103353 DPU UDC ISVM CLAMP ACCURACY CHECK
TEST 103353 RESULT -----
09:49:33 --> ***PASS
TEST 103358 DPU UDC MVM VM ACCURACY CHECK
TEST 103358 RESULT -----
09:49:40 --> ***PASS
TEST 103528 DPU MDC VSIM VS ACCURACY CHECK
TEST 103528 RESULT -----
09:49:45 --> ***PASS
TEST 103533 DPU MDC VSIM IM ACCURACY CHECK
TEST 103533 RESULT -----
09:50:13 --> ***PASS
TEST 103538 DPU MDC VSIM CLAMP ACCURACY CHECK
TEST 103538 RESULT -----
09:50:20 --> ***PASS
TEST 103543 DPU MDC ISVM IS ACCURACY CHECK
TEST 103543 RESULT -----
09:51:00 --> ***PASS
TEST 103548 DPU MDC ISVM VM ACCURACY CHECK
TEST 103548 RESULT -----
09:51:08 --> ***PASS

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TEST 103553 DPU MDC ISVM CLAMP ACCURACY CHECK
TEST 103553 RESULT -----
09:51:15 --> ***PASS
TEST 103558 DPU MDC MVM VM ACCURACY CHECK
TEST 103558 RESULT -----
09:51:21 --> ***PASS
TEST 103728 DPU DPS VSIM VS ACCURACY CHECK
TEST 103728 RESULT -----
09:51:23 --> ***PASS
TEST 103733 DPU DPS VSIM IM ACCURACY CHECK
TEST 103733 RESULT -----
09:52:05 --> ***PASS
TEST 103738 DPU DPS VSIM CLAMP ACCURACY CHECK
TEST 103738 RESULT -----
09:52:30 --> ***PASS
TEST 103763 DPU DPS VSVM VS ACCURACY CHECK
TEST 103763 RESULT -----
09:52:33 --> ***PASS
TEST 103768 DPU DPS VSVM VM ACCURACY CHECK
TEST 103768 RESULT -----
09:52:44 --> ***PASS
TEST 104303 DPU UDC VSIM IM SETTLING CHECK
TEST 104303 RESULT -----
09:55:09 --> ***PASS
TEST 104503 DPU MDC VSIM IM SETTLING CHECK
TEST 104503 RESULT -----
09:55:14 --> ***PASS
TEST 104703 DPU DPS VSIM IM SETTLING CHECK
TEST 104703 RESULT -----
09:55:33 --> ***PASS
TEST 106308 DPU UDC GUARD ALARM CHECK
TEST 106308 RESULT -----
09:55:42 --> ***PASS
TEST 106508 DPU MDC GUARD ALARM CHECK
TEST 106508 RESULT -----
09:55:46 --> ***PASS
TEST 106533 DPU MDC PARALLEL TEST CHECK
TEST 106533 RESULT -----

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```
09:55:46 --> ***PASS
TEST 106708 DPU DPS GUARD ALARM CHECK
TEST 106708 RESULT -----
09:55:57 --> ***PASS
TEST 106723 DPU DPS OSC ALARM CHECK
TEST 106723 RESULT -----
09:56:03 --> ***PASS
TEST 106728 DPU DPS KELVIN ALARM CHECK
TEST 106728 RESULT -----
09:56:03 --> ***PASS
TEST 107028 DPU DPU-PG (SELF) CHECK
TEST 107028 RESULT -----
09:56:04 --> ***PASS
TEST 109303 DPU UDC PRECHECK
TEST 109303 RESULT -----
09:56:05 --> ***PASS
TEST 109503 DPU MDC PRECHECK
TEST 109503 RESULT -----
09:56:06 --> ***PASS
TEST 109703 DPU DPS PRECHECK
TEST 109703 RESULT -----
09:56:41 --> ***PASS
TEST 110100 RATE REGISTER READ/WRITE CHECK
TEST 110100 RESULT -----
09:56:41 --> ***PASS
TEST 110200 RATE REFCLK CHECK
TEST 110200 RESULT -----
09:56:50 --> ***PASS
TEST 110300 RATE REFCLK PAUSE CHECK
TEST 110300 RESULT -----
09:56:55 --> ***PASS
TEST 110400 RATE SNCCLK PULSE WIDTH CHECK
TEST 110400 RESULT -----
09:56:56 --> ***PASS
TEST 110600 RATE COMBINATION CHECK
TEST 110600 RESULT -----
09:56:56 --> ***PASS
TEST 110700 RATE HOLD CONTINUE CHECK
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TEST 110700 RESULT -----
09:56:57 --> ***PASS
TEST 110800 RATE BURST CONTINUE CHECK
TEST 110800 RESULT -----
09:56:57 --> ***PASS
TEST 111000 RATE LOOP TPD MEASUREMENT CHECK
TEST 111000 RESULT -----
09:56:57 --> ***PASS
TEST 130000 DST REGISTER READ/WRITE CHECK
TEST 130000 RESULT -----
09:56:58 --> ***PASS
TEST 130100 DST DIST INITIALIZE CHECK
TEST 130100 RESULT -----
09:57:01 --> ***PASS
TEST 130200 DST FAILOR INITIALIZE CHECK
TEST 130200 RESULT -----
09:57:02 --> ***PASS
TEST 121000 SQPG REGISTER READ/WRITE CHECK
TEST 121000 RESULT -----
09:57:02 --> ***PASS
TEST 121002 SQPG PIN MAP MEMORY WRITE & READ CHECK
TEST 121002 RESULT -----
09:57:02 --> ***PASS
TEST 121001 SQPG I/F ALU/BYTESHIFT CHECK
TEST 121001 RESULT -----
09:57:02 --> ***PASS
TEST 120400 SQPG VGC MEMORY DATA BIT CHECK
TEST 120400 RESULT -----
09:57:02 --> ***PASS
TEST 121005 SQPG VGC TRANSFER 2WORD MEM CHECK
TEST 121005 RESULT -----
09:57:02 --> ***PASS
TEST 121006 SQPG VGC TRANSFER 3WORD MEM CHECK
TEST 121006 RESULT -----
09:57:02 --> ***PASS
TEST 121007 SQPG VGC OPCODE/OPERAND ERROR CHECK
TEST 121007 RESULT -----
09:57:02 --> ***PASS
```

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TEST 122100 SQPG CTB MEMORY WRITE & READ CHECK
TEST 122100 RESULT -----
09:57:02 --> ***PASS
TEST 122300 SQPG CTB TRANSFER CHECK
TEST 122300 RESULT -----
09:57:02 --> ***PASS
TEST 121030 SQPG CTB MEM PARBIT TRANSFER CHECK
TEST 121030 RESULT -----
09:57:02 --> ***PASS
TEST 121050 SQPG VGC/CTB/PARITY GENERATION CHECK
TEST 121050 RESULT -----
09:57:02 --> ***PASS
TEST 121040 SQPG VGC/CTB/TTB MEMORY SENSE CHECK
TEST 121040 RESULT -----
09:57:02 --> ***PASS
TEST 122000 SQPG TTB REGISTER R/W CHECK
TEST 122000 RESULT -----
09:57:03 --> ***PASS
TEST 122200 SQPG TTB MEMORY WRITE & READ CHECK
TEST 122200 RESULT -----
09:57:03 --> ***PASS
TEST 122400 SQPG STE TRANSFER CHECK
TEST 122400 RESULT -----
09:57:03 --> ***PASS
TEST 122401 SQPG TTB TRANSFER CHECK BY PHYSICAL PIN
TEST 122401 RESULT -----
09:57:03 --> ***PASS
TEST 122201 SQPG TTB PARTIAL R/W CHECK
TEST 122201 RESULT -----
09:57:03 --> ***PASS
TEST 122900 SQPG TTB PARITY BIT GENERATION CHECK
TEST 122900 RESULT -----
09:57:04 --> ***PASS
TEST 121003 SQPG TTB PIN MAP MEMORY WRITE & READ CHECK
TEST 121003 RESULT -----
09:57:04 --> ***PASS
TEST 121004 SQPG PIN MAP MEMORY WRITE MASK CHECK
TEST 121004 RESULT -----
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09:57:04 --> ***PASS
TEST 121010 SQPG TTB REGISTER R/W CHECK BY LOGICAL PIN
TEST 121010 RESULT -----
09:57:04 --> ***PASS
TEST 121020 SQPG STE TRANSFER CHECK BY LOGICAL PIN
TEST 121020 RESULT -----
09:57:04 --> ***PASS
TEST 121021 SQPG TTB TRANSFER CHECK BY LOGICAL PIN
TEST 121021 RESULT -----
09:57:05 --> ***PASS
TEST 121900 SQPG STE MEM DIAG
TEST 121900 RESULT -----
09:58:24 --> ***PASS
TEST 121910 SQPG STE MEM DIAG MASK CHECK
TEST 121910 RESULT -----
10:06:49 --> ***PASS
TEST 121720 SQPG VGC/CTB MOVE INCREMENT CHECK
TEST 121720 RESULT -----
10:06:49 --> ***PASS
TEST 121730 SQPG VGC/CTB MOVE DECREMENT CHECK
TEST 121730 RESULT -----
10:06:49 --> ***PASS
TEST 121700 SQPG STE MOVE INCREMENT CHECK
TEST 121700 RESULT -----
10:07:18 --> ***PASS
TEST 121710 SQPG STE MOVE DECREMENT CHECK
TEST 121710 RESULT -----
10:07:47 --> ***PASS
TEST 121740 SQPG TTB MOVE INCREMENT CHECK
TEST 121740 RESULT -----
10:07:47 --> ***PASS
TEST 121750 SQPG TTB MOVE DECREMENT CHECK
TEST 121750 RESULT -----
10:07:47 --> ***PASS
TEST 121800 SQPG STE PATTERN FIND FORWARD CHECK
TEST 121800 RESULT -----
10:10:49 --> ***PASS
TEST 121810 SQPG STE PATTERN FIND BACK CHECK

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TEST 121810 RESULT -----
10:13:43 --> ***PASS
TEST 125000 MATCH/FP_DIST BOARD REVISION CHECK FOR PARALLEL
TEST 125000 RESULT -----
10:13:43 --> ***PASS
TEST 125010 SQPG TTB PARALLEL PIN R/W CHECK USED TTB DBR
TEST 125010 RESULT -----
10:13:59 --> ***PASS
TEST 120061 SQPG START CHECK
TEST 120061 RESULT -----
10:13:59 --> ***PASS
TEST 120062 SQPG INSTRUCTION CHECK (STPS 1)
TEST 120062 RESULT -----
10:13:59 --> ***PASS
TEST 121100 SQPG IF1 TIMER CHECK
TEST 121100 RESULT -----
10:14:25 --> ***PASS
TEST 120070 SQPG SPC PRE CHECK
TEST 120070 RESULT -----
10:14:25 --> ***PASS
TEST 120074 SQPG SPC STOP CHECK
TEST 120074 RESULT -----
10:14:25 --> ***PASS
TEST 120071 SQPG STA CHECK 1
TEST 120071 RESULT -----
10:14:25 --> ***PASS
TEST 120077 SQPG SPA STOP CHECK 1
TEST 120077 RESULT -----
10:14:25 --> ***PASS
TEST 120079 SQPG SPA STOP CHECK 3
TEST 120079 RESULT -----
10:14:25 --> ***PASS
TEST 120081 SQPG SPA CHECK (HOLD)
TEST 120081 RESULT -----
10:14:25 --> ***PASS
TEST 120082 SQPG SPA CHECK (BURST)
TEST 120082 RESULT -----
10:14:25 --> ***PASS
```

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TEST 120075 SQPG SPC CHECK (HOLD)
TEST 120075 RESULT -----
10:14:25 --> ***PASS
TEST 120076 SQPG SPC CHECK (BURST)
TEST 120076 RESULT -----
10:14:25 --> ***PASS
TEST 120140 SQPG SPA INNER LOOP STOP CHECK
TEST 120140 RESULT -----
10:14:25 --> ***PASS
TEST 120090 SQPG INSTRUCTION CHECK 1 (IDX1)
TEST 120090 RESULT -----
10:14:41 --> ***PASS
TEST 120092 SQPG INSTRUCTION CHECK 2 (IDX1 1) COMBINATION
TEST 120092 RESULT -----
10:14:41 --> ***PASS
TEST 120093 SQPG INSTRUCTION CHECK 2 (IDX1 2) COMBINATION
TEST 120093 RESULT -----
10:14:41 --> ***PASS
TEST 120120 SQPG SPC DIAG MODE CHECK
TEST 120120 RESULT -----
10:14:41 --> ***PASS
TEST 120177 SQPG CPU MODE CHECK
TEST 120177 RESULT -----
10:14:41 --> ***PASS
TEST 120201 SQPG BUMP MODE CHECK
TEST 120201 RESULT -----
10:14:41 --> ***PASS
TEST 120126 SQPG CONTINUE MODE CHECK
TEST 120126 RESULT -----
10:14:41 --> ***PASS
TEST 120089 SQPG INSTRUCTION CHECK (STHD)
TEST 120089 RESULT -----
10:14:41 --> ***PASS
TEST 120144 SQPG CTB OUTPUT CHECK 1
TEST 120144 RESULT -----
10:14:41 --> ***PASS
TEST 120145 SQPG CTB OUTPUT CHECK 2
TEST 120145 RESULT -----
```

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```
10:14:41 --> ***PASS
TEST 120146 SQPG CTB OUTPUT CHECK 3
TEST 120146 RESULT -----
10:14:58 --> ***PASS
TEST 120190 SQPG STE ADDRESS SYNC CHECK
TEST 120190 RESULT -----
10:14:58 --> ***PASS
TEST 120191 SQPG PATTERN SYNC CHECK
TEST 120191 RESULT -----
10:14:58 --> ***PASS
TEST 120194 SQPG SYNC MODE CHECK
TEST 120194 RESULT -----
10:14:58 --> ***PASS
TEST 120160 SQPG VGC CTB PARITY ERROR GENERATION CHECK
TEST 120160 RESULT -----
10:14:58 --> ***PASS
TEST 120165 SQPG VGC CTB PARITY ERROR MASK CHECK
TEST 120165 RESULT -----
10:14:58 --> ***PASS
TEST 120300 SQPG DIST OUTPUT CHECK
TEST 120300 RESULT -----
10:14:58 --> ***PASS
TEST 120094 SQPG INSTRUCTION CHECK (STI/JNI)
TEST 120094 RESULT -----
10:14:58 --> ***PASS
TEST 120101 SQPG INSTRUCTION CHECK (JEC 1)
TEST 120101 RESULT -----
10:14:58 --> ***PASS
TEST 120102 SQPG INSTRUCTION CHECK (JEC 2)
TEST 120102 RESULT -----
10:14:58 --> ***PASS
TEST 120130 SQPG LOOP INSTRUCTION COMBINATION CHECK
(STI/JNI/IDX1)
TEST 120130 RESULT -----
10:14:58 --> ***PASS
TEST 120200 SQPG INSTRUCTION CHECK (PAUSE)
TEST 120200 RESULT -----
10:15:11 --> ***PASS
```

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```
TEST 120202 SQPG PAUSE TIMER RANGE CHECK
TEST 120202 RESULT -----
10:15:12 --> ***PASS
TEST 120203 SQPG PGCNT OUTPUT CHECK
TEST 120203 RESULT -----
10:15:12 --> ***PASS
TEST 120204 SQPG PGCNT REPEAT MODE CHECK
TEST 120204 RESULT -----
10:15:13 --> ***PASS
TEST 120205 SQPG TS BASE ADDER CHECK
TEST 120205 RESULT -----
10:15:13 --> ***PASS
TEST 120121 SQPG SPC/SPA HOLD/BURST-CONTINUE CHECK
TEST 120121 RESULT -----
10:15:13 --> ***PASS
TEST 120206 SQPG TS OVER and PAUSE ERROR CHECK
TEST 120206 RESULT -----
10:15:13 --> ***PASS
TEST 120207 SQPG CONTINUE ERROR CHECK
TEST 120207 RESULT -----
10:15:13 --> ***PASS
TEST 122700 SQPG TTB OUTPUT CHECK 1 (DATA BIT)
TEST 122700 RESULT -----
10:15:14 --> ***PASS
TEST 122701 SQPG TTB OUTPUT CHECK 2 (ADDRESS LINE)
TEST 122701 RESULT -----
10:15:14 --> ***PASS
TEST 122702 SQPG TTB OUTPUT CHECK 3 (CACHE EXCHANGE)
TEST 122702 RESULT -----
10:15:14 --> ***PASS
TEST 122800 SQPG TTB OUTPUT CHECK 4 (JNI)
TEST 122800 RESULT -----
10:15:16 --> ***PASS
TEST 122850 SQPG CTB CACHE LOAD CHECK
TEST 122850 RESULT -----
10:15:19 --> ***PASS
TEST 122851 SQPG TTB CACHE LOAD CHECK
TEST 122851 RESULT -----
```

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```
10:16:06 --> ***PASS
TEST 122901 SQPG TTB PARITY ERROR CHECK
TEST 122901 RESULT -----
10:16:06 --> ***PASS
TEST 122902 SQPG TTB PARITY ERROR MASK CHECK
TEST 122902 RESULT -----
10:16:06 --> ***PASS
TEST 123001 DFM & CPEWINDOW REGISTER W/R CHECK 1-1
TEST 123001 RESULT -----
10:16:22 --> ***PASS
TEST 123002 DFM & CPEWINDOW REGISTER RESET CHECK 1-1
TEST 123002 RESULT -----
10:16:22 --> ***PASS
TEST 123010 DFM CONTINUOUS STORE CHECK 1-1
TEST 123010 RESULT -----
10:16:25 --> ***PASS
TEST 123011 DFM STORE BOUNDARY CHECK 1-1
TEST 123011 RESULT -----
10:16:27 --> ***PASS
TEST 123012 DFM STORE BOUNDARY CHECK 1-2
TEST 123012 RESULT -----
10:16:30 --> ***PASS
TEST 123020 DFM FAIL STORE CHECK 1-1
TEST 123020 RESULT -----
10:16:30 --> ***PASS
TEST 123021 DFM FAIL STORE CHECK 1-2
TEST 123021 RESULT -----
10:16:30 --> ***PASS
TEST 123022 DFM FAIL STORE CHECK 1-3
TEST 123022 RESULT -----
10:16:30 --> ***PASS
TEST 123030 DFM COUNTER CHECK 1-1
TEST 123030 RESULT -----
10:16:30 --> ***PASS
TEST 123031 DFM COUNTER COMPARISON BIT CHECK 1-1
TEST 123031 RESULT -----
10:16:31 --> ***PASS
TEST 123032 DFM COUNTER COMPARISON BIT CHECK 1-2
```

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```
TEST 123032 RESULT -----
10:16:31 --> ***PASS
TEST 123033 DFM STORE LOOP INSTRUCTION CHECK
TEST 123033 RESULT -----
10:16:31 --> ***PASS
TEST 123040 DFM NAVIGATION MODE CHECK 1-1
TEST 123040 RESULT -----
10:16:33 --> ***PASS
TEST 123041 DFM NAVIGATION MODE CHECK 1-2
TEST 123041 RESULT -----
10:16:36 --> ***PASS
TEST 123042 DFM NAVIGATION INTERVAL CHECK
TEST 123042 RESULT -----
10:16:39 --> ***PASS
TEST 123050 DFM HOLD/BURST CHECK 1-1
TEST 123050 RESULT -----
10:16:39 --> ***PASS
TEST 123051 DFM HOLD/BURST CHECK 1-2
TEST 123051 RESULT -----
10:16:39 --> ***PASS
TEST 123052 DFM CONTINUE MODE CHECK
TEST 123052 RESULT -----
10:16:40 --> ***PASS
TEST 123060 DFM INTERLEAVE CHECK 1-1
TEST 123060 RESULT -----
10:16:44 --> ***PASS
TEST 123070 DFM MEMORY DATA CHECK 1-1
TEST 123070 RESULT -----
10:16:44 --> ***PASS
TEST 123071 DFM MEMORY DATA CHECK 1-2
TEST 123071 RESULT -----
10:16:45 --> ***PASS
TEST 123080 DFM MEMORY RELIABILITY CHECK 1-1
TEST 123080 RESULT -----
10:16:49 --> ***PASS
TEST 123081 DFM MEMORY RELIABILITY CHECK 1-2
TEST 123081 RESULT -----
10:16:53 --> ***PASS
```

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```
TEST 123090 DFM FAIL STOP CHECK 1-1
TEST 123090 RESULT -----
10:16:53 --> ***PASS
TEST 123100 DFM STATE TRIGGER STORE CHECK
TEST 123100 RESULT -----
10:16:53 --> ***PASS
TEST 125001 MATCH PARA REGISTER W/R CHECK
TEST 125001 RESULT -----
10:16:53 --> ***PASS
TEST 125002 MATCH PARA REGISTER RESET CHECK
TEST 125002 RESULT -----
10:16:54 --> ***PASS
TEST 170199 SCPG INITIALIZE PRE CHECK
TEST 170199 RESULT -----
10:16:54 --> ***PASS
TEST 170200 SCPG INITIALIZE CHECK
TEST 170200 RESULT -----
10:17:25 --> ***PASS
TEST 170201 SCPG REGISTER READ/WRITE CHECK
TEST 170201 RESULT -----
10:17:25 --> ***PASS
TEST 170202 SCPG BASE & AP ADDITION READ/WRITE CHECK
TEST 170202 RESULT -----
10:17:25 --> ***PASS
TEST 170203 SCPG MEMORY SENSE CHECK
TEST 170203 RESULT -----
10:17:25 --> ***PASS
TEST 170210 SCPG MEMORY READ/WRITE CHECK 1
TEST 170210 RESULT -----
10:17:25 --> ***PASS
TEST 170211 SCPG MEMORY READ/WRITE CHECK 2
TEST 170211 RESULT -----
10:17:25 --> ***PASS
TEST 170212 SCPG MEMORY READ/WRITE CHECK 3
TEST 170212 RESULT -----
10:20:24 --> ***PASS
TEST 170213 SCPG MEMORY READ/WRITE CHECK 4
TEST 170213 RESULT -----
```


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10:23:23 --> ***PASS
TEST 170215 SCPG PARITY BIT READ/WRITE CHECK
TEST 170215 RESULT -----
10:23:23 --> ***PASS
TEST 170240 SCPG OUTPUT PATTERN CHECK 1
TEST 170240 RESULT -----
10:23:24 --> ***PASS
TEST 170241 SCPG OUTPUT PATTERN CHECK 2
TEST 170241 RESULT -----
10:23:25 --> ***PASS
TEST 170243 SCPG OUTPUT FORMAT CHECK 1
TEST 170243 RESULT -----
10:23:25 --> ***PASS
TEST 170244 SCPG OUTPUT FORMAT CHECK 2
TEST 170244 RESULT -----
10:23:25 --> ***PASS
TEST 170245 SCPG PARITY ERROR CHECK
TEST 170245 RESULT -----
10:23:25 --> ***PASS
TEST 170246 SCPG START ADDRESS CHECK 1
TEST 170246 RESULT -----
10:23:26 --> ***PASS
TEST 170247 SCPG START ADDRESS CHECK 2
TEST 170247 RESULT -----
10:23:26 --> ***PASS
TEST 170248 SCPG VECTOR FILE GROUP CHECK
TEST 170248 RESULT -----
10:23:26 --> ***PASS
TEST 170500 ALPG OBM(OUT BUFFER MEMORY) R/W CHECK (MARCHING)
TEST 170500 RESULT -----
10:23:27 --> ***PASS
TEST 170501 ALPG REGISTER R/W CHECK
TEST 170501 RESULT -----
10:23:27 --> ***PASS
TEST 170502 ALPG OBM, OBM-AP, WCS0, WCS1-10, XSCRAM R/W CHECK
TEST 170502 RESULT -----
10:23:27 --> ***PASS
TEST 170503 ALPG IDX1-8 REG. R/W CHECK

TEST 170503 RESULT -----
10:23:27 --> ***PASS
TEST 170504 ALPG TIMER DATA & MODE REG. R/W CHECK (TMSEL1-2)
TEST 170504 RESULT -----
10:23:27 --> ***PASS
TEST 170505 ALPG TIMER DATA & MODE REG. R/W CHECK (TMSEL 0)
TEST 170505 RESULT -----
10:23:27 --> ***PASS
TEST 170507 ALPG ALPG MEMORY SELECT CHECK
TEST 170507 RESULT -----
10:23:27 --> ***PASS
TEST 170508 ALPG XSCRAM DIVIDE R/W CHECK
TEST 170508 RESULT -----
10:23:27 --> ***PASS
TEST 170509 ALPG YSCRAM DIVIDE R/W CHECK
TEST 170509 RESULT -----
10:23:27 --> ***PASS
TEST 170510 ALPG WCS0 R/W CHECK (MARCHING)
TEST 170510 RESULT -----
10:23:27 --> ***PASS
TEST 170511 ALPG WCS1-10 R/W CHECK (MARCHING)
TEST 170511 RESULT -----
10:23:27 --> ***PASS
TEST 170512 ALPG PRE-XSCRAM R/W CHECK (MARCHING)
TEST 170512 RESULT -----
10:23:29 --> ***PASS
TEST 170513 ALPG PRE-YSCRAM R/W CHECK (MARCHING)
TEST 170513 RESULT -----
10:23:30 --> ***PASS
TEST 170514 ALPG XSCRAM R/W CHECK (MARCHING)
TEST 170514 RESULT -----
10:23:32 --> ***PASS
TEST 170515 ALPG YSCRAM R/W CHECK (MARCHING)
TEST 170515 RESULT -----
10:23:35 --> ***PASS
TEST 170516 ALPG ARIRAM R/W CHECK (MARCHING)
TEST 170516 RESULT -----
10:23:36 --> ***PASS

```
TEST 170517 ALPG MARK MEMORY R/W CHECK (MARCHING)
TEST 170517 RESULT -----
10:23:36 --> ***PASS
TEST 170518 ALPG CBM1 R/W CHECK (MARCHING)
TEST 170518 RESULT -----
10:23:38 --> ***PASS
TEST 170519 ALPG CBM2 R/W CHECK (MARCHING)
TEST 170519 RESULT -----
10:23:39 --> ***PASS
TEST 170520 ALPG TBRST (TESTER BUS RESET) CHECK
TEST 170520 RESULT -----
10:23:39 --> ***PASS
TEST 170521 ALPG PG RESET (CONTROL REG.) CHECK
TEST 170521 RESULT -----
10:23:39 --> ***PASS
TEST 170550 ALPG UNIVERSAL COUNTER CLEAR CHECK
TEST 170550 RESULT -----
10:23:39 --> ***PASS
TEST 170552 ALPG UNIVERSAL COUNTER REAL-TIME (RT) CHECK
TEST 170552 RESULT -----
10:23:57 --> ***PASS
TEST 170553 ALPG STA (START PC) REG. BIT CHECK
TEST 170553 RESULT -----
10:23:57 --> ***PASS
TEST 170554 ALPG PG STOP CHECK
TEST 170554 RESULT -----
10:23:57 --> ***PASS
TEST 170555 ALPG PG RESET CHECK
TEST 170555 RESULT -----
10:23:58 --> ***PASS
TEST 170560 ALPG NOP INSTRUCTION CHECK
TEST 170560 RESULT -----
10:23:58 --> ***PASS
TEST 170561 ALPG JMP INSTRUCTION CHECK
TEST 170561 RESULT -----
10:23:58 --> ***PASS
TEST 170563 ALPG STPS INSTRUCTION CHECK (PASS FLAG)
TEST 170563 RESULT -----
```

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```
10:23:58 --> ***PASS
TEST 170564 ALPG SPFL INSTRUCTION CHECK (FAIL FLAG)
TEST 170564 RESULT -----
10:23:59 --> ***PASS
TEST 170570 ALPG PC LINE CHECK 1 (IPC OUT)
TEST 170570 RESULT -----
10:23:59 --> ***PASS
TEST 170571 ALPG PC LINE CHECK 2 (EPC OUT & IESTK)
TEST 170571 RESULT -----
10:23:59 --> ***PASS
TEST 170572 ALPG PC LINE CHECK 3 (GDB OUT)
TEST 170572 RESULT -----
10:23:59 --> ***PASS
TEST 170573 ALPG PC (EPC) & TEST LINE CHECK (AGEN OUT)
TEST 170573 RESULT -----
10:23:59 --> ***PASS
TEST 170600 ALPG PC NOP/JMP CHECK
TEST 170600 RESULT -----
10:23:59 --> ***PASS
TEST 170601 ALPG PC NOP/RTN CHECK
TEST 170601 RESULT -----
10:23:59 --> ***PASS
TEST 170602 ALPG PC JMP/RTN CHECK
TEST 170602 RESULT -----
10:23:59 --> ***PASS
TEST 170603 ALPG PC JSR/RTN INSTRUCTION CHECK
TEST 170603 RESULT -----
10:24:00 --> ***PASS
TEST 170604 ALPG PC SUBROUTINE STACK BIT CHECK
TEST 170604 RESULT -----
10:24:01 --> ***PASS
TEST 170605 ALPG PC STACK OVER/UNDER ERROR CHECK
TEST 170605 RESULT -----
10:24:02 --> ***PASS
TEST 170606 ALPG PC IDX11-8 INSTRUCTION CHECK
TEST 170606 RESULT -----
10:24:03 --> ***PASS
TEST 170610 ALPG PC JN11-8 INSTRUCTION CHECK 1 (JNIN .-1)
```

TEST 170610 RESULT -----
10:24:03 --> ***PASS
TEST 170611 ALPG PC JNI1-8 INSTRUCTION CHECK 2 (JNIN .)
TEST 170611 RESULT -----
10:24:03 --> ***PASS
TEST 170612 ALPG PC JNI1-8 INSTRUCTION CHECK 3 (ZERO FLAG)
TEST 170612 RESULT -----
10:24:03 --> ***PASS
TEST 170613 ALPG PC JNC1-8 INSTRUCTION CHECK
TEST 170613 RESULT -----
10:24:04 --> ***PASS
TEST 170620 ALPG PC JZD INSTRUCTION CHECK
TEST 170620 RESULT -----
10:24:04 --> ***PASS
TEST 170623 ALPG PC STPS INSTRUCTION CHECK
TEST 170623 RESULT -----
10:24:04 --> ***PASS
TEST 170625 ALPG PC T, TM1, TM2 INSTRUCTION CHECK
TEST 170625 RESULT -----
10:24:04 --> ***PASS
TEST 170626 ALPG PC PCSYNC SIGNAL CHECK
TEST 170626 RESULT -----
10:24:05 --> ***PASS
TEST 170630 ALPG PC INSTRUCTION COMBINATION CHECK -1
TEST 170630 RESULT -----
10:24:05 --> ***PASS
TEST 170631 ALPG PC INSTRUCTION COMBINATION CHECK -2
TEST 170631 RESULT -----
10:24:05 --> ***PASS
TEST 170632 ALPG PC IDX1-8 DATA LOAD TIMMING CHECK
TEST 170632 RESULT -----
10:24:06 --> ***PASS
TEST 170633 ALPG PC IDXR1-8 DATA READ TIMMING CHECK
TEST 170633 RESULT -----
10:24:07 --> ***PASS
TEST 170639 ALPG PC INSTRUCTION COMBINATION CHECK -3
TEST 170639 RESULT -----
10:24:08 --> ***PASS

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```
TEST 170650 ALPG PC EPC GENERATION BIT CHECK
TEST 170650 RESULT -----
10:24:08 --> ***PASS
TEST 170651 ALPG PC EPC GENERATION COMBINATION CHECK
TEST 170651 RESULT -----
10:24:08 --> ***PASS
TEST 170660 ALPG PC EPC GENERATION & INTERRUPT (1)
TEST 170660 RESULT -----
10:24:09 --> ***PASS
TEST 170661 ALPG PC EPC GENERATION & INTERRUPT (2)
TEST 170661 RESULT -----
10:24:09 --> ***PASS
TEST 170700 ALPG AGEN SET INSTRUCTION CHECK 1
TEST 170700 RESULT -----
10:24:10 --> ***PASS
TEST 170701 ALPG AGEN SET INSTRUCTION CHECK 2
TEST 170701 RESULT -----
10:24:10 --> ***PASS
TEST 170702 ALPG AGEN XT FUNCTION CHECK
TEST 170702 RESULT -----
10:24:10 --> ***PASS
TEST 170703 ALPG AGEN YT FUNCTION CHECK
TEST 170703 RESULT -----
10:24:10 --> ***PASS
TEST 170704 ALPG AGEN XB INSTRUCTION CHECK
TEST 170704 RESULT -----
10:24:10 --> ***PASS
TEST 170705 ALPG AGEN YB INSTRUCTION CHECK
TEST 170705 RESULT -----
10:24:10 --> ***PASS
TEST 170710 ALPG AGEN D3 INSTRUCTION CHECK
TEST 170710 RESULT -----
10:24:10 --> ***PASS
TEST 170711 ALPG AGEN D4 INSTRUCTION CHECK
TEST 170711 RESULT -----
10:24:10 --> ***PASS
TEST 170712 ALPG AGEN D3 D4 REGISTER LINK CHECK
TEST 170712 RESULT -----
```

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10:24:10 --> ***PASS
TEST 170713 ALPG AGEN X-ALU SOURCE A FIELD CHECK
TEST 170713 RESULT -----
10:24:10 --> ***PASS
TEST 170714 ALPG AGEN Y-ALU SOURCE A FIELD CHECK
TEST 170714 RESULT -----
10:24:11 --> ***PASS
TEST 170715 ALPG AGEN X-ALU SOURCE B FIELD CHECK
TEST 170715 RESULT -----
10:24:11 --> ***PASS
TEST 170716 ALPG AGEN Y-ALU SOURCE B FIELD CHECK
TEST 170716 RESULT -----
10:24:12 --> ***PASS
TEST 170720 ALPG AGEN X-ALU OPERATION FIELD CHECK
TEST 170720 RESULT -----
10:24:12 --> ***PASS
TEST 170721 ALPG AGEN Y-ALU OPERATION FIELD CHECK 1
TEST 170721 RESULT -----
10:24:12 --> ***PASS
TEST 170722 ALPG AGEN Y-ALU OPERATION FIELD CHECK 2
TEST 170722 RESULT -----
10:24:12 --> ***PASS
TEST 170723 ALPG AGEN Y-ALU OPERATION FIELD CHECK 3
TEST 170723 RESULT -----
10:24:12 --> ***PASS
TEST 170724 ALPG AGEN Y-ALU OPERATION FIELD CHECK 4
TEST 170724 RESULT -----
10:24:12 --> ***PASS
TEST 170725 ALPG AGEN Y-ALU OPERATION FIELD CHECK 5
TEST 170725 RESULT -----
10:24:12 --> ***PASS
TEST 170726 ALPG AGEN X-ALU DESTINATION FIELD CHECK
TEST 170726 RESULT -----
10:24:12 --> ***PASS
TEST 170727 ALPG AGEN Y-ALU DESTINATION FIELD CHECK
TEST 170727 RESULT -----
10:24:12 --> ***PASS
TEST 170730 ALPG AGEN X-OUT (L<(XC, XB, XS)) DATA MUX CHECK

TEST 170730 RESULT -----
10:24:12 --> ***PASS
TEST 170731 ALPG AGEN Y-OUT (H<(YC, YB, YS)) DATA MUX CHECK
TEST 170731 RESULT -----
10:24:12 --> ***PASS
TEST 170732 ALPG AGEN XB CARRY CHECK
TEST 170732 RESULT -----
10:24:12 --> ***PASS
TEST 170733 ALPG AGEN XC CARRY CHECK
TEST 170733 RESULT -----
10:24:12 --> ***PASS
TEST 170734 ALPG AGEN YB CARRY CHECK
TEST 170734 RESULT -----
10:24:12 --> ***PASS
TEST 170735 ALPG AGEN YC CARRY CHECK
TEST 170735 RESULT -----
10:24:12 --> ***PASS
TEST 170736 ALPG AGEN LMAX<>HMAX INSTRUCTION CHECK
TEST 170736 RESULT -----
10:24:13 --> ***PASS
TEST 170740 ALPG AGEN Z INSTRUCTION CHECK 1
TEST 170740 RESULT -----
10:24:13 --> ***PASS
TEST 170741 ALPG AGEN Z INSTRUCTION CHECK 2
TEST 170741 RESULT -----
10:24:13 --> ***PASS
TEST 170742 ALPG AGEN Z INSTRUCTION CHECK 3
TEST 170742 RESULT -----
10:24:13 --> ***PASS
TEST 170743 ALPG AGEN N INSTRUCTION CHECK
TEST 170743 RESULT -----
10:24:13 --> ***PASS
TEST 170744 ALPG AGEN X MUX (X<L, X<H, X<L' Z, X<Z) CHECK
TEST 170744 RESULT -----
10:24:13 --> ***PASS
TEST 170745 ALPG AGEN Y MUX (Y<L, Y<H, Y<L' Z, Y<Z) CHECK
TEST 170745 RESULT -----
10:24:13 --> ***PASS


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TEST 170746 ALPG AGEN /X INSTRUCTION CHECK
TEST 170746 RESULT -----
10:24:13 --> ***PASS
TEST 170747 ALPG AGEN /Y INSTRUCTION CHECK
TEST 170747 RESULT -----
10:24:13 --> ***PASS
TEST 170748 ALPG AGEN /Z INSTRUCTION CHECK
TEST 170748 RESULT -----
10:24:13 --> ***PASS
TEST 170750 ALPG AGEN XMAX MASK CHECK
TEST 170750 RESULT -----
10:24:13 --> ***PASS
TEST 170751 ALPG AGEN YMAX MASK CHECK
TEST 170751 RESULT -----
10:24:13 --> ***PASS
TEST 170752 ALPG AGEN ZMAX MASK CHECK
TEST 170752 RESULT -----
10:24:13 --> ***PASS
TEST 170753 ALPG AGEN X-Z MULTIPLEXER (XZ ENABLE) CHECK
TEST 170753 RESULT -----
10:24:13 --> ***PASS
TEST 170754 ALPG AGEN Y-Z MULTIPLEXER (YZ ENABLE) CHECK
TEST 170754 RESULT -----
10:24:13 --> ***PASS
TEST 170755 ALPG AGEN X-N MULTIPLEXER (XN ENABLE) CHECK
TEST 170755 RESULT -----
10:24:13 --> ***PASS
TEST 170756 ALPG AGEN Y-N MULTIPLEXER (YN ENABLE) CHECK
TEST 170756 RESULT -----
10:24:13 --> ***PASS
TEST 170757 ALPG AGEN RF COUNTER CHECK
TEST 170757 RESULT -----
10:24:13 --> ***PASS
TEST 170760 ALPG AGEN X OFFSET ADDRESS ADDER CHECK
TEST 170760 RESULT -----
10:24:13 --> ***PASS
TEST 170761 ALPG AGEN Y OFFSET ADDRESS ADDER CHECK
TEST 170761 RESULT -----
```

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10:24:15 --> ***PASS
TEST 170770 ALPG AGEN ILM-SET INSTRUCTION CHECK
TEST 170770 RESULT -----
10:24:15 --> ***PASS
TEST 170771 ALPG AGEN XB EX-INSTRUCTION CHECK
TEST 170771 RESULT -----
10:24:15 --> ***PASS
TEST 170772 ALPG AGEN YB EX-INSTRUCTION CHECK
TEST 170772 RESULT -----
10:24:15 --> ***PASS
TEST 170773 ALPG AGEN D3 EX-INSTRUCTION CHECK
TEST 170773 RESULT -----
10:24:15 --> ***PASS
TEST 170774 ALPG AGEN D4 EX-INSTRUCTION CHECK
TEST 170774 RESULT -----
10:24:15 --> ***PASS
TEST 170775 ALPG AGEN XC EX-INSTRUCTION CHECK 1
TEST 170775 RESULT -----
10:24:15 --> ***PASS
TEST 170776 ALPG AGEN YC EX-INSTRUCTION CHECK 1
TEST 170776 RESULT -----
10:24:15 --> ***PASS
TEST 170777 ALPG AGEN XC EX-INSTRUCTION CHECK 2
TEST 170777 RESULT -----
10:24:15 --> ***PASS
TEST 170778 ALPG AGEN YC EX-INSTRUCTION CHECK 2
TEST 170778 RESULT -----
10:24:16 --> ***PASS
TEST 170780 ALPG AGEN ILM XB CARRY CHECK
TEST 170780 RESULT -----
10:24:16 --> ***PASS
TEST 170781 ALPG AGEN ILM XC CARRY CHECK
TEST 170781 RESULT -----
10:24:16 --> ***PASS
TEST 170782 ALPG AGEN ILM YB CARRY CHECK
TEST 170782 RESULT -----
10:24:16 --> ***PASS
TEST 170783 ALPG AGEN ILM YC CARRY CHECK

TEST 170783 RESULT -----
10:24:16 --> ***PASS
TEST 170784 ALPG AGEN Z EX-INSTRUCTION CHECK
TEST 170784 RESULT -----
10:24:16 --> ***PASS
TEST 170785 ALPG AGEN N EX-INSTRUCTION CHECK
TEST 170785 RESULT -----
10:24:16 --> ***PASS
TEST 170786 ALPG AGEN PSCROFF/SCROFF OUTPUT CHECK
TEST 170786 RESULT -----
10:24:16 --> ***PASS
TEST 170800 ALPG PRESCRAM FORMAT REG R/W CHECK
TEST 170800 RESULT -----
10:24:16 --> ***PASS
TEST 170801 ALPG PRESCRAM PREXSCRAM ADDRESS SELECT CHECK
TEST 170801 RESULT -----
10:24:17 --> ***PASS
TEST 170802 ALPG PRESCRAM PREYSCRAM ADDRESS SELECT CHECK
TEST 170802 RESULT -----
10:24:17 --> ***PASS
TEST 170803 ALPG PRESCRAM PREXSCRAM MULTIPLEX CHECK
TEST 170803 RESULT -----
10:24:17 --> ***PASS
TEST 170804 ALPG PRESCRAM PREYSCRAM MULTIPLEX CHECK
TEST 170804 RESULT -----
10:24:17 --> ***PASS
TEST 170805 ALPG PRESCRAM PREXSCRAM CONVERSION CHECK
TEST 170805 RESULT -----
10:24:17 --> ***PASS
TEST 170806 ALPG PRESCRAM PREYSCRAM CONVERSION CHECK
TEST 170806 RESULT -----
10:24:17 --> ***PASS
TEST 170807 ALPG PRESCRAM P. S. MODE X ADDRESS ON/OFF CHECK
TEST 170807 RESULT -----
10:24:17 --> ***PASS
TEST 170808 ALPG PRESCRAM P. S. MODE Y ADDRESS ON/OFF CHECK
TEST 170808 RESULT -----
10:24:17 --> ***PASS

TEST 170810 ALPG SCRAM FORMAT REG. R/W CHECK
TEST 170810 RESULT -----
10:24:17 --> ***PASS
TEST 170811 ALPG SCRAM XSCRAM ADDRESS SELECT CHECK
TEST 170811 RESULT -----
10:24:18 --> ***PASS
TEST 170812 ALPG SCRAM YSCRAM ADDRESS SELECT CHECK
TEST 170812 RESULT -----
10:24:18 --> ***PASS
TEST 170813 ALPG SCRAM XSCRAM MULTIPLEX CHECK (UX0-15)
TEST 170813 RESULT -----
10:24:18 --> ***PASS
TEST 170814 ALPG SCRAM YSCRAM MULTIPLEX CHECK (UY0-15)
TEST 170814 RESULT -----
10:24:19 --> ***PASS
TEST 170815 ALPG SCRAM XSCRAM CONVERSION CHECK (UX0-15)
TEST 170815 RESULT -----
10:24:19 --> ***PASS
TEST 170816 ALPG SCRAM YSCRAM CONVERSION CHECK (UY0-15)
TEST 170816 RESULT -----
10:24:19 --> ***PASS
TEST 170820 ALPG SCRAM XSCRAM MULTIPLEX CHECK (FX0-15)
TEST 170820 RESULT -----
10:24:19 --> ***PASS
TEST 170821 ALPG SCRAM YSCRAM MULTIPLEX CHECK (FY0-15)
TEST 170821 RESULT -----
10:24:19 --> ***PASS
TEST 170822 ALPG PRESCRAM REAL TIME OFF CHECK
TEST 170822 RESULT -----
10:24:19 --> ***PASS
TEST 170823 ALPG SCRAM REAL TIME OFF CHECK 1 (UX, UY)
TEST 170823 RESULT -----
10:24:19 --> ***PASS
TEST 170824 ALPG SCRAM REAL TIME OFF CHECK 2 (FX, FY)
TEST 170824 RESULT -----
10:24:19 --> ***PASS
TEST 170825 ALPG SCRAM XSCRAM CONVERSION CHECK (FX0-15)
TEST 170825 RESULT -----

10:24:19 --> ***PASS
TEST 170826 ALPG SCRAM YSCRAM CONVERSION CHECK (FY0-15)
TEST 170826 RESULT -----
10:24:19 --> ***PASS
TEST 170830 ALPG SCRAM XSYNC MODE FUNCTION CHECK
TEST 170830 RESULT -----
10:24:19 --> ***PASS
TEST 170831 ALPG SCRAM YSYNC MODE FUNCTION CHECK
TEST 170831 RESULT -----
10:24:19 --> ***PASS
TEST 170832 ALPG MAIN SCRAM SYNC MODE FUNCTION CHECK
TEST 170832 RESULT -----
10:24:19 --> ***PASS
TEST 170840 ALPG SCRAM XWINDOW FUNCTION CHECK
TEST 170840 RESULT -----
10:24:19 --> ***PASS
TEST 170841 ALPG SCRAM YWINDOW FUNCTION CHECK
TEST 170841 RESULT -----
10:24:19 --> ***PASS
TEST 170842 ALPG SCRAM WINDOW MODE FUNCTION CHECK
TEST 170842 RESULT -----
10:24:19 --> ***PASS
TEST 170850 ALPG PRESCRAM P. S. CONT REG ON/OFF CHECK
TEST 170850 RESULT -----
10:24:19 --> ***PASS
TEST 170851 ALPG PRESCRAM Y PLUS Z FUNCTION CHECK
TEST 170851 RESULT -----
10:24:19 --> ***PASS
TEST 170852 ALPG PRESCRAM Y XOR Z FUNCTION CHECK
TEST 170852 RESULT -----
10:24:19 --> ***PASS
TEST 170853 ALPG PRESCRAM P. S. MODE SELECT CHECK
TEST 170853 RESULT -----
10:24:19 --> ***PASS
TEST 170860 ALPG SCRAM PDS ADDRESS ACCESS CHECK 1 (PX PY)
TEST 170860 RESULT -----
10:24:19 --> ***PASS
TEST 170861 ALPG SCRAM FM ADDRESS ACCESS CHECK 1 (PX PY)

TEST 170861 RESULT -----
10:24:19 --> ***PASS
TEST 170862 ALPG SCRAM PDS ADDRESS ACCESS CHECK 2 (FX FY)
TEST 170862 RESULT -----
10:24:19 --> ***PASS
TEST 170863 ALPG SCRAM FM ADDRESS ACCESS CHECK 2 (FX FY)
TEST 170863 RESULT -----
10:24:19 --> ***PASS
TEST 170864 ALPG SCRAM DIVIDE MODE FUNCTION CHECK 1 (PX PY)
TEST 170864 RESULT -----
10:24:20 --> ***PASS
TEST 170865 ALPG SCRAM DIVIDE MODE FUNCTION CHECK 2 (FX FY)
TEST 170865 RESULT -----
10:24:20 --> ***PASS
TEST 170900 ALPG DGEN SET INSTRUCTION CHECK 1
TEST 170900 RESULT -----
10:24:22 --> ***PASS
TEST 170901 ALPG DGEN SET INSTRUCTION CHECK 2
TEST 170901 RESULT -----
10:24:22 --> ***PASS
TEST 170902 ALPG DGEN TP1 REGISTER FUNCTION CHECK

TEST 170902 RESULT -----
10:24:22 --> ***PASS
TEST 170903 ALPG DGEN TP2 REGISTER FUNCTION CHECK
TEST 170903 RESULT -----
10:24:22 --> ***PASS
TEST 170904 ALPG DGEN /D1 & DCMR1 FUNCTION CHECK
TEST 170904 RESULT -----
10:24:22 --> ***PASS
TEST 170905 ALPG DGEN /D2 & DCMR2 FUNCTION CHECK
TEST 170905 RESULT -----
10:24:22 --> ***PASS
TEST 170906 ALPG DGEN DATA HOLD INSTRUCTION(PD) CHECK
TEST 170906 RESULT -----
10:24:22 --> ***PASS
TEST 170907 ALPG DGEN DATA PARITY MODE CHECK (MODE PARITY)
TEST 170907 RESULT -----

10:24:23 --> ***PASS
TEST 170910 ALPG DGEN DSD, RCD, CCD REG. FUNCTION CHECK
TEST 170910 RESULT -----
10:24:23 --> ***PASS
TEST 170911 ALPG DGEN FP0 FUNCTION CHECK
TEST 170911 RESULT -----
10:24:23 --> ***PASS
TEST 170912 ALPG DGEN FP1 (X0. XOR. Y0) FUNCTION CHECK
TEST 170912 RESULT -----
10:24:23 --> ***PASS
TEST 170913 ALPG DGEN FP3 (X=Y+DSD) FUNCTION CHECK
TEST 170913 RESULT -----
10:24:23 --> ***PASS
TEST 170914 ALPG DGEN FP4 (X=/(Y+DSD)) FUNCTION CHECK
TEST 170914 RESULT -----
10:24:23 --> ***PASS
TEST 170915 ALPG DGEN FP7 (MASK PARITY) FUNCTION CHECK
TEST 170915 RESULT -----
10:24:23 --> ***PASS
TEST 170916 ALPG DGEN FP8 (X=RCD) FUCNTION CHECK
TEST 170916 RESULT -----
10:24:23 --> ***PASS
TEST 170917 ALPG DGEN FP9 (Y=CCD) FUNCTION CHECK
TEST 170917 RESULT -----
10:24:23 --> ***PASS
TEST 170918 ALPG DGEN FP10 (X=RCD. AND. Y=CCD) FUNCTION CHECK
TEST 170918 RESULT -----
10:24:23 --> ***PASS
TEST 170919 ALPG DGEN FP FUNCTION DATA INVERT CHECK
TEST 170919 RESULT -----
10:24:23 --> ***PASS
TEST 170920 ALPG DGEN ARIRAM ADDRESS FORMAT REG R/W CHECK
TEST 170920 RESULT -----
10:24:23 --> ***PASS
TEST 170921 ALPG DGEN ARIRAM ADDRESS SELECT FUNCTION CHECK
TEST 170921 RESULT -----
10:24:23 --> ***PASS
TEST 170922 ALPG DGEN ARIRAM DATA INVERT CHECK

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TEST 170922 RESULT -----
10:24:23 --> ***PASS
TEST 170923 ALPG DGEN ARIRAM DATA BIT CHECK
TEST 170923 RESULT -----
10:24:23 --> ***PASS
TEST 170924 ALPG DGEN ARIRAM SIGNAL HOLD CHECK
TEST 170924 RESULT -----
10:24:23 --> ***PASS
TEST 170925 ALPG DGEN ARIRAM INVERT REAL TIME INHIBIT CHECK
TEST 170925 RESULT -----
10:24:24 --> ***PASS
TEST 170930 ALPG DGEN TPXOR FUNCTION CHECK
TEST 170930 RESULT -----
10:24:24 --> ***PASS
TEST 170931 ALPG DGEN DSC FUNCTION CHECK
TEST 170931 RESULT -----
10:24:24 --> ***PASS
TEST 170932 ALPG DGEN CBMAP FUNCTION CHECK
TEST 170932 RESULT -----
10:24:24 --> ***PASS
TEST 170933 ALPG DGEN DSYNC FUNCTION CHECK
TEST 170933 RESULT -----
10:24:24 --> ***PASS
TEST 170934 ALPG DGEN DCF FUNCTION CHECK
TEST 170934 RESULT -----
10:24:24 --> ***PASS
TEST 170935 ALPG DGEN DSEL (TP1/2/XOR) FUNCTION CHECK
TEST 170935 RESULT -----
10:24:24 --> ***PASS
TEST 170936 ALPG DGEN TPS1 FUNCTION CHECK
TEST 170936 RESULT -----
10:24:24 --> ***PASS
TEST 170937 ALPG DGEN TPS2 FUNCTION CHECK
TEST 170937 RESULT -----
10:24:25 --> ***PASS
TEST 170940 ALPG DGEN TP1 EX-INSTRUCTION CHECK 1
TEST 170940 RESULT -----
10:24:25 --> ***PASS
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TEST 170941 ALPG DGEN TP2 EX-INSTRUCTION CHECK 1
TEST 170941 RESULT -----
10:24:25 --> ***PASS
TEST 170942 ALPG DGEN TP1 EX-INSTRUCTION CHECK 2
TEST 170942 RESULT -----
10:24:25 --> ***PASS
TEST 170943 ALPG DGEN TP2 EX-INSTRUCTION CHECK 2
TEST 170943 RESULT -----
10:24:25 --> ***PASS
TEST 170944 ALPG DGEN CBMAP EX-INSTRUCTION CHECK
TEST 170944 RESULT -----
10:24:26 --> ***PASS
TEST 170950 ALPG DGEN BWPE BD/CD/MD RF R/W CHECK
TEST 170950 RESULT -----
10:24:26 --> ***PASS
TEST 170951 ALPG DGEN BWPE ADDRESS SEL R/W CHECK
TEST 170951 RESULT -----
10:24:26 --> ***PASS
TEST 170952 ALPG DGEN BWPE ADDRESS SEL FUNCTION CHECK
TEST 170952 RESULT -----
10:24:26 --> ***PASS
TEST 170953 ALPG DGEN BWPE BD/CD/MD REAL-TIME WRITE CHECK
TEST 170953 RESULT -----
10:24:26 --> ***PASS
TEST 170954 ALPG DGEN BWPE BD/CD/MD REAL-TIME READ CHECK
TEST 170954 RESULT -----
10:24:26 --> ***PASS
TEST 170955 ALPG DGEN BWPE BD EMULATION CHECK
TEST 170955 RESULT -----
10:24:26 --> ***PASS
TEST 170956 ALPG DGEN BWPE CD EMULATION CHECK
TEST 170956 RESULT -----
10:24:26 --> ***PASS
TEST 170957 ALPG DGEN BWPE MD EMULATION CHECK
TEST 170957 RESULT -----
10:24:26 --> ***PASS
TEST 170958 ALPG DGEN BWPE TP1 EMULATION CHECK
TEST 170958 RESULT -----

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10:24:26 --> ***PASS
TEST 170959 ALPG DGEN BWPE TP2 EMULATION CHECK
TEST 170959 RESULT -----
10:24:26 --> ***PASS
TEST 171000 ALPG CDB CBMA/CBMB DATA OUTPUT CHECK
TEST 171000 RESULT -----
10:24:26 --> ***PASS
TEST 171001 ALPG CDB DA/DB DATA OUTPUT CHECK
TEST 171001 RESULT -----
10:24:27 --> ***PASS
TEST 171002 ALPG CDB DA/DB DATA SELECT CHECK
TEST 171002 RESULT -----
10:24:27 --> ***PASS
TEST 171003 ALPG CDB DSC OUTPUT CHECK
TEST 171003 RESULT -----
10:24:27 --> ***PASS
TEST 171004 ALPG CDB DCF OUTPUT CHECK
TEST 171004 RESULT -----
10:24:27 --> ***PASS
TEST 171005 ALPG CDB ARI DATA OUTPUT CHECK
TEST 171005 RESULT -----
10:24:27 --> ***PASS
TEST 171006 ALPG CDB ARI SIGNAL HOLD CHECK
TEST 171006 RESULT -----
10:24:27 --> ***PASS
TEST 171007 ALPG CDB ARI CONTROL REAL-TIME INHIBIT CHECK
TEST 171007 RESULT -----
10:24:27 --> ***PASS
TEST 171010 ALPG CDB TSO-3 OUTPUT CHECK
TEST 171010 RESULT -----
10:24:27 --> ***PASS
TEST 171011 ALPG CDB TS SYNC SELECT CHECK
TEST 171011 RESULT -----
10:24:27 --> ***PASS
TEST 171012 ALPG CDB FM CONTROL SIGNAL OUTPUT CHECK
TEST 171012 RESULT -----
10:24:27 --> ***PASS
TEST 171013 ALPG CDB MUT SYNC SIGNAL SELECT CHECK

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TEST 171013 RESULT -----
10:24:27 --> ***PASS
TEST 171014 ALPG CDB MUT CONTROL BIT OUTPUT CHECK
TEST 171014 RESULT -----
10:24:27 --> ***PASS
TEST 171015 ALPG CDB PPS CONTROL SIGNAL SELECT CHECK
TEST 171015 RESULT -----
10:24:28 --> ***PASS
TEST 171020 ALPG CDB PREF FUNCTION CHECK
TEST 171020 RESULT -----
10:24:28 --> ***PASS
TEST 171021 ALPG CDB CPE1-4, DRE1-2, RINV BIT CHECK
TEST 171021 RESULT -----
10:24:28 --> ***PASS
TEST 171022 ALPG CDB CPE1-4, DRE1-2, RINV SELECT CHECK
TEST 171022 RESULT -----
10:24:28 --> ***PASS
TEST 171023 ALPG CDB WINDOW MODE CHECK
TEST 171023 RESULT -----
10:24:28 --> ***PASS
TEST 171024 ALPG CDB FLAG OUTPUT CHECK
TEST 171024 RESULT -----
10:24:28 --> ***PASS
TEST 171025 ALPG CDB SYNC SELECT CHECK
TEST 171025 RESULT -----
10:24:28 --> ***PASS
TEST 171105 ALPG PGCNT TIMER1 (GENERAL TIMER) FULL BIT COUNT
TEST 171105 RESULT -----
10:24:29 --> ***PASS
TEST 171110 ALPG PGCNT TIMER OPERATION CHECK-1 (TIMER DATA=#1)
TEST 171110 RESULT -----
10:24:29 --> ***PASS
TEST 171111 ALPG PGCNT TIMER OPERATION CHECK-2 (FIRST PATTERN
START)
TEST 171111 RESULT -----
10:24:29 --> ***PASS
TEST 171113 ALPG PGCNT TIMER CHECK (JET INSTRUCTION CHECK)
TEST 171113 RESULT -----
```

10:24:29 --> ***PASS
TEST 171114 ALPG PGCNT TIMER BRANCH MODE CHECK
TEST 171114 RESULT -----
10:24:29 --> ***PASS
TEST 171115 ALPG PGCNT TIMER REFRESH-A MODE CHECK(T START &
RESTART)
TEST 171115 RESULT -----
10:24:29 --> ***PASS
TEST 171116 ALPG PGCNT TIMER REFRESH-B MODE CHECK(T START &
RESTART)
TEST 171116 RESULT -----
10:24:30 --> ***PASS
TEST 171120 ALPG PGCNT TIMER1 RANGE CHECK (CURRENT CLK)
TEST 171120 RESULT -----
10:24:30 --> ***PASS
TEST 171121 ALPG PGCNT TIMER1 RANGE CHECK (TIMER RANGE)
TEST 171121 RESULT -----
10:24:31 --> ***PASS
TEST 171124 ALPG PGCNT TM1 TM2 FUNCTION CHECK
TEST 171124 RESULT -----
10:24:32 --> ***PASS
TEST 171130 ALPG PC INTERRUPT CHECK (ISP REG. BIT CHECK, REF-B)
TEST 171130 RESULT -----
10:24:32 --> ***PASS
TEST 171131 ALPG PC INTERRUPT CHECK (INTERRUPT RETURN, REF-B)
TEST 171131 RESULT -----
10:24:32 --> ***PASS
TEST 171132 ALPG PC INTERRUPT CHECK (ISTK BIT CHECK, REF-B)
TEST 171132 RESULT -----
10:24:32 --> ***PASS
TEST 171133 ALPG PC INTERRUPT CHECK (JSR INST, REF-B)
TEST 171133 RESULT -----
10:24:32 --> ***PASS
TEST 171134 ALPG PC INTERRUPT CHECK (IDXI INST, OP.=0, REF-B)
TEST 171134 RESULT -----
10:24:32 --> ***PASS
TEST 171135 ALPG PC INTERRUPT CHECK (IDXI INST, OP.<>0, REF-B)
TEST 171135 RESULT -----

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```
10:24:32 --> ***PASS
TEST 171136 ALPG PC INTERRUPT CHECK (JNI
INST, OP. =PC, IDX=0, REF-B)
TEST 171136 RESULT -----
10:24:32 --> ***PASS
TEST 171137 ALPG PC INTERRUPT CHECK (JNI
INST, OP. =PC, IDX<>0, REF-B)
TEST 171137 RESULT -----
10:24:32 --> ***PASS
TEST 171138 ALPG PC INTERRUPT CHECK (JNI INST, OP. <>PC, REF-B)
TEST 171138 RESULT -----
10:24:32 --> ***PASS
TEST 171140 ALPG PC INTERRUPT CHECK (RTN INST, REF-B)
TEST 171140 RESULT -----
10:24:32 --> ***PASS
TEST 171141 ALPG PC INTERRUPT CHECK (STPS INST, REF-B)
TEST 171141 RESULT -----
10:24:32 --> ***PASS
TEST 171142 ALPG PC INTERRUPT CHECK (STFL INST, REF-B)
TEST 171142 RESULT -----
10:24:32 --> ***PASS
TEST 171143 ALPG PC INTERRUPT CHECK (PCG MODE=2, REF-B)
TEST 171143 RESULT -----
10:24:32 --> ***PASS
TEST 171144 ALPG PC INTERRUPT CHECK (I INST, REF-B)
TEST 171144 RESULT -----
10:24:32 --> ***PASS
TEST 171145 ALPG PC INTERRUPT CHECK (NOP OR JMP INST, BRANCH)
TEST 171145 RESULT -----
10:24:32 --> ***PASS
TEST 171146 ALPG PC INTERRUPT CHECK (JSR OR RTN&STACK
INST, BRANCH)
TEST 171146 RESULT -----
10:24:32 --> ***PASS
TEST 171150 ALPG PC INTERRUPT CHECK (IDXI INST, OP. <>0, BRANCH)
TEST 171150 RESULT -----
10:24:32 --> ***PASS
TEST 171151 ALPG PC INTERRUPT CHECK (IDXI INST, OP. =0, BRANCH)
```

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```
TEST 171151 RESULT -----
10:24:32 --> ***PASS
TEST 171152 ALPG PC INTERRUPT CHECK (JNI INST, OP. =PC, BRANCH)
TEST 171152 RESULT -----
10:24:32 --> ***PASS
TEST 171153 ALPG PC INTERRUPT CHECK (JNI INST, OP. <>PC, BRANCH)
TEST 171153 RESULT -----
10:24:32 --> ***PASS
TEST 171200 ALPG PGCNT REPEAT FUNCTION CHECK (RESTART)
TEST 171200 RESULT -----
10:24:33 --> ***PASS
TEST 171201 ALPG PGCNT REPEAT FUNCTION CHECK (PASS/FAIL
STATUS)
TEST 171201 RESULT -----
10:24:33 --> ***PASS
TEST 171202 ALPG PGCNT REPEAT FUNCTION CHECK (PASS/FAIL INTR
INHIBIT)
TEST 171202 RESULT -----
10:24:33 --> ***PASS
TEST 171203 ALPG PGCNT REPEAT FUNCTION CHECK (STACK ERROR
INTR)
TEST 171203 RESULT -----
10:24:33 --> ***PASS
TEST 171204 ALPG PGCNT REPEAT FUNCTION CHECK (STACK ERROR NOT
REPEAT)
TEST 171204 RESULT -----
10:24:33 --> ***PASS
TEST 171205 ALPG PGCNT REPEAT FUNCTION CHECK (STOP-PG REPEAT
STOP)
TEST 171205 RESULT -----
10:24:33 --> ***PASS
TEST 171500 LINK REGISTER READ/WRITE CHECK 1
TEST 171500 RESULT -----
10:24:34 --> ***PASS
TEST 171501 LINK REGISTER RESET CHECK 1
TEST 171501 RESULT -----
10:24:34 --> ***PASS
TEST 171510 LINK MEMORY READ/WRITE CHECK 1
```

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```
TEST 171510 RESULT -----
10:24:34 --> ***PASS
TEST 171520 LINK REGISTER READ/WRITE CHECK 2
TEST 171520 RESULT -----
10:24:34 --> ***PASS
TEST 171521 LINK REGISTER RESET CHECK 2
TEST 171521 RESULT -----
10:24:34 --> ***PASS
TEST 171540 LINK REGISTER READ/WRITE CHECK 3
TEST 171540 RESULT -----
10:24:34 --> ***PASS
TEST 171541 LINK REGISTER RESET CHECK 3
TEST 171541 RESULT -----
10:24:34 --> ***PASS
TEST 171600 LINK SCPG DATA PATH CHECK
TEST 171600 RESULT -----
10:24:35 --> ***PASS
TEST 171610 LINK SCPG DATA PATH CHECK 2
TEST 171610 RESULT -----
10:24:35 --> ***PASS
TEST 171620 LINK SCPG ERROR FLAG CHECK
TEST 171620 RESULT -----
10:24:37 --> ***PASS
TEST 171650 LINK SCPG DFM STORE CHECK
TEST 171650 RESULT -----
10:24:38 --> ***PASS
TEST 171651 LINK SCPG DFM STORE CHECK 2
TEST 171651 RESULT -----
10:24:38 --> ***PASS
TEST 171660 LINK SCPG /A FUNCTION CHECK
TEST 171660 RESULT -----
10:24:39 --> ***PASS
TEST 171670 LINK SCPG AP STORE CHECK
TEST 171670 RESULT -----
10:24:39 --> ***PASS
TEST 171700 LINK ALPG RTN CHECK
TEST 171700 RESULT -----
10:24:40 --> ***PASS
```

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```
TEST 171710 LINK ALPG DATA PATH CHECK
TEST 171710 RESULT -----
10:24:42 --> ***PASS
TEST 171720 LINK ALPG ERROR FLAG CHECK
TEST 171720 RESULT -----
10:24:44 --> ***PASS
TEST 171730 LINK ALPG SYNC CHECK
TEST 171730 RESULT -----
10:24:46 --> ***PASS
TEST 171740 LINK ALPG TS CPE PATH CHECK
TEST 171740 RESULT -----
10:24:49 --> ***PASS
TEST 171750 LINK ALPG DFM STORE CHECK
TEST 171750 RESULT -----
10:24:51 --> ***PASS
TEST 171770 LINK ALPG PAUSE CHECK
TEST 171770 RESULT -----
10:24:54 --> ***PASS
TEST 171780 LINK ALPG CYCLE SHIFT CHECK
TEST 171780 RESULT -----
10:24:57 --> ***PASS
TEST 172000 LINK PDS CHANNEL SENSE CHECK
TEST 172000 RESULT -----
10:24:57 --> ***PASS
TEST 172001 LINK PDS BOARD SENSE CHECK
TEST 172001 RESULT -----
10:24:57 --> ***PASS
TEST 172010 LINK PDS REGISTER READ/WRITE CHECK
TEST 172010 RESULT -----
10:24:57 --> ***PASS
TEST 172020 LINK PDS PIN DATA READ/WRITE CHECK
TEST 172020 RESULT -----
10:24:57 --> ***PASS
TEST 172030 LINK PDS REGISTER RESET CHECK
TEST 172030 RESULT -----
10:24:57 --> ***PASS
TEST 172040 LINK PDS SELECT MONITOR READ/WRITE CHECK
TEST 172040 RESULT -----
```


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10:24:57 --> ***PASS
TEST 172060 LINK PDS PARALLEL MONITOR READ/WRITE CHECK
TEST 172060 RESULT -----
10:24:57 --> ***PASS
TEST 172100 LINK PDS SCPG DATA SELECT CHECK 1
TEST 172100 RESULT -----
10:24:58 --> ***PASS
TEST 173000 AFM REGISTER READ/WRITE CHECK 1
TEST 173000 RESULT -----
10:24:58 --> ***PASS
TEST 173001 AFM REGISTER RESET CHECK 1
TEST 173001 RESULT -----
10:24:58 --> ***PASS
TEST 173010 AFM REGISTER READ/WRITE CHECK 2
TEST 173010 RESULT -----
10:24:58 --> ***PASS
TEST 173011 AFM REGISTER RESET CHECK 2
TEST 173011 RESULT -----
10:24:58 --> ***PASS
TEST 173020 AFM REGISTER READ/WRITE CHECK 3
TEST 173020 RESULT -----
10:24:58 --> ***PASS
TEST 173021 AFM REGISTER RESET CHECK 3
TEST 173021 RESULT -----
10:24:58 --> ***PASS
TEST 173030 AFM READ/WRITE ADDRESS SELECT CHECK
TEST 173030 RESULT -----
10:24:58 --> ***PASS
TEST 173040 AFM AFM MEMORY READ/WRITE CHECK
TEST 173040 RESULT -----
10:24:58 --> ***PASS
TEST 173050 AFM AFM MEMORY SET/RESET CHECK
TEST 173050 RESULT -----
10:24:58 --> ***PASS
TEST 173060 AFM AFM MEMORY MOVE CHECK
TEST 173060 RESULT -----
10:24:58 --> ***PASS
TEST 173100 AFM ALPG DFM STORE CHECK

TEST 173100 RESULT -----
10:24:59 --> ***PASS
TEST 173120 AFM ALPG CYCLE SHIFT CHECK
TEST 173120 RESULT -----
10:25:00 --> ***PASS
TEST 173150 AFM ALPG FAIL FLAG CHECK
TEST 173150 RESULT -----
10:25:00 --> ***PASS
TEST 173160 AFM ALPG HOLD/CONTINUE CHECK
TEST 173160 RESULT -----
10:25:01 --> ***PASS
TEST 173170 AFM ALPG BURST/CONTINUE CHECK
TEST 173170 RESULT -----
10:25:01 --> ***PASS
TEST 173200 AFM FMUX CHANNEL SENSE CHECK
TEST 173200 RESULT -----
10:25:01 --> ***PASS
TEST 173210 AFM FMUX REGISTER READ/WRITE CHECK
TEST 173210 RESULT -----
10:25:01 --> ***PASS
TEST 173220 AFM FMUX DATA READ/WRITE CHECK
TEST 173220 RESULT -----
10:25:07 --> ***PASS
TEST 173230 AFM FMUX REGISTER RESET CHECK
TEST 173230 RESULT -----
10:25:07 --> ***PASS
TEST 173250 AFM FMUX PARALLEL TEST READ/WRITE CHECK
TEST 173250 RESULT -----
10:25:07 --> ***PASS
TEST 173300 AFM FCC REGISTER READ/WRITE CHECK
TEST 173300 RESULT -----
10:25:07 --> ***PASS
TEST 173301 AFM FCC REGISTER RESET CHECK
TEST 173301 RESULT -----
10:25:07 --> ***PASS
TEST 173310 AFM FCC FBM READ/WRITE CHECK
TEST 173310 RESULT -----
10:25:09 --> ***PASS

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```
TEST 173320 AFM FCC FUNCTION CHECK
TEST 173320 RESULT -----
10:25:12 --> ***PASS
TEST 173330 AFM FCC SET/RESET CHECK
TEST 173330 RESULT -----
10:25:41 --> ***PASS
TEST 173340 AFM FCC MOVE CHECK 1
TEST 173340 RESULT -----
10:26:05 --> ***PASS
TEST 173350 AFM FCC MOVE CHECK 2
TEST 173350 RESULT -----
10:26:41 --> ***PASS
TEST 131000 FPU REGISTER READ/WRITE 1 CHECK (FPIF)
TEST 131000 RESULT -----
10:26:46 --> ***PASS
TEST 131100 FPU REGISTER READ/WRITE 2 CHECK (FP)
TEST 131100 RESULT -----
10:26:58 --> ***PASS
TEST 131150 FPU REGISTER READ/WRITE 3 CHECK (FP PARALLEL)
TEST 131150 RESULT -----
10:27:02 --> ***PASS
TEST 131200 FPU INPUT CHECK
TEST 131200 RESULT -----
10:27:05 --> ***PASS
TEST 131300 FPU FP INITIALIZE CHECK
TEST 131300 RESULT -----
10:27:08 --> ***PASS
TEST 131400 FPU LINEARITY CHECK
TEST 131400 RESULT -----
10:32:13 --> ***PASS
TEST 131500 FPU MUTUAL COMPARE CHECK
TEST 131500 RESULT -----
10:32:23 --> ***PASS
TEST 131700 FPU FORMAT CONTROL CHECK
TEST 131700 RESULT -----
10:32:49 --> ***PASS
TEST 131800 FPU DIGITAL COMPARE CHECK
TEST 131800 RESULT -----
```

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10:32:54 --> ***PASS
TEST 131900 FPU MUTUAL COMPARE CHECK-2
TEST 131900 RESULT -----
10:33:14 --> ***PASS
TEST 132000 FPU RELIABILITY CHECK (MEMORY R/W)
TEST 132000 RESULT -----
10:33:35 --> ***PASS
TEST 137000 TRG REGISTER READ/WRITE CHECK
TEST 137000 RESULT -----
10:33:37 --> ***PASS
TEST 137100 TRG INPUT CHECK
TEST 137100 RESULT -----
10:33:43 --> ***PASS
TEST 137200 TRG INITIALIZE CHECK
TEST 137200 RESULT -----
10:34:13 --> ***PASS
TEST 137300 TRG LINEARITY CHECK
TEST 137300 RESULT -----
10:34:48 --> ***PASS
TEST 137400 TRG FORMAT CONTROL CHECK
TEST 137400 RESULT -----
10:34:48 --> ***PASS
TEST 137500 TRG DIGITAL COMPARE CHECK
TEST 137500 RESULT -----
10:34:49 --> ***PASS
TEST 137600 TRG MUTUAL CHECK
TEST 137600 RESULT -----
10:34:55 --> ***PASS
TEST 137700 TRG MUTUAL CHECK-2
TEST 137700 RESULT -----
10:35:10 --> ***PASS
TEST 137800 TRG RELIABILITY CHECK (MEMORY R/W)
TEST 137800 RESULT -----
10:35:18 --> ***PASS
TEST 140000 TH1 PB FLAG READ CHECK
TEST 140000 RESULT -----
10:35:18 --> ***PASS
TEST 140020 TH1 PARALLEL MODE TH BOARD REVISION FITING CHECK

TEST 140020 RESULT -----
10:35:18 --> ***PASS
TEST 140100 TH1 STATION SELECT REGISTER RESET CHECK
TEST 140100 RESULT -----
10:35:18 --> ***PASS
TEST 140105 TH1 STATION SELECT REGISTER SYSTEM RESET CHECK
TEST 140105 RESULT -----
10:35:18 --> ***PASS
TEST 140110 TH1 PIN DOMAIN REGISTER RESET CHECK
TEST 140110 RESULT -----
10:35:18 --> ***PASS
TEST 140115 TH1 PIN DOMAIN REGISTER SYSTEM RESET CHECK
TEST 140115 RESULT -----
10:35:19 --> ***PASS
TEST 140120 TH1 CONT REGISTER RESET CHECK
TEST 140120 RESULT -----
10:35:19 --> ***PASS
TEST 140125 TH1 CONT REGISTER SYSTEM RESET CHECK
TEST 140125 RESULT -----
10:35:19 --> ***PASS
TEST 140126 TH1 DC PARA RESET CHECK
TEST 140126 RESULT -----
10:35:19 --> ***PASS
TEST 140130 TH1 RON/ROFF REGISTER RESET CHECK
TEST 140130 RESULT -----
10:35:19 --> ***PASS
TEST 140135 TH1 RON/ROFF REGISTER SYSTEM RESET CHECK
TEST 140135 RESULT -----
10:35:19 --> ***PASS
TEST 140140 TH1 DIRECT RON/ROFF REGISTER RESET CHECK
TEST 140140 RESULT -----
10:35:19 --> ***PASS
TEST 140145 TH1 DIRECT RON/ROFF REGISTER SYSTEM RESET CHECK
TEST 140145 RESULT -----
10:35:19 --> ***PASS
TEST 140150 TH1 PARALLEL CONT REGISTER RESET CHECK
TEST 140150 RESULT -----
10:35:19 --> ***PASS

TEST 140155 TH1 PARALLEL CONT REGISTER SYSTEM RESET CHECK
TEST 140155 RESULT -----
10:35:19 --> ***PASS
TEST 140200 TH1 RON/ROF REGISTER READ/WRITE CHECK
TEST 140200 RESULT -----
10:35:19 --> ***PASS
TEST 140300 TH1 PIN/CONT REGISTER READ/WRITE CHECK
TEST 140300 RESULT -----
10:35:24 --> ***PASS
TEST 140310 TH1 OFFSET&GAIN REGISTER READ/WRITE CHECK
TEST 140310 RESULT -----
10:35:32 --> ***PASS
TEST 140320 TH1 PARALLEL CONT REGISTER READ/WRITE CHECK
TEST 140320 RESULT -----
10:35:32 --> ***PASS
TEST 140500 TH1 RON/ROFF TIMER OPERATION TIME CHECK
TEST 140500 RESULT -----
10:37:16 --> ***PASS
TEST 140510 TH1 RON/ROFF TIMER OPERATION RON/ROFF SET CHECK
TEST 140510 RESULT -----
10:37:17 --> ***PASS
TEST 140520 TH1 DIRECT RON OPERATION RON/ROFF SET CHECK
TEST 140520 RESULT -----
10:37:17 --> ***PASS
TEST 140530 TH1 SETTLING BUSY FLAG OPERATION CHECK
TEST 140530 RESULT -----
10:37:17 --> ***PASS
TEST 140600 TH1 PIN GROUP SETUP OPERATION CHECK
TEST 140600 RESULT -----
10:37:21 --> ***PASS
TEST 140610 TH1 PARALLEL MODE PIN READ/WRITE CHECK
TEST 140610 RESULT -----
10:39:13 --> ***PASS
TEST 140620 TH1 PARALLEL MODE PIN GROUP SETUP OPERATION CHECK
TEST 140620 RESULT -----
10:39:14 --> ***PASS
TEST 140630 TH1 PARALLEL MODE PIN GROUP PARTIAL WRITE CHECK
TEST 140630 RESULT -----

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10:39:15 --> ***PASS
TEST 140340 TH1 DLL ERROR READ
TEST 140340 RESULT -----
10:39:15 --> ***PASS
TEST 140900 TH1 INIT TH ADJUSTMENT CHECK
TEST 140900 RESULT -----
10:40:19 --> ***PASS
TEST 140910 TH1 INIT TRG ADJUSTMENT CHECK
TEST 140910 RESULT -----
10:40:30 --> ***PASS
TEST 141100 TH1 DR OUTPUT VOLTAGE LINEARITY & RON/ROF CHECK
TEST 141100 RESULT -----
10:41:03 --> ***PASS
TEST 141110 TH1 DR OUTPUT CURRENT & IMPEDANCE CHECK
TEST 141110 RESULT -----
10:41:17 --> ***PASS
TEST 141120 TH1 DR OUTPUT CURRENT LIMIT CHECK
TEST 141120 RESULT -----
10:41:26 --> ***PASS
TEST 141200 TH1 VT LINEARITY VOLTAGE & RON/ROF CHECK
TEST 141200 RESULT -----
10:41:34 --> ***PASS
TEST 141210 TH1 VT CURRENT & RESISTANCE CHECK
TEST 141210 RESULT -----
10:41:48 --> ***PASS
TEST 141300 TH1 PL CURRENT LINEARITY VOLTAGE & RON/ROF CHECK
TEST 141300 RESULT -----
10:42:13 --> ***PASS
TEST 141310 TH1 PL THRESHOLD VOLTAGE CHECK
TEST 141310 RESULT -----
10:42:21 --> ***PASS
TEST 141400 TH1 LIMITER FAIL SENSE VOLTAGE CHECK
TEST 141400 RESULT -----
10:42:26 --> ***PASS
TEST 141500 TH1 PARALLEL MODE MASK OPERATION CHECK
TEST 141500 RESULT -----
10:42:42 --> ***PASS
TEST 142000 TH1 I/O LEAK CURRENT (NORMAL, HS MODE) CHECK

```
TEST 142000 RESULT -----
10:42:56 --> ***PASS
TEST 142100 TH1 CP REFERENCE VOLTAGE LINEARITY CHECK
TEST 142100 RESULT -----
10:43:28 --> ***PASS
TEST 142200 TH1 DCLMP VOLTAGE LINEARITY & RON/ROF CHECK
TEST 142200 RESULT -----
10:44:06 --> ***PASS
TEST 142210 TH1 DCLMP CURRENT LIMIT CHECK
TEST 142210 RESULT -----
10:44:38 --> ***PASS
TEST 143000 TH1 TRIGGER DR OUTPUT VOLTAGE LINEARITY CHECK
TEST 143000 RESULT -----
10:44:39 --> ***PASS
TEST 143100 TH1 TRIGGER CP REFERENCE VOLTAGE LINEARITY CHECK
TEST 143100 RESULT -----
10:44:40 --> ***PASS
TEST 144000 TH1 PRE DR - CP PHASE CHECK
TEST 144000 RESULT -----
10:44:52 --> ***PASS
TEST 144100 TH1 PRE I/O TIMING (NORMAL MODE) CHECK
TEST 144100 RESULT -----
10:45:01 --> ***PASS
TEST 144200 TH1 PRE DRE MINIMUM ON/OFF (NORMAL MODE) CHECK
TEST 144200 RESULT -----
10:45:14 --> ***PASS
TEST 145000 TH1 DR - CP PHASE CAL_FILE CHECK
TEST 145000 RESULT -----
10:45:25 --> ***PASS
TEST 145110 TH1 I/O TIMING (HS MODE) CAL_FILE CHECK
TEST 145110 RESULT -----
10:45:35 --> ***PASS
TEST 145120 TH1 I/O TIMING (VTT MODE) CAL_FILE CHECK
TEST 145120 RESULT -----
10:45:44 --> ***PASS
TEST 145300 TH1 DR TR/TF CHECK
TEST 145300 RESULT -----
10:45:54 --> ***PASS
```


TEST 160001 DFM REGISTER W/R CHECK 2-1
TEST 160001 RESULT -----
10:46:11 --> ***PASS
TEST 160002 DFM REGISTER RESET CHECK 2-1
TEST 160002 RESULT -----
10:46:12 --> ***PASS
TEST 160010 DFM CONTINUOUS STORE CHECK 2-1
TEST 160010 RESULT -----
10:46:14 --> ***PASS
TEST 160020 DFM FAIL STORE CHECK 2-1
TEST 160020 RESULT -----
10:46:19 --> ***PASS
TEST 160021 DFM FAIL STORE CHECK 2-2
TEST 160021 RESULT -----
10:46:22 --> ***PASS
TEST 160022 DFM FAIL STORE CHECK 2-3
TEST 160022 RESULT -----
10:46:26 --> ***PASS
TEST 160040 DFM NAVIGATION MODE CHECK 2-1
TEST 160040 RESULT -----
10:46:29 --> ***PASS
TEST 160041 DFM NAVIGATION MODE CHECK 2-2
TEST 160041 RESULT -----
10:46:31 --> ***PASS
TEST 160050 DFM HOLD/BURST CHECK 2-1
TEST 160050 RESULT -----
10:46:32 --> ***PASS
TEST 160051 DFM HOLD/BURST CHECK 2-2
TEST 160051 RESULT -----
10:46:34 --> ***PASS
TEST 160060 DFM INTERLEAVE CHECK 2-1
TEST 160060 RESULT -----
10:46:39 --> ***PASS
TEST 160070 DFM PIN_FAIL DATA BIT CHECK 2-1
TEST 160070 RESULT -----
10:46:41 --> ***PASS
TEST 160090 DFM FAIL STOP CHECK 2-1
TEST 160090 RESULT -----

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```
10:46:42 --> ***PASS
TEST 160091 DFM FAIL STOP CHECK 2-2
TEST 160091 RESULT -----
10:46:42 --> ***PASS
TEST 161010 CPEWINDOW MODE CHECK 1
TEST 161010 RESULT -----
10:46:44 --> ***PASS
TEST 161011 CPEWINDOW MODE CHECK 2
TEST 161011 RESULT -----
10:46:45 --> ***PASS
TEST 161030 CPEWINDOW COUNTER CHECK
TEST 161030 RESULT -----
10:46:45 --> ***PASS
TEST 161031 CPEWINDOW COUNTER COMPARISION BIT CHECK 1
TEST 161031 RESULT -----
10:46:50 --> ***PASS
TEST 161032 CPEWINDOW COUNTER COMPARISION BIT CHECK 2
TEST 161032 RESULT -----
10:46:56 --> ***PASS
TEST 161050 CPEWINDOW HOLD/BURST CHECK 1
TEST 161050 RESULT -----
10:46:57 --> ***PASS
TEST 161051 CPEWINDOW HOLD/BURST CHECK 2
TEST 161051 RESULT -----
10:46:57 --> ***PASS
TEST 161060 CPEWINDOW INTERLEAVE CHECK 1
TEST 161060 RESULT -----
10:47:02 --> ***PASS
TEST 162100 MATCH HMATCH FUNCTION CHECK 1
TEST 162100 RESULT -----
10:47:04 --> ***PASS
TEST 162110 MATCH LMATCH FUNCTION CHECK 1
TEST 162110 RESULT -----
10:47:05 --> ***PASS
TEST 162120 MATCH HMATCH FUNCTION CHECK 2
TEST 162120 RESULT -----
10:47:07 --> ***PASS
TEST 162130 MATCH HMATCH FUNCTION CHECK 3
```

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```
TEST 162130 RESULT -----
10:47:08 --> ***PASS
TEST 162131 MATCH HMATCH FUNCTION CHECK 4
TEST 162131 RESULT -----
10:47:09 --> ***PASS
TEST 162140 MATCH HMATCH FUNCTION CHECK 5
TEST 162140 RESULT -----
10:47:10 --> ***PASS
TEST 162141 MATCH HMATCH FUNCTION CHECK 6
TEST 162141 RESULT -----
10:47:11 --> ***PASS
TEST 162142 MATCH HMATCH FUNCTION CHECK 7
TEST 162142 RESULT -----
10:47:12 --> ***PASS
TEST 162143 MATCH HMATCH FUNCTION CHECK 8
TEST 162143 RESULT -----
10:47:13 --> ***PASS
TEST 162151 MATCH LMATCH FUNCTION CHECK 2
TEST 162151 RESULT -----
10:47:15 --> ***PASS
TEST 162160 MATCH LMATCH FUNCTION CHECK 3
TEST 162160 RESULT -----
10:47:16 --> ***PASS
TEST 162170 MATCH HMATCH FIDX BIT CHECK
TEST 162170 RESULT -----
10:47:17 --> ***PASS
TEST 162171 MATCH HMATCH BAR BIT CHECK 1
TEST 162171 RESULT -----
10:47:17 --> ***PASS
TEST 162172 MATCH HMATCH BAR BIT CHECK 2
TEST 162172 RESULT -----
10:47:18 --> ***PASS
TEST 162180 MATCH FLAGH CONNECTION CHECK
TEST 162180 RESULT -----
10:47:18 --> ***PASS
TEST 162181 MATCH FLAGL CONNECTION CHECK
TEST 162181 RESULT -----
10:47:18 --> ***PASS
```

TEST 162190 MATCH LMATCH RATE ERROR CHECK
TEST 162190 RESULT -----
10:47:19 --> ***PASS
TEST 162200 MATCH LOOP MODE CHECK
TEST 162200 RESULT -----
10:47:21 --> ***PASS
TEST 162300 MATCH HMATCH CPEW CHECK
TEST 162300 RESULT -----
10:47:22 --> ***PASS
TEST 162310 MATCH LMATCH CPEW CHECK
TEST 162310 RESULT -----
10:47:23 --> ***PASS
TEST 163010 DFM TOFL DIVIDE & OUTPUT CHECK
TEST 163010 RESULT -----
10:47:23 --> ***PASS
TEST 163020 DFM FAIL DUT REJECT CHECK
TEST 163020 RESULT -----
10:47:23 --> ***PASS
TEST 163030 DFM FAILURE MASK CHECK
TEST 163030 RESULT -----
10:47:24 --> ***PASS
TEST 163040 DFM PARALLEL FIRST FAIL MODE CHECK
TEST 163040 RESULT -----
10:47:24 --> ***PASS
TEST 163050 DFM PIN FAIL READ CHECK
TEST 163050 RESULT -----
10:47:24 --> ***PASS
TEST 163100 MATCH START & MA INPUT CHECK
TEST 163100 RESULT -----
10:47:25 --> ***PASS
TEST 163110 MATCH FLGH DIVIDE CHECK
TEST 163110 RESULT -----
10:47:26 --> ***PASS
TEST 163120 MATCH DUT REJECT CHECK 1
TEST 163120 RESULT -----
10:47:27 --> ***PASS
TEST 163130 MATCH FLGL DIVIDE CHECK
TEST 163130 RESULT -----

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```
10:47:28 --> ***PASS
TEST 163140 MATCH DUT REJECT CHECK 2
TEST 163140 RESULT -----
10:47:28 --> ***PASS
TEST 163150 MATCH RESTART MODE CHECK
TEST 163150 RESULT -----
10:47:29 --> ***PASS
TEST 163160 MATCH HMATCH CONTINUOUS CHECK
TEST 163160 RESULT -----
10:47:30 --> ***PASS
TEST 163170 MATCH LMATCH CONTINUOUS CHECK
TEST 163170 RESULT -----
10:47:31 --> ***PASS
TEST 163180 MATCH HMATCH PARALLEL PULSE TRAIN MATCH CHECK
TEST 163180 RESULT -----
10:47:31 --> ***PASS
TEST 163190 MATCH LMATCH PARALLEL PULSE TRAIN MATCH CHECK
TEST 163190 RESULT -----
10:47:33 --> ***PASS
TEST 175100 LINK PDS ALPG DATA PINDFM PATH CHECK
TEST 175100 RESULT -----
10:47:34 --> ***PASS
TEST 175110 LINK PDS ALPG DATA SELECT PINDFM CHECK
TEST 175110 RESULT -----
10:47:50 --> ***PASS
TEST 175120 LINK ALPG HOLD/CONTINUE CHECK
TEST 175120 RESULT -----
10:47:51 --> ***PASS
TEST 175130 LINK ALPG BURST/CONTINUE CHECK
TEST 175130 RESULT -----
10:47:52 --> ***PASS
TEST 175140 LINK ALPG CPE STORE CHECK
TEST 175140 RESULT -----
10:47:52 --> ***PASS
TEST 175150 LINK ALPG SINGLE BIT MODE CHECK
TEST 175150 RESULT -----
10:47:54 --> ***PASS
TEST 175200 AFM FMUX DATA SELECT CHECK 1
```

TEST 175200 RESULT -----
10:47:54 --> ***PASS
TEST 175210 AFM FMUX DATA SELECT CHECK 2
TEST 175210 RESULT -----
10:47:55 --> ***PASS
TEST 175220 AFM FMUX SINGLE BIT MODE CHECK
TEST 175220 RESULT -----
10:47:55 --> ***PASS
TEST 175230 AFM FMUX FAILURE MASK CHECK
TEST 175230 RESULT -----
10:48:06 --> ***PASS
TEST 175250 AFM ENABLE CHECK
TEST 175250 RESULT -----
10:48:07 --> ***PASS
TEST 175260 AFM ADDRESS CHECK
TEST 175260 RESULT -----
10:48:10 --> ***PASS
TEST 175270 AFM DATA CHECK
TEST 175270 RESULT -----
10:48:11 --> ***PASS
TEST 175300 AFM AFM ACTION CHECK 1
TEST 175300 RESULT -----
10:48:13 --> ***PASS
TEST 175310 AFM AFM ACTION CHECK 2
TEST 175310 RESULT -----
10:48:14 --> ***PASS
TEST 175320 AFM AFM ACTION CHECK 3
TEST 175320 RESULT -----
10:48:16 --> ***PASS
TEST 175390 AFM AFM DOUBLE MODE CHECK
TEST 175390 RESULT -----
10:48:16 --> ***PASS
TEST 175400 AFM AFM FAIL FLAG PATH CHECK
TEST 175400 RESULT -----
10:48:18 --> ***PASS
TEST 175410 AFM AFM MEMORY READ/WRITE CHECK 2
TEST 175410 RESULT -----
10:48:20 --> ***PASS

```
TEST 193128 DSPU-PG CABLE CONNECTION CHECK
TEST 193128 RESULT -----
10:48:20 --> ***PASS
TEST 180100 DSPU CM VXI CONFIGURATION REGISTER READ CHECK
TEST 180100 RESULT -----
10:48:20 --> ***PASS
TEST 180101 DSPU CM DEVICE DEPENDENT REGISTER WRITE/READ CHECK
TEST 180101 RESULT -----
10:48:20 --> ***PASS
TEST 180110 DSPU CM BOARD RESET REGISTER CHECK
TEST 180110 RESULT -----
10:48:20 --> ***PASS
TEST 180111 DSPU CM BOARD RESET FREQUENCY CHECK
TEST 180111 RESULT -----
10:48:22 --> ***PASS
TEST 180112 DSPU CM SOFT RESET REGISTER CHECK
TEST 180112 RESULT -----
10:48:22 --> ***PASS
TEST 180113 DSPU CM SOFT RESET FREQUENCY CHECK
TEST 180113 RESULT -----
10:48:24 --> ***PASS
TEST 180120 DSPU CM EXTERNAL REFERENCE FREQUENCY CHECK
TEST 180120 RESULT -----
10:48:24 --> ***PASS
TEST 180121 DSPU CM REFERENCE PLL FREQUENCY CHECK
TEST 180121 RESULT -----
10:48:25 --> ***PASS
TEST 180130 DSPU CM OUTPUT CLOCK START CHECK
TEST 180130 RESULT -----
10:48:26 --> ***PASS
TEST 180131 DSPU CM OUTPUT CLOCK STOP CHECK
TEST 180131 RESULT -----
10:48:26 --> ***PASS
TEST 180141 DSPU CM OUTPUT CLOCK FREQUENCY 01 CHECK
TEST 180141 RESULT -----
10:48:27 --> ***PASS
TEST 180142 DSPU CM OUTPUT CLOCK FREQUENCY 02 CHECK
TEST 180142 RESULT -----
```

10:48:28 --> ***PASS
TEST 180143 DSPU CM OUTPUT CLOCK FREQUENCY 03 CHECK
TEST 180143 RESULT -----
10:48:29 --> ***PASS
TEST 180144 DSPU CM OUTPUT CLOCK FREQUENCY 04 CHECK
TEST 180144 RESULT -----
10:48:30 --> ***PASS
TEST 180145 DSPU CM OUTPUT CLOCK FREQUENCY 05 CHECK
TEST 180145 RESULT -----
10:48:31 --> ***PASS
TEST 180146 DSPU CM OUTPUT CLOCK FREQUENCY 06 CHECK
TEST 180146 RESULT -----
10:48:31 --> ***PASS
TEST 180147 DSPU CM OUTPUT CLOCK FREQUENCY 07 CHECK
TEST 180147 RESULT -----
10:48:32 --> ***PASS
TEST 180148 DSPU CM OUTPUT CLOCK FREQUENCY 08 CHECK
TEST 180148 RESULT -----
10:48:34 --> ***PASS
TEST 180149 DSPU CM OUTPUT CLOCK FREQUENCY 09 CHECK
TEST 180149 RESULT -----
10:48:36 --> ***PASS
TEST 180150 DSPU CM OUTPUT CLOCK FREQUENCY 10 CHECK
TEST 180150 RESULT -----
10:48:37 --> ***PASS
TEST 180151 DSPU CM OUTPUT CLOCK FREQUENCY 11 CHECK
TEST 180151 RESULT -----
10:48:39 --> ***PASS
TEST 180152 DSPU CM OUTPUT CLOCK FREQUENCY 12 CHECK
TEST 180152 RESULT -----
10:48:41 --> ***PASS
TEST 180153 DSPU CM OUTPUT CLOCK FREQUENCY 13 CHECK
TEST 180153 RESULT -----
10:48:43 --> ***PASS
TEST 180154 DSPU CM OUTPUT CLOCK FREQUENCY 14 CHECK
TEST 180154 RESULT -----
10:48:46 --> ***PASS
TEST 180155 DSPU CM OUTPUT CLOCK FREQUENCY 15 CHECK


```
TEST 180155 RESULT -----
10:48:49 --> ***PASS
TEST 180156 DSPU CM  OUTPUT CLOCK FREQUENCY 16 CHECK
TEST 180156 RESULT -----
10:48:53 --> ***PASS
TEST 180157 DSPU CM  OUTPUT CLOCK FREQUENCY 17 CHECK
TEST 180157 RESULT -----
10:48:56 --> ***PASS
TEST 180158 DSPU CM  OUTPUT CLOCK FREQUENCY 18 CHECK
TEST 180158 RESULT -----
10:49:00 --> ***PASS
TEST 180200 DSPU EM  CONFIG REGISTER WRITE/READ CHECK
TEST 180200 RESULT -----
10:49:00 --> ***PASS
TEST 180201 DSPU EM  CONFIG REGISTER RESET CHECK
TEST 180201 RESULT -----
10:49:00 --> ***PASS
TEST 180202 DSPU EM  A16 REGISTER WRITE/READ CHECK
TEST 180202 RESULT -----
10:49:00 --> ***PASS
TEST 180203 DSPU EM  A16 REGISTER RESET CHECK
TEST 180203 RESULT -----
10:49:00 --> ***PASS
TEST 180204 DSPU EM  A32 REGISTER WRITE/READ CHECK
TEST 180204 RESULT -----
10:49:00 --> ***PASS
TEST 180205 DSPU EM  A32 REGISTER RESET CHECK
TEST 180205 RESULT -----
10:49:00 --> ***PASS
TEST 180210 DSPU EM  MEMORY ADDRESS LINE CHECK
TEST 180210 RESULT -----
10:49:00 --> ***PASS
TEST 180212 DSPU EM  MEMORY WRITE/READ CHECK
TEST 180212 RESULT -----
10:49:04 --> ***PASS
TEST 180214 DSPU EM  MEMORY CLEAR CHECK
TEST 180214 RESULT -----
10:49:09 --> ***PASS
```

```
TEST 180220 DSPU EM    CLOCK SOURCE SELECTION CHECK
TEST 180220 RESULT -----
10:49:09 --> ***PASS
TEST 180230 DSPU EM    CLOCK DIVIDE CHECK
TEST 180230 RESULT -----
10:49:09 --> ***PASS
TEST 180235 DSPU EM    CLOCK BURST CHECK
TEST 180235 RESULT -----
10:49:10 --> ***PASS
TEST 180240 DSPU EM    CLOCK SEQUENTIAL START CHECK
TEST 180240 RESULT -----
10:49:10 --> ***PASS
TEST 180250 DSPU EM    EVENT SEQUENCER STATUS CHECK
TEST 180250 RESULT -----
10:49:10 --> ***PASS
TEST 180252 DSPU EM    EVENT SEQUENCER ADDRESSING CHECK
TEST 180252 RESULT -----
10:49:41 --> ***PASS
TEST 180254 DSPU EM    EVENT SEQUENCER ADDRESS COUNTER CHECK
TEST 180254 RESULT -----
10:50:03 --> ***PASS
TEST 180260 DSPU EM    EVENT MUX CHECK
TEST 180260 RESULT -----
10:50:06 --> ***PASS
TEST 180265 DSPU EM    EVENT MODE CHECK
TEST 180265 RESULT -----
10:50:06 --> ***PASS
TEST 180270 DSPU EM    EXT/DUT CLOCK CONTACT CHECK
TEST 180270 RESULT -----
10:50:07 --> ***PASS
TEST 180275 DSPU EM    EXT/DUT EVENT CONTACT CHECK
TEST 180275 RESULT -----
10:50:09 --> ***PASS
TEST 180280 DSPU EM    EVENT SEQUENCER EXT START CHECK
TEST 180280 RESULT -----
10:50:10 --> ***PASS
TEST 180300 DSPU ACAL  CONFIGURATION CHECK
TEST 180300 RESULT -----
```

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```
10:50:10 --> ***PASS
TEST 180310 DSPU ACAL WRITE/READ CHECK
TEST 180310 RESULT -----
10:50:10 --> ***PASS
TEST 180311 DSPU ACAL BOARD RESET CHECK
TEST 180311 RESULT -----
10:50:10 --> ***PASS
TEST 180312 DSPU ACAL SOFT RESET CHECK
TEST 180312 RESULT -----
10:50:10 --> ***PASS
TEST 180320 DSPU ACAL DAC LINEARITY CHECK
TEST 180320 RESULT -----
10:50:16 --> ***PASS
TEST 180330 DSPU ACAL DAC ON/OFF CHECK
TEST 180330 RESULT -----
10:50:18 --> ***PASS
TEST 180340 DSPU ACAL RMS-DC CONVERTER CHECK
TEST 180340 RESULT -----
10:50:20 --> ***PASS
TEST 180350 DSPU ACAL EXTERNAL CLOCK CHECK
TEST 180350 RESULT -----
10:50:20 --> ***PASS
TEST 181000 DSPU AFG AFG MOUNT CHECK
TEST 181000 RESULT -----
10:50:20 --> ***PASS
TEST 181010 DSPU AFG AFLT MOUNT CHECK
TEST 181010 RESULT -----
10:50:20 --> ***PASS
TEST 181020 DSPU AFG PINGARD MOUNT CHECK
TEST 181020 RESULT -----
10:50:20 --> ***PASS
TEST 181050 DSPU AFG AFG REGISTER READ/WRITE CHECK
TEST 181050 RESULT -----
10:50:21 --> ***PASS
TEST 181060 DSPU AFG AFLT REGISTER READ/WRITE CHECK
TEST 181060 RESULT -----
10:50:21 --> ***PASS
TEST 181070 DSPU AFG PINGARD REGISTER READ/WRITE CHECK
```

TEST 181070 RESULT -----
10:50:21 --> ***PASS
TEST 181080 DSPU AFG VREF/AFG INITIALIZE CHECK
TEST 181080 RESULT -----
10:53:26 --> ***PASS
TEST 181090 DSPU AFG SAMPLING CLOCK LINE CHECK
TEST 181090 RESULT -----
10:53:28 --> ***PASS
TEST 181100 DSPU AFG PINGARD VRH DC VOLTAGE CHECK
TEST 181100 RESULT -----
10:53:29 --> ***PASS
TEST 181110 DSPU AFG PINGARD VRL DC VOLTAGE CHECK
TEST 181110 RESULT -----
10:53:30 --> ***PASS
TEST 181200 DSPU AFG AFLT DC VOLTAGE CHECK
TEST 181200 RESULT -----
10:54:01 --> ***PASS
TEST 181210 DSPU AFG PE POS DC VOLTAGE CHECK
TEST 181210 RESULT -----
10:54:50 --> ***PASS
TEST 181220 DSPU AFG PE POS BUFFER DC VOLTAGE CHECK
TEST 181220 RESULT -----
10:55:38 --> ***PASS
TEST 181230 DSPU AFG PE NEG DC VOLTAGE CHECK
TEST 181230 RESULT -----
10:56:26 --> ***PASS
TEST 181250 DSPU AFG PE POS BIAS DC VOLTAGE CHECK
TEST 181250 RESULT -----
10:56:28 --> ***PASS
TEST 181260 DSPU AFG PE POS BIAS BUFFER DC VOLTAGE CHECK
TEST 181260 RESULT -----
10:56:30 --> ***PASS
TEST 181270 DSPU AFG PE NEG BIAS DC VOLTAGE CHECK
TEST 181270 RESULT -----
10:56:32 --> ***PASS
TEST 181300 DSPU AFG AFLT AC VOLTAGE CHECK
TEST 181300 RESULT -----
10:56:37 --> ***PASS

```
TEST 181310 DSPU AFG PE POS AC VOLTAGE CHECK
TEST 181310 RESULT -----
10:56:37 --> ***PASS
TEST 181320 DSPU AFG PE POS BUFFER AC VOLTAGE CHECK
TEST 181320 RESULT -----
10:56:38 --> ***PASS
TEST 181330 DSPU AFG PE NEG AC VOLTAGE CHECK
TEST 181330 RESULT -----
10:56:38 --> ***PASS
TEST 181380 DSPU AFG PINGARD UDC LINE CONNECT CHECK
TEST 181380 RESULT -----
10:56:40 --> ***PASS
TEST 181381 DSPU AFG PINGARD UDC LINE OPEN CHECK
TEST 181381 RESULT -----
10:56:43 --> ***PASS
TEST 181400 DSPU AFG PINGARD VRH_LINE CONNECT CHECK
TEST 181400 RESULT -----
10:56:43 --> ***PASS
TEST 181410 DSPU AFG PINGARD VRL_LINE CONNECT CHECK
TEST 181410 RESULT -----
10:56:43 --> ***PASS
TEST 181440 DSPU AFG PINGARD AFG_POS_LINE CONNECT CHECK
TEST 181440 RESULT -----
10:56:43 --> ***PASS
TEST 181450 DSPU AFG PINGARD AFG_NEG_LINE CONNECT CHECK
TEST 181450 RESULT -----
10:56:44 --> ***PASS
TEST 181500 DSPU AFD AFD MOUNT CHECK
TEST 181500 RESULT -----
10:56:44 --> ***PASS
TEST 181510 DSPU AFD AFLT MOUNT CHECK
TEST 181510 RESULT -----
10:56:44 --> ***PASS
TEST 181520 DSPU AFD PINGARD MOUNT CHECK
TEST 181520 RESULT -----
10:56:44 --> ***PASS
TEST 181550 DSPU AFD AFD REGISTER READ/WRITE CHECK
TEST 181550 RESULT -----
```

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```
10:56:44 --> ***PASS
TEST 181560 DSPU AFD AFLT REGISTER READ/WRITE CHECK
TEST 181560 RESULT -----
10:56:48 --> ***PASS
TEST 181570 DSPU AFD PINGARD REGISTER READ/WRITE CHECK
TEST 181570 RESULT -----
10:56:48 --> ***PASS
TEST 181580 DSPU AFD INITIALIZE CHECK
TEST 181580 RESULT -----
10:58:06 --> ***PASS
TEST 181590 DSPU AFD PINGARD AFD_POS_LINE CONNECT CHECK
TEST 181590 RESULT -----
10:58:07 --> ***PASS
TEST 181591 DSPU AFD PINGARD AFD_NEG_LINE CONNECT CHECK
TEST 181591 RESULT -----
10:58:08 --> ***PASS
TEST 181592 DSPU AFD PINGARD VOFF_LINE CONNECT CHECK
TEST 181592 RESULT -----
10:58:08 --> ***PASS
TEST 181595 DSPU AFD PINGARD MTX_LINE (PE_INSIDE) CONNECT CHECK
TEST 181595 RESULT -----
10:58:15 --> ***PASS
TEST 181600 DSPU AFD AFD FLT DC VOLTAGE CHECK
TEST 181600 RESULT -----
10:58:18 --> ***PASS
TEST 181610 DSPU AFD AFD PE DC VOLTAGE CHECK
TEST 181610 RESULT -----
11:00:03 --> ***PASS
TEST 181620 DSPU AFD AFD PE DC BIAS VOLTAGE CHECK
TEST 181620 RESULT -----
11:00:06 --> ***PASS
TEST 181630 DSPU AFD AFD SAMPLING CLOCK LINE CHECK
TEST 181630 RESULT -----
11:00:08 --> ***PASS
TEST 181640 DSPU AFD AFLT CUT_OFF CHECK
TEST 181640 RESULT -----
11:00:12 --> ***PASS
TEST 181650 DSPU AFD INTERNAL TRIGGER (POS_SLOPE) FUNCTION
```

CHECK

TEST 181650 RESULT -----

11:00:16 --> ***PASS

TEST 181651 DSPU AFD INTERNAL TRIGGER(NEG_SLOPE) FUNCTION
CHECK

TEST 181651 RESULT -----

11:00:21 --> ***PASS

TEST 181652 DSPU AFD INTERNAL TRIGGER(POS_LEVEL) FUNCTION
CHECK

TEST 181652 RESULT -----

11:00:30 --> ***PASS

TEST 181653 DSPU AFD INTERNAL TRIGGER(NEG_LEVEL) FUNCTION
CHECK

TEST 181653 RESULT -----

11:00:40 --> ***PASS

TEST 181800 DSPU AFD AFG TO AFD ANALOG PERFORMANCE CHECK

TEST 181800 RESULT -----

11:01:08 --> ***PASS

TEST 181990 DSPU AFD PINCARD CW2 SENSE CHECK

TEST 181990 RESULT -----

11:01:08 --> ***PASS

TEST 182100 DSPU VFG1 BOARD MOUNT CHECK

TEST 182100 RESULT -----

11:01:08 --> ***PASS

TEST 182101 DSPU VFG1 REGISTER WRITE/READ CHECK

TEST 182101 RESULT -----

11:01:08 --> ***PASS

TEST 182102 DSPU VFG1 WAVE MEMORY CHECK

TEST 182102 RESULT -----

11:01:15 --> ***PASS

TEST 182103 DSPU VFG1 SEGMENT LIST MEMORY CHECK

TEST 182103 RESULT -----

11:01:17 --> ***PASS

TEST 182104 DSPU VFG1 DAC OUTPUT PRECHECK

TEST 182104 RESULT -----

11:01:18 --> ***PASS

TEST 182105 DSPU VFG1 EM LINE CHECK

TEST 182105 RESULT -----

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11:01:21 --> ***PASS
TEST 182106 DSPU VFG1 SEGMENT SEQUENCE CHECK
TEST 182106 RESULT -----
11:01:23 --> ***PASS
TEST 182107 DSPU VFG1 PE MOUNT CHECK
TEST 182107 RESULT -----
11:01:23 --> ***PASS
TEST 182108 DSPU VFG1 PE REGISTER WRITE/READ CHECK
TEST 182108 RESULT -----
11:01:23 --> ***PASS
TEST 182109 DSPU VFG1 PE CAL LINE CHECK
TEST 182109 RESULT -----
11:01:23 --> ***PASS
TEST 182110 DSPU VFG1 PE ATT CHECK
TEST 182110 RESULT -----
11:01:25 --> ***PASS
TEST 182111 DSPU VFG1 PE UDC LINE CHECK
TEST 182111 RESULT -----
11:01:25 --> ***PASS
TEST 182112 DSPU VFG1 PE OUTPUT IMPEDANCE CHECK
TEST 182112 RESULT -----
11:01:25 --> ***PASS
TEST 182113 DSPU VFG1 INITIALIZE CHECK
TEST 182113 RESULT -----
11:01:30 --> ***PASS
TEST 182114 DSPU VFG1 DC CHECK
TEST 182114 RESULT -----
11:01:35 --> ***PASS
TEST 182115 DSPU VFG1 PE DC CHECK
TEST 182115 RESULT -----
11:01:53 --> ***PASS
TEST 182116 DSPU VFG1 AC CHECK
TEST 182116 RESULT -----
11:01:55 --> ***PASS
TEST 182117 DSPU VFG1 PE AC COUPLING CHECK
TEST 182117 RESULT -----
11:01:56 --> ***PASS
TEST 182118 DSPU VFG1 PE LIMITER FAIL CHECK

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```
TEST 182118 RESULT -----
11:01:56 --> ***PASS
TEST 182119 DSPU VFG1 PE CONTACT CHECK
TEST 182119 RESULT -----
11:01:56 --> ***PASS
TEST 182200 DSPU VFG2 BOARD MOUNT CHECK
TEST 182200 RESULT -----
11:01:56 --> ***PASS
TEST 182201 DSPU VFG2 REGISTER WRITE/READ CHECK
TEST 182201 RESULT -----
11:01:56 --> ***PASS
TEST 182202 DSPU VFG2 WAVE MEMORY CHECK
TEST 182202 RESULT -----
11:02:03 --> ***PASS
TEST 182203 DSPU VFG2 SEGMENT LIST MEMORY CHECK
TEST 182203 RESULT -----
11:02:05 --> ***PASS
TEST 182204 DSPU VFG2 DAC OUTPUT PRECHECK
TEST 182204 RESULT -----
11:02:06 --> ***PASS
TEST 182205 DSPU VFG2 EM LINE CHECK
TEST 182205 RESULT -----
11:02:09 --> ***PASS
TEST 182206 DSPU VFG2 SEGMENT SEQUENCE CHECK
TEST 182206 RESULT -----
11:02:10 --> ***PASS
TEST 182207 DSPU VFG2 PE MOUNT CHECK
TEST 182207 RESULT -----
11:02:10 --> ***PASS
TEST 182208 DSPU VFG2 PE REGISTER WRITE/READ CHECK
TEST 182208 RESULT -----
11:02:10 --> ***PASS
TEST 182209 DSPU VFG2 PE CAL LINE CHECK
TEST 182209 RESULT -----
11:02:11 --> ***PASS
TEST 182210 DSPU VFG2 PE ATT CHECK
TEST 182210 RESULT -----
11:02:13 --> ***PASS
```

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```
TEST 182211 DSPU VFG2 PE UDC LINE CHECK
TEST 182211 RESULT -----
11:02:13 --> ***PASS
TEST 182212 DSPU VFG2 PE OUTPUT IMPEDANCE CHECK
TEST 182212 RESULT -----
11:02:13 --> ***PASS
TEST 182213 DSPU VFG2 INITIALIZE CHECK
TEST 182213 RESULT -----
11:02:17 --> ***PASS
TEST 182214 DSPU VFG2 DC CHECK
TEST 182214 RESULT -----
11:02:23 --> ***PASS
TEST 182215 DSPU VFG2 PE DC CHECK
TEST 182215 RESULT -----
11:02:41 --> ***PASS
TEST 182216 DSPU VFG2 AC CHECK
TEST 182216 RESULT -----
11:02:43 --> ***PASS
TEST 182217 DSPU VFG2 PE AC COUPLING CHECK
TEST 182217 RESULT -----
11:02:43 --> ***PASS
TEST 182218 DSPU VFG2 PE LIMITTER FAIL CHECK
TEST 182218 RESULT -----
11:02:44 --> ***PASS
TEST 182219 DSPU VFG2 PE CONTACT CHECK
TEST 182219 RESULT -----
11:02:44 --> ***PASS
TEST 182600 DSPU VFD1 BOARD MOUNT CHECK
TEST 182600 RESULT -----
11:02:45 --> ***PASS
TEST 182601 DSPU VFD1 REGISTER WRITE/READ CHECK
TEST 182601 RESULT -----
11:02:45 --> ***PASS
TEST 182602 DSPU VFD1 WAVE MEMORY CHECK
TEST 182602 RESULT -----
11:02:47 --> ***PASS
TEST 182603 DSPU VFD1 INPUT IMPEDANCE CHECK
TEST 182603 RESULT -----
```

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```
11:02:47 --> ***PASS
TEST 182604 DSPU VFD1 LPF CHECK
TEST 182604 RESULT -----
11:02:48 --> ***PASS
TEST 182605 DSPU VFD1 DECIMATION CHECK
TEST 182605 RESULT -----
11:02:49 --> ***PASS
TEST 182606 DSPU VFD1 INTERNAL TRIGGER CHECK
TEST 182606 RESULT -----
11:03:06 --> ***PASS
TEST 182607 DSPU VFD1 OVER RANGE CHECK
TEST 182607 RESULT -----
11:03:07 --> ***PASS
TEST 182608 DSPU VFD1 EM LINE CHECK
TEST 182608 RESULT -----
11:03:07 --> ***PASS
TEST 182609 DSPU VFD1 PE BOARD MOUNT CHECK
TEST 182609 RESULT -----
11:03:07 --> ***PASS
TEST 182610 DSPU VFD1 PE REGISTER WRITE/READ CHECK
TEST 182610 RESULT -----
11:03:07 --> ***PASS
TEST 182611 DSPU VFD1 INITIALIZE CHECK
TEST 182611 RESULT -----
11:03:44 --> ***PASS
TEST 182612 DSPU VFD1 DC GUARANTEE CHECK
TEST 182612 RESULT -----
11:04:33 --> ***PASS
TEST 182613 DSPU VFD1 PE VCOMMON CHECK
TEST 182613 RESULT -----
11:04:34 --> ***PASS
TEST 182614 DSPU VFD1 DC LINEARITY CHECK
TEST 182614 RESULT -----
11:04:51 --> ***PASS
TEST 182615 DSPU VFD1 NOISE CHECK
TEST 182615 RESULT -----
11:04:51 --> ***PASS
TEST 182616 DSPU VFD1 PE CMRR CHECK
```

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TEST 182616 RESULT -----
11:04:51 --> ***PASS
TEST 182617 DSPU VFD1 PE UDC LINE CHECK
TEST 182617 RESULT -----
11:04:52 --> ***PASS
TEST 182618 DSPU VFD1 PE AC COUPLING CHECK
TEST 182618 RESULT -----
11:04:52 --> ***PASS
TEST 182619 DSPU VFD1 PE INPUT IMPEDANCE CHECK
TEST 182619 RESULT -----
11:04:52 --> ***PASS
TEST 182620 DSPU VFD1 PE LIMITER FAIL CHECK
TEST 182620 RESULT -----
11:04:53 --> ***PASS
TEST 182621 DSPU VFD1 PE CONTACT CHECK
TEST 182621 RESULT -----
11:04:53 --> ***PASS
TEST 182622 DSPU VFD1 EXTERNAL LOGIC SIGNAL CHECK
TEST 182622 RESULT -----
11:04:54 --> ***PASS
TEST 182700 DSPU VFD2 BOARD MOUNT CHECK
TEST 182700 RESULT -----
11:04:54 --> ***PASS
TEST 182701 DSPU VFD2 REGISTER WRITE/READ CHECK
TEST 182701 RESULT -----
11:04:54 --> ***PASS
TEST 182702 DSPU VFD2 WAVE MEMORY CHECK
TEST 182702 RESULT -----
11:04:56 --> ***PASS
TEST 182703 DSPU VFD2 INPUT IMPEDANCE CHECK
TEST 182703 RESULT -----
11:04:56 --> ***PASS
TEST 182704 DSPU VFD2 LPF CHECK
TEST 182704 RESULT -----
11:04:56 --> ***PASS
TEST 182705 DSPU VFD2 DECIMATION CHECK
TEST 182705 RESULT -----
11:04:57 --> ***PASS

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```
TEST 182706 DSPU VFD2 INTERNAL TRIGGER CHECK
TEST 182706 RESULT -----
11:05:15 --> ***PASS
TEST 182707 DSPU VFD2 OVER RANGE CHECK
TEST 182707 RESULT -----
11:05:15 --> ***PASS
TEST 182708 DSPU VFD2 EM LINE CHECK
TEST 182708 RESULT -----
11:05:16 --> ***PASS
TEST 182709 DSPU VFD2 PE BOARD MOUNT CHECK
TEST 182709 RESULT -----
11:05:16 --> ***PASS
TEST 182710 DSPU VFD2 PE REGISTER WRITE/READ CHECK
TEST 182710 RESULT -----
11:05:16 --> ***PASS
TEST 182711 DSPU VFD2 INITIALIZE CHECK
TEST 182711 RESULT -----
11:05:53 --> ***PASS
TEST 182712 DSPU VFD2 DC GUARANTEE CHECK
TEST 182712 RESULT -----
11:06:42 --> ***PASS
TEST 182713 DSPU VFD2 PE VCOMMON CHECK
TEST 182713 RESULT -----
11:06:42 --> ***PASS
TEST 182714 DSPU VFD2 DC LINEARITY CHECK
TEST 182714 RESULT -----
11:06:59 --> ***PASS
TEST 182715 DSPU VFD2 NOISE CHECK
TEST 182715 RESULT -----
11:07:00 --> ***PASS
TEST 182716 DSPU VFD2 PE CMRR CHECK
TEST 182716 RESULT -----
11:07:00 --> ***PASS
TEST 182717 DSPU VFD2 PE UDC LINE CHECK
TEST 182717 RESULT -----
11:07:00 --> ***PASS
TEST 182718 DSPU VFD2 PE AC COUPLING CHECK
TEST 182718 RESULT -----
```

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11:07:01 --> ***PASS
TEST 182719 DSPU VFD2 PE INPUT IMPEDANCE CHECK
TEST 182719 RESULT -----
11:07:01 --> ***PASS
TEST 182720 DSPU VFD2 PE LIMITTER FAIL CHECK
TEST 182720 RESULT -----
11:07:02 --> ***PASS
TEST 182721 DSPU VFD2 PE CONTACT CHECK
TEST 182721 RESULT -----
11:07:02 --> ***PASS
TEST 182722 DSPU VFD2 EXTERNAL LOGIC SIGNAL CHECK
TEST 182722 RESULT -----
11:07:02 --> ***PASS
TEST 183000 DSPU DCAP MOUNT CHECK
TEST 183000 RESULT -----
11:07:03 --> ***PASS
TEST 183002 DSPU DCAP READ/WRITE(A16) CHECK
TEST 183002 RESULT -----
11:07:04 --> ***PASS
TEST 183004 DSPU DCAP RESET(A16) CHECK
TEST 183004 RESULT -----
11:07:04 --> ***PASS
TEST 183008 DSPU DCAP READ/WRITE(A24) CHECK
TEST 183008 RESULT -----
11:07:04 --> ***PASS
TEST 183010 DSPU DCAP RESET(A24) CHECK
TEST 183010 RESULT -----
11:07:04 --> ***PASS
TEST 183012 DSPU DCAP MEMORY CHECK
TEST 183012 RESULT -----
11:07:27 --> ***PASS
TEST 183020 DSPU DCAP INPUT CHECK
TEST 183020 RESULT -----
11:07:42 --> ***PASS
TEST 183025 DSPU DCAP ENABLE CHECK
TEST 183025 RESULT -----
11:07:46 --> ***PASS
TEST 183030 DSPU DCAP SERIAL1 CHECK

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```
TEST 183030 RESULT -----
11:08:11 --> ***PASS
TEST 183035 DSPU DCAP SERIAL2 CHECK
TEST 183035 RESULT -----
11:08:30 --> ***PASS
TEST 183040 DSPU DCAP COUNTER CHECK
TEST 183040 RESULT -----
11:08:44 --> ***PASS
TEST 183045 DSPU DCAP PARALLEL1 CHECK
TEST 183045 RESULT -----
11:08:59 --> ***PASS
TEST 183300 THU AFE1 REGISTER WRITE/READ CHECK
TEST 183300 RESULT -----
11:08:59 --> ***PASS
TEST 183301 THU AFE1 DC/AC RELAY CHECK
TEST 183301 RESULT -----
11:09:01 --> ***PASS
TEST 183302 THU AFE1 DIFFERENTIAL INPUT LPF CHECK
TEST 183302 RESULT -----
11:09:03 --> ***PASS
TEST 183303 THU AFE1 R/L TO FLAT/NOTCH SELECT CHECK
TEST 183303 RESULT -----
11:09:07 --> ***PASS
TEST 183304 THU AFE1 GAIN CHECK
TEST 183304 RESULT -----
11:09:08 --> ***PASS
TEST 183305 THU AFE1 FREQUENCY CHECK
TEST 183305 RESULT -----
11:09:12 --> ***PASS
TEST 183306 THU AFE1 OUTPUT 20KHZ LPF CHECK
TEST 183306 RESULT -----
11:09:14 --> ***PASS
TEST 183307 THU AFE1 OUTPUT OFFSET VOLTAGE CHECK
TEST 183307 RESULT -----
11:09:16 --> ***PASS
TEST 183308 THU AFE1 INPUT RELAY CHECK
TEST 183308 RESULT -----
11:09:19 --> ***PASS
```

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TEST 183309 THU AFE1 OUTPUT RELAY CHECK
TEST 183309 RESULT -----
11:09:19 --> ***PASS
TEST 183310 THU AFE1 CONTACT CHECK
TEST 183310 RESULT -----
11:09:20 --> ***PASS
TEST 183320 THU AFE2 REGISTER WRITE/READ CHECK
TEST 183320 RESULT -----
11:09:20 --> ***PASS
TEST 183321 THU AFE2 DC/AC RELAY CHECK
TEST 183321 RESULT -----
11:09:22 --> ***PASS
TEST 183322 THU AFE2 DIFFERENTIAL INPUT LPF CHECK
TEST 183322 RESULT -----
11:09:24 --> ***PASS
TEST 183323 THU AFE2 R/L TO FLAT/NOTCH SELECT CHECK
TEST 183323 RESULT -----
11:09:28 --> ***PASS
TEST 183324 THU AFE2 GAIN CHECK
TEST 183324 RESULT -----
11:09:29 --> ***PASS
TEST 183325 THU AFE2 FREQUENCY CHECK
TEST 183325 RESULT -----
11:09:33 --> ***PASS
TEST 183326 THU AFE2 OUTPUT 20KHZ LPF CHECK
TEST 183326 RESULT -----
11:09:35 --> ***PASS
TEST 183327 THU AFE2 OUTPUT OFFSET VOLTAGE CHECK
TEST 183327 RESULT -----
11:09:37 --> ***PASS
TEST 183328 THU AFE2 INPUT RELAY CHECK
TEST 183328 RESULT -----
11:09:40 --> ***PASS
TEST 183329 THU AFE2 OUTPUT RELAY CHECK
TEST 183329 RESULT -----
11:09:40 --> ***PASS
TEST 183330 THU AFE2 CONTACT CHECK
TEST 183330 RESULT -----

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```
11:09:41 --> ***PASS
TEST 173200 AFM FMUX CHANNEL SENSE CHECK
TEST 173200 RESULT -----
11:09:41 --> ***PASS
TEST 173210 AFM FMUX REGISTER READ/WRITE CHECK
TEST 173210 RESULT -----
11:09:41 --> ***PASS
TEST 173220 AFM FMUX DATA READ/WRITE CHECK
TEST 173220 RESULT -----
11:09:47 --> ***PASS
TEST 173230 AFM FMUX REGISTER RESET CHECK
TEST 173230 RESULT -----
11:09:47 --> ***PASS
DIAGNOSIS END AT 2017/11/15 11:09:47
SYSTEM RESULT
.....>> ***PASS
```

```
Start the auto saving initialization at 2017/11/15 11:09:47
DPU INIT DATA SAVE succeeded at 11:09:49
RATE INIT DATA SAVE succeeded at 11:09:49
DIST INIT DATA SAVE succeeded at 11:09:50
SQPG INIT DATA SAVE succeeded at 11:09:50
FPU INIT DATA SAVE succeeded at 11:09:51
TRIG INIT DATA SAVE succeeded at 11:09:51
THU INIT DATA SAVE succeeded at 11:09:51
AFG INIT DATA SAVE succeeded at 11:09:51
AFD INIT DATA SAVE succeeded at 11:09:51
VFG INIT DATA SAVE succeeded at 11:09:51
VFD INIT DATA SAVE succeeded at 11:09:51
T6671 auto saving initialization is finished at 2017/11/15
11:09:51
Initialization data was successfully saved.
Initializing vector. Please wait.....
.....Vector Initialization complete.
```