

SYSTEM CONFIGURATION GENERATE

CONFIGURATION OF TEST HEAD

NUMBER OF TEST HEAD [1,2] .....> 2

CONFIGURATION OF TEST HEAD 1

(0:NOT EXIST 1:NORMAL BOARD 2:HSC BOARD)

1 1 1 1 1 1 1 2 2 2 2  
3 3 4 5 9 0 0 1 6 6 7 7 2 3 3 4  
3 9 5 1 7 3 9 5 1 7 3 9 5 1 7 3  
A A A A A A A A A A A A A A A A

CHILD A1 1 1 1 1 1 1 1 1 0 0 0 0 0 0 0 0  
CHILD A3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0

CONFIGURATION OF TEST HEAD 2

(0:NOT EXIST 1:NORMAL BOARD 2:HSC BOARD)

1 1 1 1 1 1 1 2 2 2 2  
3 3 4 5 9 0 0 1 6 6 7 7 2 3 3 4  
3 9 5 1 7 3 9 5 1 7 3 9 5 1 7 3  
A A A A A A A A A A A A A A A A

CHILD A1 1 1 1 1 1 1 1 1 0 0 0 0 0 0 0 0  
CHILD A3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0

CONFIGURATION OF DPU

TEST HEAD 1 DC CONFIGURATION [1- 64] .....> 1-32  
10V PPS CONFIGURATION [1-320] .....> 1-128,257-288

TEST HEAD 2 DC CONFIGURATION [1- 64] .....> 1-32  
10V PPS CONFIGURATION [1-320] .....> 1-128,257-288

AC FREQUENCY (Hz) [50,60] .....> 60

CONFIGURATION OF ALPG

SUBALPG OPTION EXIST [Y,N] .....> YES

CONFIGURATION OF PDS

SUFFIX OF PDSDR [1:NONE,2:X02] .....> 2

CONFIGURATION OF DBM

DBM OPTION EXIST [Y,N] .....> YES

SIZE OF DBM BOARD [1:2M,2:4M] .....> 1

CONFIGURATION OF FM

NUMBER OF FM BOARD [0,2,4,8] .....> 8

SIZE OF FM BOARD [1:2G,2:4G] .....> 1

FM ADDRESS DELAY [1:0-7,2:0-15] .....> 1

CONFIGURATION OF SC

DQS ANALIZER OPTION EXIST [Y,N] .....> YES

END SAVE