```
Successfully removed tdl files
Successfully started file /tmp/advantest/_default/hardware.tdl
T6672 system initialization start at 2022/04/06 19:46:01
 ----- SYSTEM CONFIGURATION INFORMATION ----------------------------------------
--
 PIN CONFIGURATION
  MAINFRAME            :   1 -  512
  TH1    [CTD       ] :   1 -  512
 DPU CONFIGURATION
  DPS CH      [TH1]  :  1 - 32
  UDC CH      [TH1]  :  1 -  4
  MDC CH      [TH1]  :  1 - 32
  DPS HS MODE        : OFF
  DPS PSRR           : OFF
 LINE FREQUENCY       : 50Hz
 DMM MODEL            : R6581T
 SQPG CONFIGURATION
  MAX VGC            : 4MW
  MAX CTB            : 64MW
  MAX TTB            : 16MW
 OPTIONS CONFIGURATIONS : HRS OFF  SCPG ON   ALPG ON   MIXED ON  MIXED2 OFF
  OPTION PG
   MAX SCPG           : 4Gbit
   MAX ALPG           : 1KW
   MAX PM             : 4MW
   PDS                :   1 -  512
   MDS        [TH1]  :   1 -  512
   FMUX       [TH1]  :   1 -  512
  MIXED OPTION [TH1]      : BASICS[CAL EM CM1] DCAP
                         : AFG    1 2 3 4
                         : AFD    1 2 3 4
                         : VFG    1 2
                         : VFD    1 2
 HI-FIX TYPE   [TH1]   : 512B[512+Mixed+VFD/VFG+ASPE]
 -------------------------------------------------------------------------------
---
```

```
DPU      STN1 initialization start     at 19:46:02
      30 31 100KR  80uA  0.0000e+00     8V -1.9528e-08  0.0000e+00 -1.9528e-
08  1.7749e-08    0 ___AA_
      40 31  10KR 800uA  0.0000e+00     8V -1.9363e-07  0.0000e+00 -1.9363e-
07  1.7751e-07    0 ___AA_
      50 31   1KR   8mA  0.0000e+00     8V -1.9472e-06  0.0000e+00 -1.9472e-
06  1.7749e-06    0 ___AA_
DPU      STN1 initialization failed. : 0X1
SQPG         initialization start    at 19:52:40
SQPG         successfully initialized.
FPU          initialization start    at 19:52:41
FPU          successfully initialized.
TRIGFP       initialization start    at 19:53:21
TRIGFP       successfully initialized.
THU      STN1 initialization start    at 19:53:59
INIT VIH OFFSET    PIN=  173 REG= 0X093E LIMIT=   0.014 ,  -0.014 MEAS=
0.020 *** FAIL
INIT VIL OFFSET    PIN=  173 REG= 0X0930 LIMIT=   0.014 ,  -0.014 MEAS=
0.015 *** FAIL
THU      STN1 initialization failed. : 0X1
TRIGTH   STN1 initialization start    at 19:57:25
TRIGTH   STN1 successfully initialized.
TRIGFPTH STN1 initialization start    at 19:57:28
TRIGFPTH STN1 successfully initialized.
AFG      STN1 initialization start    at 19:58:06
AFG      STN1 successfully initialized.
AFD      STN1 initialization start    at 20:01:15
AFD      STN1 successfully initialized.
VFG      STN1 initialization start    at 20:02:35
VFG      STN1 successfully initialized.
VFD      STN1 initialization start    at 20:02:44
VFD      STN1 successfully initialized.
T6672 system initialization finish at 2022/04/06 20:12:04
  -----> FAIL ***
```