

A563テスト構成

```
No. 1
#
# Confsim file created on: 06/29/:0 11:44:06
#
# A563 tester sfdcs1_t
#
#
# Up to 8 Universal Backplane/Synch Power Subsystem
# cages are allowed
#
# For the Synch Power Subsystem:
# Slot   Type       Name           Instr1 #   Instr2 # Ammeter #
#
#       Instr1 #   - instrument connected to the first two matrix lines
#       Instr2 #   - instrument connected to the last two matrix lines
#       Ammeter # #NAME?
#       to AVOID errors, put NO 0 if no instrument is connected.
#
#
UB_SPS_CAGE 1
# Slot   Type       Serial #   Num   Name
1  879-802-01 0x00f3fd8 0   # UB_SPS_802
2  517-301-01 0x9411078 0   # UB_MATRIX
3  517-301-01 0x0210208 0   # UB_MATRIX
4  517-301-01 0x9409029 0   # UB_MATRIX
5  517-301-01 0x01bf6fe 0   # UB_MATRIX
6  517-301-01 0x9409028 0   # UB_MATRIX
7  517-301-01 0x010d85e 0   # UB_MATRIX
8  517-301-01 0x01bf468 0   # UB_MATRIX
9  517-301-01 0x016b6d1 0   # UB_MATRIX
22 517-300-00 0x9408066 0   # UB_TJ300
END

#
# M620 subsystem
#
M620_SUBSYSTEM
#Slot   Type
2       PGU
3       PGU_TIMING_REFERENCE
4       PGU_FORMATTER
5       PGU_FORMATTER
6       PGU_FORMATTER
7       PGU_FORMATTER
9       ASU
13      VMCU
14      VMCU
15      VMCU
16      VMCU
17      VMCU
18      VMCU
19      VMCU
20      VMCU
21      CFU RFU
22      CMU
END

#
# DC Subsystem -
#
#   SRC <NUM>           *[1 - 4, 6 - 8]
#   (sources 1-4 are MATRIX sources 1-4
#   sources 6-8 are DUT sources 1-3)
#   HCU <NUM>           *[1 - 4]
#   REF HCU <NUM>       *[1 - 4]
#   HVSRC <NUM>         *[1 - 4]
#   PWSRC <NUM>         [1 - 4]
#
# ** These instruments share the same seven-slot cage -- only one
# instrument is allowed per slot.
#
DC_SUBSYSTEM
SRC 1
SRC 2
SRC 3
SRC 4
END

#
# This keyword indicates the presence of Standard Linear bulkhead
# test stations.
#
LINEAR
END
```