

# **Trust Technology Corporation**

(E-mail) business@trust-t.com, (URL) http://www.trust-t.com/, (TEL) +81-3-3344-5540

TTC ID

Maker TERADYNE

Model J750

Description Tester

Vintage

QTY 1

[NOTES]

There are more tools listed on the following URL.

We hope you will also view it.

(Used Tools Information)

http://www.trust-t.com/ttcen/equipment/

# CurrentConfig.

#slot[.subslot]	Type	idprom (type serial rev company)
-1	sli	239-624-00 c01d63d 9846-A 5445
0	channel	239-026-03 500a534 0951-E 5445
1	channel	$239 \text{-} 026 \text{-} 03\ 5009 \text{e}7 \text{c}\ 1541 \text{-} B\ 5445$
2	channel	239-026-05 c00ba21 0626-5 5445
3	channel	$239\text{-}026\text{-}03\ 802\text{e}71\text{c}\ 0621\text{-}B\ 5445$
4	channel	$239 \text{-} 026 \text{-} 03\ 80170 \text{c} 3\ 0951 \text{-} B\ 5445$
5	channel	$239 \hbox{-} 026 \hbox{-} 05 \hbox{ c} 00 \hbox{b} 7 \hbox{fc} \ 0746 \hbox{-} 5 \ 5445$
6	channel	239-026-31 c0eb261 1042-5 5445
7	channel	239-026-05 c00523d 0746-5 5445
17	cto	239-029-02 c398747 1332-D 5445
18	cub	239-020-09 c017d6d 1045-6 5445
21	dps	239-016-04 5009cd5 0507-D 5445
22	dps	239-016-03 800e4c6 1543-D 5445
23	dps	239-016-04 500b23a 0645-D 5445
24	dps	239-016-04 5009dd2 0509-D 5445

# Quick\_Module\_Calibration\_Performance\_PASS

# - PCIT CARD INFORMATION:

Part Number: 939-360-00

Serial Number: 0 Revision Date: A0422

%JOB\_END - \*\*\*\*PASSED\*\*\*\* PCIT Quick Check of slot 0 at 5:12:15 PM

- Starting DIB Power Tests
- Completed DIB Power Tests
- Starting System Fan Checks
- Completed System Fan Checks
- Starting PG\_History\_Ram
- Completed PG\_History\_Ram
- Starting PG\_Svm\_Ram
- Completed PG\_Svm\_Ram
- Starting PG\_Scramble\_Ram, ADSS
- Completed PG\_Scramble\_Ram
- Starting PG Scramble Ram, Tset
- Completed PG\_Scramble\_Ram
- Skipping PG\_LVM\_BIST\_Ram
- Beginning Qck\_Register Test
- Completed Qck\_Register Test
- Starting CalCub\_TG\_Register Tests
- LRS Off
- LRS On
- Completed CalCub\_TG\_Register Tests
- Started IdProm Test

- Completed IdProm Test
- Beginning Force Voltage Test
- Completed Force Voltage Test
- Beginning TestRefToDac
- Completed TestRefToDac
- Starting the CalCubSLITest
- Completed the CalCubSLITest

%JOB\_END - \*\*\*\*PASSED\*\*\*\* CUB Quick Check of slot 18 (C017D6D) at 5:12:30 PM

%JOB\_START - Beginning Channel\_Board Quick Check test on slot 0 at 5:12:36 PM on 2/29/2020 Workbook Rev V7.30.12\_0835\_MOUT IG-XL Version: 3.40.13 DIB # 305253E Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting PG\_History\_Ram
- Completed PG\_History\_Ram
- Starting PG\_Svm\_Ram
- Completed PG\_Svm\_Ram
- Starting PG\_Scramble\_Ram, ADSS
- Completed PG\_Scramble\_Ram
- Starting PG\_Scramble\_Ram, Tset
- Completed PG\_Scramble\_Ram
- Starting PG\_LVM\_BIST\_Ram (up to 32 sec)
- Completed PG\_LVM\_BIST\_Ram in 21.6 sec
- LRS Off
- Starting TG Register Tests
- Completed TG Register Tests
- LRS On
- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG\_Period\_Ram
- Completed TG\_Period\_Ram
- Starting TG\_Period\_Map\_Ram
- Completed TG\_Period\_Map\_Ram

- Starting TG\_LVM\_BIST\_Ram (up to 32 sec)
- Completed TG\_LVM\_BIST\_Ram in 10.8 sec
- Starting TG\_History\_Ram
- Completed TG\_History\_Ram
- Starting TG\_SVM\_Ram
- Completed TG\_SVM\_Ram
- Starting TG\_ADSS\_Ram
- Completed  $TG\_ADSS\_Ram$
- Starting TG\_KeepAlive\_Ram
- Completed TG\_KeepAlive\_Ram
- Starting TG\_Tset\_LkDwn\_Ram
- Completed TG\_Tset\_LkDwn\_Ram
- Starting TG\_Edge\_Ram
- Completed TG\_Edge\_Ram
- Starting TG\_Format\_Ram
- Completed TG\_Format\_Ram
- Starting TG\_FormatLkDwn\_Ram
- Completed TG\_FormatLkDwn\_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Channel\_Board Quick Check of slot 0 (500A534) at 5:13:25 PM

%JOB\_START - Beginning Channel\_Board Quick Check test on slot 1 at 5:13:31 PM on 2/29/2020 Workbook Rev V7.30.12\_0835\_MOUT IG-XL Version: 3.40.13 DIB # 305253E Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting PG\_History\_Ram
- Completed PG\_History\_Ram
- Starting PG\_Svm\_Ram
- Completed PG\_Svm\_Ram

- Starting PG\_Scramble\_Ram, ADSS
- Completed PG\_Scramble\_Ram
- Starting PG\_Scramble\_Ram, Tset
- Completed PG\_Scramble\_Ram
- Starting PG\_LVM\_BIST\_Ram (up to 32 sec)
- Completed PG\_LVM\_BIST\_Ram in 21.6 sec

# - LRS Off

- Starting TG Register Tests
- Completed TG Register Tests

#### - LRS On

- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG\_Period\_Ram
- Completed TG\_Period\_Ram
- Starting TG\_Period\_Map\_Ram
- Completed TG\_Period\_Map\_Ram
- Starting TG\_LVM\_BIST\_Ram (up to 32 sec)
- Completed TG\_LVM\_BIST\_Ram in 10.8 sec
- Starting TG\_History\_Ram
- Completed TG\_History\_Ram
- Starting TG\_SVM\_Ram
- Completed TG\_SVM\_Ram
- Starting TG\_ADSS\_Ram
- Completed TG\_ADSS\_Ram
- Starting TG\_KeepAlive\_Ram
- Completed TG\_KeepAlive\_Ram
- Starting TG Tset LkDwn Ram
- Completed TG\_Tset\_LkDwn\_Ram
- Starting TG\_Edge\_Ram
- Completed TG\_Edge\_Ram
- Starting TG\_Format\_Ram
- Completed TG Format Ram
- Starting TG\_FormatLkDwn\_Ram
- Completed TG\_FormatLkDwn\_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers

- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Channel\_Board Quick Check of slot 1 (5009E7C) at 5:14:21 PM

 $\% JOB\_START$  - Beginning Channel\_Board Quick Check test on slot 2 at 5:14:26 PM on 2/29/2020 Workbook Rev V7.30.12\_0835\_MOUT  $\,$  IG-XL Version: 3.40.13  $\,$  DIB # 305253E Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting PG\_History\_Ram
- Completed PG\_History\_Ram
- Starting PG\_Svm\_Ram
- Completed PG\_Svm\_Ram
- Starting PG\_Scramble\_Ram, ADSS
- Completed PG\_Scramble\_Ram
- Starting PG\_Scramble\_Ram, Tset
- Completed PG\_Scramble\_Ram
- Starting PG\_LVM\_BIST\_Ram (up to 32 sec)
- Completed PG\_LVM\_BIST\_Ram in 21.6 sec
- LRS Off
- Starting TG Register Tests
- Completed TG Register Tests
- LRS On
- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG\_Period\_Ram
- Completed TG\_Period\_Ram
- Starting TG\_Period\_Map\_Ram
- Completed TG\_Period\_Map\_Ram
- Starting TG\_LVM\_BIST\_Ram (up to 32 sec)
- Completed TG\_LVM\_BIST\_Ram in 10.8 sec

- Starting TG\_History\_Ram
- Completed TG\_History\_Ram
- Starting TG\_SVM\_Ram
- Completed TG\_SVM\_Ram
- Starting TG\_ADSS\_Ram
- Completed TG\_ADSS\_Ram
- Starting TG\_KeepAlive\_Ram
- Completed TG\_KeepAlive\_Ram
- Starting TG\_Tset\_LkDwn\_Ram
- Completed TG\_Tset\_LkDwn\_Ram
- Starting TG\_Edge\_Ram
- Completed TG\_Edge\_Ram
- Starting TG\_Format\_Ram
- Completed TG\_Format\_Ram
- Starting TG\_FormatLkDwn\_Ram
- $\hbox{-} \quad Completed \ TG\_FormatLkDwn\_Ram$
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Channel\_Board Quick Check of slot 2 (C00BA21) at 5:15:16 PM

%JOB\_START - Beginning Channel\_Board Quick Check test on slot 3 at 5:15:21 PM on 2/29/2020 Workbook Rev V7.30.12\_0835\_MOUT IG-XL Version: 3.40.13 DIB # 305253E Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting PG\_History\_Ram
- Completed PG\_History\_Ram
- Starting PG\_Svm\_Ram
- Completed PG\_Svm\_Ram
- Starting PG\_Scramble\_Ram, ADSS
- $\hbox{-} \quad Completed \ PG\_Scramble\_Ram$

- Starting PG\_Scramble\_Ram, Tset
- Completed PG\_Scramble\_Ram
- Starting PG\_LVM\_BIST\_Ram (up to 32 sec)
- Completed PG\_LVM\_BIST\_Ram in 21.6 sec
- LRS Off
- Starting TG Register Tests
- Completed TG Register Tests
- LRS On
- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG\_Period\_Ram
- Completed TG\_Period\_Ram
- Starting TG\_Period\_Map\_Ram
- Completed TG\_Period\_Map\_Ram
- Starting TG\_LVM\_BIST\_Ram (up to 32 sec)
- Completed TG\_LVM\_BIST\_Ram in 10.8 sec
- Starting TG\_History\_Ram
- Completed TG\_History\_Ram
- Starting TG\_SVM\_Ram
- Completed TG\_SVM\_Ram
- Starting TG\_ADSS\_Ram
- Completed TG\_ADSS\_Ram
- Starting TG\_KeepAlive\_Ram
- Completed TG\_KeepAlive\_Ram
- Starting TG\_Tset\_LkDwn\_Ram
- Completed TG\_Tset\_LkDwn\_Ram
- Starting TG Edge Ram
- Completed TG\_Edge\_Ram
- Starting TG\_Format\_Ram
- Completed TG\_Format\_Ram
- Starting TG\_FormatLkDwn\_Ram
- Completed TG\_FormatLkDwn\_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers

- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Channel\_Board Quick Check of slot 3 (802E71C) at 5:16:11 PM

%JOB\_START - Beginning Channel\_Board Quick Check test on slot 4 at 5:16:16 PM on 2/29/2020 Workbook Rev V7.30.12\_0835\_MOUT IG-XL Version: 3.40.13 DIB # 305253E Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting PG\_History\_Ram
- Completed PG\_History\_Ram
- Starting PG\_Svm\_Ram
- Completed PG\_Svm\_Ram
- Starting PG\_Scramble\_Ram, ADSS
- Completed PG\_Scramble\_Ram
- Starting PG\_Scramble\_Ram, Tset
- Completed PG\_Scramble\_Ram
- Starting PG\_LVM\_BIST\_Ram (up to 32 sec)
- Completed PG\_LVM\_BIST\_Ram in 21.6 sec

#### - LRS Off

- Starting TG Register Tests
- Completed TG Register Tests

#### - LRS On

- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG\_Period\_Ram
- Completed TG\_Period\_Ram
- Starting TG\_Period\_Map\_Ram
- Completed TG\_Period\_Map\_Ram
- Starting TG\_LVM\_BIST\_Ram (up to 32 sec)
- Completed TG\_LVM\_BIST\_Ram in 10.8 sec
- Starting TG\_History\_Ram
- Completed TG\_History\_Ram

- Starting TG\_SVM\_Ram
- Completed TG\_SVM\_Ram
- Starting TG\_ADSS\_Ram
- Completed TG\_ADSS\_Ram
- Starting TG\_KeepAlive\_Ram
- Completed TG\_KeepAlive\_Ram
- Starting TG\_Tset\_LkDwn\_Ram
- Completed TG\_Tset\_LkDwn\_Ram
- Starting TG\_Edge\_Ram
- Completed TG\_Edge\_Ram
- Starting TG\_Format\_Ram
- Completed TG\_Format\_Ram
- Starting TG\_FormatLkDwn\_Ram
- Completed TG\_FormatLkDwn\_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB END - \*\*\*\*PASSED\*\*\*\* Channel Board Quick Check of slot 4 (80170C3) at 5:17:06 PM

 $\% JOB\_START$ - Beginning Channel\_Board Quick Check test on slot 5 at 5:17:11 PM on 2/29/2020 Workbook Rev V7.30.12\_0835\_MOUT  $\,$  IG-XL Version: 3.40.13  $\,$  DIB # 305253E Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting PG\_History\_Ram
- Completed PG\_History\_Ram
- Starting PG\_Svm\_Ram
- Completed PG Svm Ram
- Starting PG\_Scramble\_Ram, ADSS
- Completed PG\_Scramble\_Ram
- Starting PG\_Scramble\_Ram, Tset
- Completed PG\_Scramble\_Ram

- Starting PG\_LVM\_BIST\_Ram (up to 32 sec)
- Completed PG\_LVM\_BIST\_Ram in 21.6 sec
- LRS Off
- Starting TG Register Tests
- Completed TG Register Tests
- LRS On
- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG\_Period\_Ram
- Completed TG\_Period\_Ram
- Starting TG\_Period\_Map\_Ram
- Completed TG\_Period\_Map\_Ram
- Starting TG\_LVM\_BIST\_Ram (up to 32 sec)
- Completed TG\_LVM\_BIST\_Ram in 10.8 sec
- Starting TG\_History\_Ram
- Completed TG\_History\_Ram
- Starting TG\_SVM\_Ram
- Completed TG\_SVM\_Ram
- Starting TG\_ADSS\_Ram
- Completed TG\_ADSS\_Ram
- Starting TG\_KeepAlive\_Ram
- Completed TG\_KeepAlive\_Ram
- Starting TG\_Tset\_LkDwn\_Ram
- Completed TG\_Tset\_LkDwn\_Ram
- Starting TG\_Edge\_Ram
- Completed TG\_Edge\_Ram
- Starting TG Format Ram
- Completed TG\_Format\_Ram
- Starting TG\_FormatLkDwn\_Ram
- Completed TG\_FormatLkDwn\_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test

#### - Checker COMPLETE!

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Channel\_Board Quick Check of slot 5 (C00B7FC) at 5:18:01 PM

 $\% JOB\_START$  - Beginning Channel\_Board Quick Check test on slot 6 at 5:18:06 PM on 2/29/2020 Workbook Rev V7.30.12\_0835\_MOUT  $\,$  IG-XL Version: 3.40.13  $\,$  DIB # 305253E Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting PG\_History\_Ram
- Completed PG\_History\_Ram
- Starting PG\_Svm\_Ram
- Completed PG\_Svm\_Ram
- Starting PG\_Scramble\_Ram, ADSS
- Completed PG\_Scramble\_Ram
- Starting PG\_Scramble\_Ram, Tset
- Completed PG\_Scramble\_Ram
- Starting PG\_LVM\_BIST\_Ram (up to 32 sec)
- Completed PG\_LVM\_BIST\_Ram in 21.6 sec

#### - LRS Off

- Starting TG Register Tests
- Completed TG Register Tests

# - LRS On

- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG Period Ram
- Completed TG\_Period\_Ram
- Starting TG\_Period\_Map\_Ram
- Completed TG\_Period\_Map\_Ram
- Starting TG\_LVM\_BIST\_Ram (up to 32 sec)
- Completed TG\_LVM\_BIST\_Ram in 10.8 sec
- Starting TG\_History\_Ram
- Completed TG\_History\_Ram
- Starting TG\_SVM\_Ram
- Completed TG\_SVM\_Ram

- Starting TG\_ADSS\_Ram
- Completed TG\_ADSS\_Ram
- Starting TG\_KeepAlive\_Ram
- Completed TG\_KeepAlive\_Ram
- Starting TG\_Tset\_LkDwn\_Ram
- Completed TG\_Tset\_LkDwn\_Ram
- Starting TG\_Edge\_Ram
- Completed TG\_Edge\_Ram
- Starting TG\_Format\_Ram
- Completed TG\_Format\_Ram
- Starting TG\_FormatLkDwn\_Ram
- Completed TG\_FormatLkDwn\_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Channel\_Board Quick Check of slot 6 (C0EB261) at 5:18:56 PM

%JOB\_START - Beginning Channel\_Board Quick Check test on slot 7 at 5:19:02 PM on 2/29/2020 Workbook Rev V7.30.12\_0835\_MOUT IG-XL Version: 3.40.13 DIB # 305253E Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting PG History Ram
- Completed PG\_History\_Ram
- Starting PG\_Svm\_Ram
- Completed PG\_Svm\_Ram
- Starting PG\_Scramble\_Ram, ADSS
- Completed PG\_Scramble\_Ram
- Starting PG\_Scramble\_Ram, Tset
- Completed PG\_Scramble\_Ram
- Starting PG\_LVM\_BIST\_Ram (up to 32 sec)
- Completed PG\_LVM\_BIST\_Ram in 21.6 sec

- LRS Off
- Starting TG Register Tests
- Completed TG Register Tests
- LRS On
- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG\_Period\_Ram
- Completed TG\_Period\_Ram
- Starting TG\_Period\_Map\_Ram
- Completed TG\_Period\_Map\_Ram
- Starting TG\_LVM\_BIST\_Ram (up to 32 sec)
- Completed TG\_LVM\_BIST\_Ram in 10.8 sec
- Starting TG\_History\_Ram
- Completed TG\_History\_Ram
- Starting TG\_SVM\_Ram
- Completed TG\_SVM\_Ram
- Starting TG\_ADSS\_Ram
- Completed TG\_ADSS\_Ram
- Starting TG\_KeepAlive\_Ram
- Completed TG\_KeepAlive\_Ram
- Starting TG\_Tset\_LkDwn\_Ram
- Completed TG\_Tset\_LkDwn\_Ram
- Starting TG\_Edge\_Ram
- Completed TG\_Edge\_Ram
- Starting TG\_Format\_Ram
- Completed TG\_Format\_Ram
- Starting TG FormatLkDwn Ram
- Completed TG\_FormatLkDwn\_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Channel\_Board Quick Check of slot 7 (C00523D) at 5:19:51 PM

 $\mbox{\sc MJOB\_START}$  - Beginning Relay\_Board\_Lower Quick Check test on slot 0 at 5:19:57 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT  $\,$  IG-XL Version: 3.40.13  $\,$  DIB # 305253E Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 0 in Quick Mode (Cal Relay DIB Not Required)

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Relay\_Board\_Lower Quick Check of slot 0 at 5:19:57 PM

 $\mbox{\ensuremath{\mbox{MOB\_START}}}$  - Beginning Relay\_Board\_Lower Quick Check test on slot 1 at 5:20:03 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT  $\,$  IG-XL Version: 3.40.13  $\,$  DIB # 305253E Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 1 in Quick Mode (Cal Relay DIB Not Required)

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Relay\_Board\_Lower Quick Check of slot 1 at 5:20:03 PM

 $\mbox{\sc MOB\_START}$  - Beginning Relay\_Board\_Lower Quick Check test on slot 2 at 5:20:09 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT  $\,$  IG-XL Version: 3.40.13  $\,$  DIB # 305253E Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 2 in Quick Mode (Cal Relay DIB Not Required)

%JOB END - \*\*\*\*PASSED\*\*\*\* Relay Board Lower Quick Check of slot 2 at 5:20:09 PM

 $\mbox{\sc MJOB\_START}$  - Beginning Relay\_Board\_Lower Quick Check test on slot 3 at 5:20:15 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT IG-XL Version: 3.40.13 DIB # 305253E Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 3 in Quick Mode (Cal Relay DIB Not

Required)

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Relay\_Board\_Lower Quick Check of slot 3 at 5:20:15 PM

 $\mbox{\sc MJOB\_START}$  - Beginning Relay\_Board\_Lower Quick Check test on slot 4 at 5:20:21 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT  $\,$  IG-XL Version: 3.40.13  $\,$  DIB # 305253E Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 4 in Quick Mode (Cal Relay DIB Not Required)

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Relay\_Board\_Lower Quick Check of slot 4 at 5:20:21 PM

 $\mbox{\sc MJOB\_START}$  - Beginning Relay\_Board\_Lower Quick Check test on slot 5 at 5:20:27 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT  $\,$  IG-XL Version: 3.40.13  $\,$  DIB # 305253E Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 5 in Quick Mode (Cal Relay DIB Not Required)

%JOB END - \*\*\*\*PASSED\*\*\*\* Relay Board Lower Quick Check of slot 5 at 5:20:28 PM

 $\% JOB\_START$  - Beginning Relay\_Board\_Lower Quick Check test on slot 6 at 5:20:33 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT  $\,$  IG-XL Version: 3.40.13  $\,$  DIB # 305253E Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 6 in Quick Mode (Cal Relay DIB Not Required)

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Relay\_Board\_Lower Quick Check of slot 6 at 5:20:34 PM

 $\mbox{\sc MJOB\_START}$  - Beginning Relay\_Board\_Lower Quick Check test on slot 7 at 5:20:39 PM on 2/29/2020

- Running Relay Checker Ver 1.03 on AG012 in Slot 7 in Quick Mode (Cal Relay DIB Not Required)

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Relay\_Board\_Lower Quick Check of slot 7 at 5:20:40 PM

%JOB\_START - Beginning Relay\_Board\_Upper Quick Check test on slot 0 at 5:20:45 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT  $\,$  IG-XL Version: 3.40.13  $\,$  DIB # 305253E Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 0 in Quick Mode (Cal Relay DIB Not Required)

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Relay\_Board\_Upper Quick Check of slot 0 at 5:20:46 PM

 $\mbox{\sc MJOB\_START}$  - Beginning Relay\_Board\_Upper Quick Check test on slot 1 at 5:20:51 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT  $\,$  IG-XL Version: 3.40.13  $\,$  DIB # 305253E Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 1 in Quick Mode (Cal Relay DIB Not Required)

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Relay\_Board\_Upper Quick Check of slot 1 at 5:20:52 PM

 $\mbox{\sc MJOB\_START}$  - Beginning Relay\_Board\_Upper Quick Check test on slot 2 at 5:20:58 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT  $\,$  IG-XL Version: 3.40.13  $\,$  DIB # 305253E Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 2 in Quick Mode (Cal Relay DIB Not Required)

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Relay\_Board\_Upper Quick Check of slot 2 at 5:20:58 PM

 $\% JOB\_START$  - Beginning Relay\_Board\_Upper Quick Check test on slot 3 at 5:21:04 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT  $\,$  IG-XL Version: 3.40.13  $\,$  DIB # 305253E Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 3 in Quick Mode (Cal Relay DIB Not Required)

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Relay\_Board\_Upper Quick Check of slot 3 at 5:21:04 PM

 $\mbox{\sc MJOB\_START}$  - Beginning Relay\_Board\_Upper Quick Check test on slot 4 at 5:21:10 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT  $\,$  IG-XL Version: 3.40.13  $\,$  DIB # 305253E Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 4 in Quick Mode (Cal Relay DIB Not Required)

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Relay\_Board\_Upper Quick Check of slot 4 at 5:21:10 PM

%JOB\_START - Beginning Relay\_Board\_Upper Quick Check test on slot 5 at 5:21:16 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT  $\,$  IG-XL Version: 3.40.13  $\,$  DIB # 305253E Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 5 in Quick Mode (Cal Relay DIB Not Required)

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Relay\_Board\_Upper Quick Check of slot 5 at 5:21:16 PM

 $\% JOB\_START$  - Beginning Relay\_Board\_Upper Quick Check test on slot 6 at 5:21:22 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT IG-XL Version: 3.40.13 DIB # 305253E Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 6 in Quick Mode (Cal Relay DIB Not Required)

%JOB END - \*\*\*\*PASSED\*\*\*\* Relay Board Upper Quick Check of slot 6 at 5:21:22 PM

%JOB\_START - Beginning Relay\_Board\_Upper Quick Check test on slot 7 at 5:21:28 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT  $\,$  IG-XL Version: 3.40.13  $\,$  DIB # 305253E Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 7 in Quick Mode (Cal Relay DIB Not Required)

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Relay\_Board\_Upper Quick Check of slot 7 at 5:21:29 PM

- Performing register test...
- Completed register test.
- Performing idprom and temperature test...
- Completed idprom and temperature test.
- Performing PG test...
- Starting PG\_History\_Ram
- Completed PG\_History\_Ram
- Starting PG\_Svm\_Ram
- Completed PG\_Svm\_Ram
- Starting PG\_Scramble\_Ram, ADSS
- Completed PG Scramble Ram
- Starting PG\_Scramble\_Ram, Tset
- Completed PG\_Scramble\_Ram
- Starting PG\_LVM\_BIST\_Ram (up to 32 sec)
- Completed PG LVM BIST Ram in 21.6 sec
- Completed PG test.
- Performing internal loopback test...
- Completed internal loopback test.
- Performing local reference test...
- Completed local reference test.
- Performing internal Capture burst test...
- Completed internal Capture burst test.
- Performing internal loopback burst test...
- Completed internal loopback burst test.

%JOB\_END - \*\*\*\*PASSED\*\*\*\* CTO Quick Check of slot 17 (C398747) at 5:21:58 PM

%JOB\_START - Beginning CTO\_DIB Quick Check test on slot 17 at 5:22:04 PM on 2/29/2020 Workbook Rev V7.30.12\_0835\_MOUT IG-XL Version: 3.40.13 DIB # 305253E Rev 1137A

- Performing relay test...

%JOB\_END - \*\*\*\*PASSED\*\*\*\* CTO\_DIB Quick Check of slot 17 (C398747) at 5:22:04 PM

- Verifying DPS Current Leakage
- Channel 0
- Channel 1
- Channel 2
- Channel 3
- Channel 4
- Channel 5
- Channel 6
- Channel 7
- Finished Verifying DPS Current Leakage

 $\mbox{\%JOB\_END}$  - \*\*\*\*PASSED\*\*\*\* DPS Quick Check of slot 21 (5009CD5) at 5:22:12 PM

- Verifying DPS Current Leakage
- Channel 0
- Channel 1
- Channel 2
- Channel 3

- Channel 4
- Channel 5
- Channel 6
- Channel 7
- Finished Verifying DPS Current Leakage

%JOB\_END - \*\*\*\*PASSED\*\*\*\* DPS Quick Check of slot 22 (800E4C6) at 5:22:20 PM

%JOB\_START - Beginning DPS Quick Check test on slot 23 at 5:22:26 PM on 2/29/2020 Workbook Rev V7.30.12\_0835\_MOUT IG-XL Version: 3.40.13 DIB # 305253E Rev 1137A

- Verifying DPS Current Leakage
- Channel 0
- Channel 1
- Channel 2
- Channel 3
- Channel 4
- Channel 5
- Channel 6
- Channel 7
- Finished Verifying DPS Current Leakage

%JOB\_END - \*\*\*\*PASSED\*\*\*\* DPS Quick Check of slot 23 (500B23A) at 5:22:28 PM

%JOB\_START - Beginning DPS Quick Check test on slot 24 at 5:22:33 PM on 2/29/2020 Workbook Rev V7.30.12\_0835\_MOUT IG-XL Version: 3.40.13 DIB # 305253E Rev 1137A

- Verifying DPS Current Leakage
- Channel 0
- Channel 1
- Channel 2
- Channel 3
- Channel 4
- Channel 5

- Channel 6
- Channel 7
- Finished Verifying DPS Current Leakage

%JOB\_END - \*\*\*\*PASSED\*\*\*\* DPS Quick Check of slot 24 (5009DD2) at 5:22:36 PM

 $\mbox{\%}\mbox{JOB\_START}$  - Beginning systemwide tests at 5:22:42 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT IG-XL Version: 3.40.13 DIB # 305253E Rev 1137A

- Systemwide functionality and continuity to slot 0
- Systemwide functionality and continuity to slot 1
- Systemwide functionality and continuity to slot 2
- Systemwide functionality and continuity to slot 3
- Systemwide functionality and continuity to slot 4
- Systemwide functionality and continuity to slot 5
- Systemwide functionality and continuity to slot 6
- Systemwide functionality and continuity to slot 7
- Starting BackPlane Fail Bus test
- Completed BackPlane Fail Bus test
- Completed Extra

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Systemwide tests at 5:23:49 PM

%JOB\_START - Beginning CUB Module Check test on slot 18 at 5:23:55 PM on 2/29/2020 Workbook Rev V7.30.12\_0835\_MOUT IG-XL Version: 3.40.13 DIB # 305253E Rev 1137A

- Testing CalCUB and CalDIB Leakage
- Completed Cal Dib Leakage Test
- Testing CalCUB Voltage Sources on CalDIB
- Beginning Cal DIB RawV Test on Slot18
- Completed Cal DIB RawV Test on Slot 18
- Beginning Cal Dib to BPMU Test on Slot 0
- Completed Cal Dib to BPMU Test on Slot 0
- Beginning Cal Dib to EXTERN Test on Slot 0
- Completed Cal Dib to EXTERN Test on Slot 0

- Testing CalDIB Utility Bit Relays using Channel Board Utility Bits for Slot 0
- Completed Cal Dib to UTIL Test on Slot 0
- Beginning Cal Dib RF Tree & Pogo Test
- Beginning Cal Dib to BPMU Test on Slot 1
- Completed Cal Dib to BPMU Test on Slot 1
- Beginning Cal Dib to EXTERN Test on Slot 1
- Completed Cal Dib to EXTERN Test on Slot 1
- Testing CalDIB Utility Bit Relays using Channel Board Utility Bits for Slot 1
- Completed Cal Dib to UTIL Test on Slot 1
- Beginning Cal Dib RF Tree & Pogo Test
- Beginning Cal Dib to BPMU Test on Slot 2
- Completed Cal Dib to BPMU Test on Slot 2
- Beginning Cal Dib to EXTERN Test on Slot 2
- Completed Cal Dib to EXTERN Test on Slot 2
- Testing CalDIB Utility Bit Relays using Channel Board Utility Bits for Slot 2
- Completed Cal Dib to UTIL Test on Slot 2
- Beginning Cal Dib RF Tree & Pogo Test
- Beginning Cal Dib to BPMU Test on Slot 3
- Completed Cal Dib to BPMU Test on Slot 3
- Beginning Cal Dib to EXTERN Test on Slot 3
- Completed Cal Dib to EXTERN Test on Slot 3
- Testing CalDIB Utility Bit Relays using Channel Board Utility Bits for Slot 3
- Completed Cal Dib to UTIL Test on Slot 3
- Beginning Cal Dib RF Tree & Pogo Test
- Beginning Cal Dib to BPMU Test on Slot 4
- Completed Cal Dib to BPMU Test on Slot 4
- Beginning Cal Dib to EXTERN Test on Slot 4
- Completed Cal Dib to EXTERN Test on Slot 4
- Testing CalDIB Utility Bit Relays using Channel Board Utility Bits for Slot 4
- Completed Cal Dib to UTIL Test on Slot 4
- Beginning Cal Dib RF Tree & Pogo Test
- Beginning Cal Dib to BPMU Test on Slot 5
- Completed Cal Dib to BPMU Test on Slot 5

- Beginning Cal Dib to EXTERN Test on Slot 5
- Completed Cal Dib to EXTERN Test on Slot 5
- Testing CalDIB Utility Bit Relays using Channel Board Utility Bits for Slot 5
- Completed Cal Dib to UTIL Test on Slot 5
- Beginning Cal Dib RF Tree & Pogo Test
- Beginning Cal Dib to BPMU Test on Slot 6
- Completed Cal Dib to BPMU Test on Slot 6
- Beginning Cal Dib to EXTERN Test on Slot 6
- Completed Cal Dib to EXTERN Test on Slot 6
- Testing CalDIB Utility Bit Relays using Channel Board Utility Bits for Slot 6
- Completed Cal Dib to UTIL Test on Slot 6
- Beginning Cal Dib RF Tree & Pogo Test
- Beginning Cal Dib to BPMU Test on Slot 7
- Completed Cal Dib to BPMU Test on Slot 7
- Beginning Cal Dib to EXTERN Test on Slot 7
- Completed Cal Dib to EXTERN Test on Slot 7
- Testing CalDIB Utility Bit Relays using Channel Board Utility Bits for Slot 7
- Completed Cal Dib to UTIL Test on Slot 7
- Beginning Cal Dib RF Tree & Pogo Test
- Beginning Cal DIB to DPS\_FSG Test on Slot 21
- Completed Cal DIB to DPS\_FSG Test on Slot 21
- Beginning Cal DIB to DPS\_FSG Test on Slot 22
- Completed Cal DIB to DPS\_FSG Test on Slot 22
- Beginning Cal DIB to DPS\_FSG Test on Slot 23
- Completed Cal DIB to DPS\_FSG Test on Slot 23
- Beginning Cal DIB to DPS FSG Test on Slot 24
- Completed Cal DIB to DPS\_FSG Test on Slot 24
- Testing Device Ground Sense on CalDIB
- Completed DGS Test
- \*\*\*\*Completed CalDib Test\*\*\*\*
- Start Compare Level Vol test
- Completed Compare Level Vol test
- Start Compare Level Voh test
- Completed Compare Level Voh test
- Start Drive\_Level\_test VIL

- Completed Drive\_Level\_test VIL
- Start Drive\_Level\_test VIH
- Completed Drive\_Level\_test VIH
- Beginning CalCub\_Measure\_Current using Bpmu in Slot 0
- Completed CalCub\_Measure\_Current

%JOB\_END - \*\*\*\*PASSED\*\*\*\* CUB Module Check of slot 18 (C017D6D) at 5:25:30 PM

 $\mbox{\sc MOB\_START}$  - Beginning Channel\_Board\_DIB Module Check test on slot 0 at 5:25:35 PM on 2/29/2020

- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test
- Starting Pin PMU Checker
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests...
- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
- Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
- Continuing Compare Level tests

- Completed Commpare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz

- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
- Statebus: Checking STB lines: Normal mode, 30MHz
- Statebus : Checking STB lines : Normal mode, 50MHz
- Statebus: Checking STB lines: Normal mode, 80MHz
- Statebus: Checking STB lines: Normal mode, 100MHz
- Statebus: Checking State number lines: Extended mode, 25MHz
- Statebus: Checking State number lines: Extended mode, 30MHz
- Statebus : Checking State number lines : Extended mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 25MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 30MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 35MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 35MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 80MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
- Started Scan Load test
- Completed Scan Load test
- Started Scan ADB test
- Completed Scan ADB test
- Completed Scan test
- Starting MultiClock test
- Completed MultiClock test.
- Starting SCIO test

- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel\_Board\_DIB test on slot 0

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Channel\_Board\_DIB Module Check of slot 0 (500A534) at 5:30:00 PM

 $\mbox{\sc MOB\_START}$  - Beginning Channel\_Board\_DIB Module Check test on slot 1 at 5:30:05 PM on 2/29/2020

- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test
- Starting Pin PMU Checker
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests...
- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
- Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
- Continuing Compare Level tests

- Completed Commpare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz

- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
- Statebus: Checking STB lines: Normal mode, 30MHz
- Statebus: Checking STB lines: Normal mode, 50MHz
- Statebus: Checking STB lines: Normal mode, 80MHz
- Statebus: Checking STB lines: Normal mode, 100MHz
- Statebus: Checking State number lines: Extended mode, 25MHz
- Statebus: Checking State number lines: Extended mode, 30MHz
- Statebus : Checking State number lines : Extended mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 25MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 30MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 35MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 35MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 80MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
- Started Scan Load test
- Completed Scan Load test
- Started Scan ADB test
- Completed Scan ADB test
- Completed Scan test
- Starting MultiClock test
- Completed MultiClock test.
- Starting SCIO test

- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel\_Board\_DIB test on slot 1

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Channel\_Board\_DIB Module Check of slot 1 (5009E7C) at 5:34:29 PM

 $\mbox{\sc MOB\_START}$  - Beginning Channel\_Board\_DIB Module Check test on slot 2 at 5:34:35 PM on 2/29/2020

- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test
- Starting Pin PMU Checker
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests...
- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
- Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
- Continuing Compare Level tests

- Continuing Compare Level tests
- Continuing Compare Level tests
- Completed Commpare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.

- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
- Statebus: Checking STB lines: Normal mode, 30MHz
- Statebus: Checking STB lines: Normal mode, 50MHz
- Statebus: Checking STB lines: Normal mode, 80MHz
- Statebus: Checking STB lines: Normal mode, 100MHz
- Statebus: Checking State number lines: Extended mode, 25MHz
- Statebus : Checking State number lines : Extended mode, 30MHz
- Statebus: Checking State number lines: Extended mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 25MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 30MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 35MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 35MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 80MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
- Started Scan Load test
- Completed Scan Load test
- Started Scan ADB test
- Completed Scan ADB test
- Completed Scan test
- Starting MultiClock test

- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel\_Board\_DIB test on slot 2

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Channel\_Board\_DIB Module Check of slot 2 (C00BA21) at 5:38:59 PM

 $\mbox{\sc MOB\_START}$  - Beginning Channel\_Board\_DIB Module Check test on slot 3 at 5:39:04 PM on 2/29/2020

- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test
- Starting Pin PMU Checker
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests...
- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
- Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests

- Continuing Compare Level tests
- Continuing Compare Level tests
- Completed Commpare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.

- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
- Statebus: Checking STB lines: Normal mode, 30MHz
- Statebus: Checking STB lines: Normal mode, 50MHz
- Statebus: Checking STB lines: Normal mode, 80MHz
- Statebus: Checking STB lines: Normal mode, 100MHz
- Statebus: Checking State number lines: Extended mode, 25MHz
- Statebus : Checking State number lines : Extended mode, 30MHz
- Statebus: Checking State number lines: Extended mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 25MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 30MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 35MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 35MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 80MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
- Started Scan Load test
- Completed Scan Load test
- Started Scan ADB test
- Completed Scan ADB test
- Completed Scan test
- Starting MultiClock test

- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel\_Board\_DIB test on slot 3

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Channel\_Board\_DIB Module Check of slot 3 (802E71C) at 5:43:28 PM

 $\mbox{\sc MOB\_START}$  - Beginning Channel\_Board\_DIB Module Check test on slot 4 at 5:43:34 PM on 2/29/2020

- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test
- Starting Pin PMU Checker
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests...
- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
- Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests

- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Completed Commpare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz

- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
- Statebus : Checking STB lines : Normal mode, 30MHz
- Statebus: Checking STB lines: Normal mode, 50MHz
- Statebus: Checking STB lines: Normal mode, 80MHz
- Statebus: Checking STB lines: Normal mode, 100MHz
- Statebus: Checking State number lines: Extended mode, 25MHz
- Statebus: Checking State number lines: Extended mode, 30MHz
- Statebus: Checking State number lines: Extended mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 25MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 30MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 35MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 35MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 80MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
- Started Scan Load test
- Completed Scan Load test
- Started Scan ADB test
- Completed Scan ADB test
- Completed Scan test

- Starting MultiClock test
- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel\_Board\_DIB test on slot 4

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Channel\_Board\_DIB Module Check of slot 4 (80170C3) at 5:47:58 PM

 $\mbox{\sc MOB\_START}$  - Beginning Channel\_Board\_DIB Module Check test on slot 5 at 5:48:03 PM on 2/29/2020

- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test
- Starting Pin PMU Checker
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests...
- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
- Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests

- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Completed Commpare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.

- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
- Statebus: Checking STB lines: Normal mode, 30MHz
- Statebus: Checking STB lines: Normal mode, 50MHz
- Statebus : Checking STB lines : Normal mode, 80MHz
- Statebus: Checking STB lines: Normal mode, 100MHz
- Statebus: Checking State number lines: Extended mode, 25MHz
- Statebus : Checking State number lines : Extended mode, 30MHz
- Statebus: Checking State number lines: Extended mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 25MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 30MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 35MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 35MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 80MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
- Started Scan Load test
- Completed Scan Load test
- Started Scan ADB test
- Completed Scan ADB test

- Completed Scan test
- Starting MultiClock test
- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel\_Board\_DIB test on slot 5

 $\% JOB\_END$  - \*\*\*\*PASSED\*\*\*\* Channel\_Board\_DIB Module Check of slot 5 (C00B7FC) at 5:52:27 PM

 $\mbox{\sc MOB\_START}$  - Beginning Channel\_Board\_DIB Module Check test on slot 6 at 5:52:33 PM on 2/29/2020

- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test
- Starting Pin PMU Checker
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests...
- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
- Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
- Continuing Compare Level tests

- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Completed Commpare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.

- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
- Statebus: Checking STB lines: Normal mode, 30MHz
- Statebus: Checking STB lines: Normal mode, 50MHz
- Statebus: Checking STB lines: Normal mode, 80MHz
- Statebus: Checking STB lines: Normal mode, 100MHz
- Statebus: Checking State number lines: Extended mode, 25MHz
- Statebus : Checking State number lines : Extended mode, 30MHz
- Statebus: Checking State number lines: Extended mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 25MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 30MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 35MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 35MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 80MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
- Started Scan Load test
- Completed Scan Load test
- Started Scan ADB test
- Completed Scan ADB test

- Completed Scan test
- Starting MultiClock test
- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel\_Board\_DIB test on slot 6

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Channel\_Board\_DIB Module Check of slot 6 (C0EB261) at 5:56:57 PM

 $\mbox{\sc MOB\_START}$  - Beginning Channel\_Board\_DIB Module Check test on slot 7 at 5:57:02 PM on 2/29/2020

- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test
- Starting Pin PMU Checker
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests...
- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
- Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests

- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Completed Commpare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.

- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
- Statebus : Checking STB lines : Normal mode, 30MHz
- Statebus : Checking STB lines : Normal mode, 50MHz
- Statebus : Checking STB lines : Normal mode, 80MHz
- Statebus: Checking STB lines: Normal mode, 100MHz
- Statebus: Checking State number lines: Extended mode, 25MHz
- Statebus: Checking State number lines: Extended mode, 30MHz
- Statebus: Checking State number lines: Extended mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 25MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 30MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 35MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 35MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 80MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
- Started Scan Load test
- Completed Scan Load test
- Started Scan ADB test

- Completed Scan ADB test
- Completed Scan test
- Starting MultiClock test
- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel\_Board\_DIB test on slot 7

 $\% JOB\_END$  - \*\*\*\*PASSED\*\*\*\* Channel\_Board\_DIB Module Check of slot 7 (C00523D) at 6:01:26 PM

 $\mbox{\sc Module Check}$ test on slot0 at 6:01:32 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT  $\,$  IG-XL Version: 3.40.13  $\,$  DIB # 305253E Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 0 in Full Mode

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Relay\_Board\_Lower Module Check of slot 0 at 6:01:33 PM

 $\% JOB\_START$  - Beginning Relay\_Board\_Lower Module Check test on slot 1 at 6:01:38 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT  $\,$  IG-XL Version: 3.40.13  $\,$  DIB # 305253E Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 1 in Full Mode

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Relay\_Board\_Lower Module Check of slot 1 at 6:01:39 PM

%JOB\_START - Beginning Relay\_Board\_Lower Module Check test on slot 2 at 6:01:44 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT  $\,$  IG-XL Version: 3.40.13  $\,$  DIB # 305253E Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 2 in Full Mode

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Relay\_Board\_Lower Module Check of slot 2 at 6:01:45 PM

 $\mbox{\sc Module Check test}$  on slot 3 at 6:01:50 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT  $\,$  IG-XL Version: 3.40.13  $\,$  DIB # 305253E Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 3 in Full Mode

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Relay\_Board\_Lower Module Check of slot 3 at 6:01:51 PM

 $\mbox{\sc Module Check}$ test on slot4 at 6:01:56 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT  $\,$  IG-XL Version: 3.40.13  $\,$  DIB # 305253E Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 4 in Full Mode

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Relay\_Board\_Lower Module Check of slot 4 at 6:01:57 PM

%JOB\_START - Beginning Relay\_Board\_Lower Module Check test on slot 5 at 6:02:03 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT  $\,$  IG-XL Version: 3.40.13  $\,$  DIB # 305253E Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 5 in Full Mode

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Relay\_Board\_Lower Module Check of slot 5 at 6:02:03 PM

%JOB\_START - Beginning Relay\_Board\_Lower Module Check test on slot 6 at 6:02:09 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT  $\,$  IG-XL Version: 3.40.13  $\,$  DIB # 305253E Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 6 in Full Mode

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Relay\_Board\_Lower Module Check of slot 6 at 6:02:09 PM

 $\% JOB\_START$  - Beginning Relay\_Board\_Lower Module Check test on slot 7 at 6:02:15 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT  $\,$  IG-XL Version: 3.40.13  $\,$  DIB # 305253E Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 7 in Full Mode

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Relay\_Board\_Lower Module Check of slot 7 at 6:02:15 PM

%JOB\_START - Beginning Relay\_Board\_Upper Module Check test on slot 0 at 6:02:21 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT  $\,$  IG-XL Version: 3.40.13  $\,$  DIB # 305253E Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 0 in Full Mode

 $\mbox{\%JOB\_END}$  - \*\*\*\*PASSED\*\*\*\* Relay\_Board\_Upper Module Check of slot 0 at 6:02:21 PM

%JOB\_START - Beginning Relay\_Board\_Upper Module Check test on slot 1 at 6:02:27 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT  $\,$  IG-XL Version: 3.40.13  $\,$  DIB # 305253E Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 1 in Full Mode

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Relay\_Board\_Upper Module Check of slot 1 at 6:02:27 PM

 $\mbox{\sc MOB\_START}$  - Beginning Relay\_Board\_Upper Module Check test on slot 2 at 6:02:33 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT  $\,$  IG-XL Version: 3.40.13  $\,$  DIB # 305253E Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 2 in Full Mode

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Relay\_Board\_Upper Module Check of slot 2 at 6:02:34 PM

 $\% JOB\_START$  - Beginning Relay\_Board\_Upper Module Check test on slot 3 at 6:02:39 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT  $\,$  IG-XL Version: 3.40.13  $\,$  DIB # 305253E Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 3 in Full Mode

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Relay\_Board\_Upper Module Check of slot 3 at 6:02:40 PM

%JOB\_START - Beginning Relay\_Board\_Upper Module Check test on slot 4 at 6:02:45 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT  $\,$  IG-XL Version: 3.40.13  $\,$  DIB # 305253E Rev 1137A

- Running Relay Checker Ver1.03 on AG009 in Slot 4 in Full Mode

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Relay\_Board\_Upper Module Check of slot 4 at 6:02:46 PM

 $\mbox{\sc MOB\_START}$  - Beginning Relay\_Board\_Upper Module Check test on slot 5 at 6:02:51 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT  $\,$  IG-XL Version: 3.40.13  $\,$  DIB # 305253E Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 5 in Full Mode

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Relay\_Board\_Upper Module Check of slot 5 at 6:02:52 PM

 $\mbox{\sc MOB\_START}$  - Beginning Relay\_Board\_Upper Module Check test on slot 6 at 6:02:57 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT  $\,$  IG-XL Version: 3.40.13  $\,$  DIB # 305253E Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 6 in Full Mode

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Relay\_Board\_Upper Module Check of slot 6 at 6:02:58 PM

 $\mbox{\sc MOB\_START}$  - Beginning Relay\_Board\_Upper Module Check test on slot 7 at 6:03:04 PM on 2/29/2020

- Running Relay Checker Ver 1.03 on AG009 in Slot 7 in Full Mode

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Relay\_Board\_Upper Module Check of slot 7 at 6:03:04 PM
%JOB\_START - Beginning DPS\_DIB Module Check test on slot 21 at 6:03:10 PM on 2/29/2020
Workbook Rev V7.30.12\_0835\_MOUT IG-XL Version: 3.40.13 DIB # 305253E
Rev 1137A

- Verifying DPS DIB MOUT Output Impedance Test
- Verifying DPS DIB Current Measure Output Test

%JOB\_END - \*\*\*\*PASSED\*\*\*\* DPS\_DIB Module Check of slot 21 (5009CD5) at 6:03:29 PM
%JOB\_START - Beginning DPS\_DIB Module Check test on slot 22 at 6:03:34 PM on 2/29/2020
Workbook Rev V7.30.12\_0835\_MOUT IG-XL Version: 3.40.13 DIB # 305253E
Rev 1137A

- Verifying DPS DIB MOUT Output Impedance Test
- Verifying DPS DIB Current Measure Output Test

%JOB\_END - \*\*\*\*PASSED\*\*\*\* DPS\_DIB Module Check of slot 22 (800E4C6) at 6:03:53 PM
%JOB\_START - Beginning DPS\_DIB Module Check test on slot 23 at 6:03:59 PM on 2/29/2020
Workbook Rev V7.30.12\_0835\_MOUT IG-XL Version: 3.40.13 DIB # 305253E
Rev 1137A

- Verifying DPS DIB MOUT Output Impedance Test
- Verifying DPS DIB Current Measure Output Test

%JOB\_END - \*\*\*\*PASSED\*\*\*\* DPS\_DIB Module Check of slot 23 (500B23A) at 6:04:18 PM
%JOB\_START - Beginning DPS\_DIB Module Check test on slot 24 at 6:04:24 PM on 2/29/2020
Workbook Rev V7.30.12\_0835\_MOUT IG-XL Version: 3.40.13 DIB # 305253E
Rev 1137A

- Verifying DPS DIB MOUT Output Impedance Test
- Verifying DPS DIB Current Measure Output Test

 $\mbox{\%JOB\_END}$  - \*\*\*\*PASSED\*\*\*\* DPS\_DIB Module Check of slot 24 (5009DD2) at 6:04:43 PM

- Performing CTO calibration test...
- Performing RAM test...

%JOB\_END - \*\*\*\*PASSED\*\*\*\* CTO Module Check of slot 17 (C398747) at 6:04:51 PM

 $\% JOB\_START$  - Beginning CTO\_DIB Module Check test on slot 17 at 6:04:57 PM on 2/29/2020 Workbook Rev V7.30.12\_0835\_MOUT  $\,$  IG-XL Version: 3.40.13  $\,$  DIB # 305253E Rev 1137A

- Performing internal reference test...
- Performing VREF voltage test...
- Performing PPMU force V test...
- Performing PPMU force I test...
- Performing PPMU measure V test...
- Performing PPMU measure I test...
- Performing Source voltage test...
- Performing Capture voltage test...
- Performing Source/Capture loopback test...
- Performing Source burst test...
- Performing Capture burst test...
- Performing Source/Capture loopback burst test...

 $\% JOB\_END$  - \*\*\*\*PASSED\*\*\*\* CTO\_DIB Module Check of slot 17 (C398747) at 6:06:58 PM

- Starting Channel Drive Levels: Vih Vil
- Starting Channel Compare Levels: Voh Vol

%JOB\_END - \*\*\*\*PASSED\*\*\*\* CUB Calibration of slot 18 (C017D6D) at 6:07:05 PM

%JOB\_START - Beginning Channel\_Board\_DIB Calibration test on slot 0 at 6:07:11 PM on 2/29/2020

- Starting dib\_test
- Temperature at PE Ch00 is  $57 \deg C$
- Temperature at PE Ch60 is  $40 \deg C$
- Temperature at Incoming Air is 26 deg C
- Temperature at TG Ch00 is  $45 \deg C$
- Starting BPMU Force Voltage
- Starting Bpmu Clamp Voltage
- Starting Bpmu Measure Voltage
- Starting Bpmu Measure Current
- Starting Bpmu Force current
- Starting Bpmu Limit current
- Starting Channel Drive Levels: Vih Vil
- Starting Channel Compare Levels: Voh Vol
- Starting Channel Clamps: Vch Vcl
- Starting Channel Loads: Ioh Iol
- Starting Channel Load Threshold: Vt
- Starting HV Channel Drive Levels
- Starting HV Channel Drive Current
- Ppmu Mi Warmup 6:09:23 PM
- Ppmu Mi Warmup 6:09:23 PM
- Starting Ppmu Force Voltage
- Starting Ppmu Measure Voltage
- Starting Ppmu Force Current Ppmu Force 200ua
- Starting Ppmu Force Current Ppmu Force 2ma
- Starting Ppmu Measure 2ma
- Continuing Ppmu Measure Current chan 15
- Continuing Ppmu Measure Current chan 31

- Continuing Ppmu Measure Current chan 47
- Continuing Ppmu Measure Current chan 63
- Starting Ppmu Measure 200ua
- Continuing Ppmu Measure Current chan 15
- Continuing Ppmu Measure Current chan 31
- Continuing Ppmu Measure Current chan 47
- Continuing Ppmu Measure Current chan 63
- Starting Ppmu Measure Int 20ua
- Continuing Ppmu Measure Current chan 15
- Continuing Ppmu Measure Current chan 31
- Continuing Ppmu Measure Current chan 47
- Continuing Ppmu Measure Current chan 63
- Starting Ppmu Measure Int 2ua
- Continuing Ppmu Measure Current chan 15
- Continuing Ppmu Measure Current chan 31
- Continuing Ppmu Measure Current chan 47
- Continuing Ppmu Measure Current chan 63
- Starting Ppmu Measure Int 200na
- Continuing Ppmu Measure Current chan 15
- Continuing Ppmu Measure Current chan 31
- Continuing Ppmu Measure Current chan 47
- Continuing Ppmu Measure Current chan 63
- Finished Channel Calibration

%JOB END - \*\*\*\*PASSED\*\*\*\* Channel Board DIB Calibration of slot 0 (500A534) at 6:12:29 PM

%JOB\_START - Beginning Channel\_Board\_DIB Calibration test on slot 1 at 6:12:35 PM on 2/29/2020

- Starting dib\_test
- Temperature at PE Ch00 is 59 deg C
- Temperature at PE Ch60 is 40 deg C
- Temperature at Incoming Air is 25 deg C
- Temperature at TG Ch00 is 45 deg C
- Starting BPMU Force Voltage
- Starting Bpmu Clamp Voltage

- Starting Bpmu Measure Voltage
- Starting Bpmu Measure Current
- Starting Bpmu Force current
- Starting Bpmu Limit current
- Starting Channel Drive Levels: Vih Vil
- Starting Channel Compare Levels: Voh Vol
- Starting Channel Clamps: Vch Vcl
- Starting Channel Loads: Ioh Iol
- Starting Channel Load Threshold: Vt
- Starting HV Channel Drive Levels
- Starting HV Channel Drive Current
- Ppmu Mi Warmup 6:14:47 PM
- Ppmu Mi Warmup 6:14:47 PM
- Starting Ppmu Force Voltage
- Starting Ppmu Measure Voltage
- Starting Ppmu Force Current Ppmu Force 200ua
- Starting Ppmu Force Current Ppmu Force 2ma
- Starting Ppmu Measure 2ma
- Continuing Ppmu Measure Current chan 79
- Continuing Ppmu Measure Current chan 95
- Continuing Ppmu Measure Current chan 111
- Continuing Ppmu Measure Current chan 127
- Starting Ppmu Measure 200ua
- Continuing Ppmu Measure Current chan 79
- Continuing Ppmu Measure Current chan 95
- Continuing Ppmu Measure Current chan 111
- Continuing Ppmu Measure Current chan 127
- Starting Ppmu Measure Int 20ua
- Continuing Ppmu Measure Current chan 79
- Continuing Ppmu Measure Current chan 95
- Continuing Ppmu Measure Current chan 111
- Continuing Ppmu Measure Current chan 127
- Starting Ppmu Measure Int 2ua
- Continuing Ppmu Measure Current chan 79
- Continuing Ppmu Measure Current chan 95
- Continuing Ppmu Measure Current chan 111
- Continuing Ppmu Measure Current chan 127
- Starting Ppmu Measure Int 200na

- Continuing Ppmu Measure Current chan 79
- Continuing Ppmu Measure Current chan 95
- Continuing Ppmu Measure Current chan 111
- Continuing Ppmu Measure Current chan 127
- Finished Channel Calibration

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Channel\_Board\_DIB Calibration of slot 1 (5009E7C) at 6:17:53 PM

 $\mbox{\sc MJOB\_START}$  - Beginning Channel\_Board\_DIB Calibration test on slot 2 at 6:17:59 PM on 2/29/2020

- Starting dib\_test
- Temperature at PE Ch00 is  $63 \deg C$
- Temperature at PE Ch60 is  $42 \deg C$
- Temperature at Incoming Air is 26 deg C
- Temperature at TG Ch00 is 46 deg C
- Starting BPMU Force Voltage
- Starting Bpmu Clamp Voltage
- Starting Bpmu Measure Voltage
- Starting Bpmu Measure Current
- Starting Bpmu Force current
- Starting Bpmu Limit current
- Starting Channel Drive Levels: Vih Vil
- Starting Channel Compare Levels: Voh Vol
- Starting Channel Clamps: Vch Vcl
- Starting Channel Loads: Ioh Iol
- Starting Channel Load Threshold: Vt
- Starting HV Channel Drive Levels
- Starting HV Channel Drive Current
- Ppmu Mi Warmup 6:20:11 PM
- Ppmu Mi Warmup 6:20:11 PM
- Starting Ppmu Force Voltage
- Starting Ppmu Measure Voltage
- Starting Ppmu Force Current Ppmu Force 200ua
- Starting Ppmu Force Current Ppmu Force 2ma
- Starting Ppmu Measure 2ma

- Continuing Ppmu Measure Current chan 143
- Continuing Ppmu Measure Current chan 159
- Continuing Ppmu Measure Current chan 175
- Continuing Ppmu Measure Current chan 191
- Starting Ppmu Measure 200ua
- Continuing Ppmu Measure Current chan 143
- Continuing Ppmu Measure Current chan 159
- Continuing Ppmu Measure Current chan 175
- Continuing Ppmu Measure Current chan 191
- Starting Ppmu Measure Int 20ua
- Continuing Ppmu Measure Current chan 143
- Continuing Ppmu Measure Current chan 159
- Continuing Ppmu Measure Current chan 175
- Continuing Ppmu Measure Current chan 191
- Starting Ppmu Measure Int 2ua
- Continuing Ppmu Measure Current chan 143
- Continuing Ppmu Measure Current chan 159
- Continuing Ppmu Measure Current chan 175
- Continuing Ppmu Measure Current chan 191
- Starting Ppmu Measure Int 200na
- Continuing Ppmu Measure Current chan 143
- Continuing Ppmu Measure Current chan 159
- Continuing Ppmu Measure Current chan 175
- Continuing Ppmu Measure Current chan 191
- Finished Channel Calibration

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Channel\_Board\_DIB Calibration of slot 2 (C00BA21) at 6:23:17 PM

%JOB\_START - Beginning Channel\_Board\_DIB Calibration test on slot 3 at 6:23:23 PM on 2/29/2020

- Starting dib\_test
- Temperature at PE Ch00 is 59 deg C
- Temperature at PE Ch60 is 38 deg C
- Temperature at Incoming Air is 24 deg C

- Temperature at TG Ch00 is 43 deg C
- Starting BPMU Force Voltage
- Starting Bpmu Clamp Voltage
- Starting Bpmu Measure Voltage
- Starting Bpmu Measure Current
- Starting Bpmu Force current
- Starting Bpmu Limit current
- Starting Channel Drive Levels: Vih Vil
- Starting Channel Compare Levels: Voh Vol
- Starting Channel Clamps: Vch Vcl
- Starting Channel Loads: Ioh Iol
- Starting Channel Load Threshold: Vt
- Starting HV Channel Drive Levels
- Starting HV Channel Drive Current
- Ppmu Mi Warmup 6:25:36 PM
- Ppmu Mi Warmup 6:25:36 PM
- Starting Ppmu Force Voltage
- Starting Ppmu Measure Voltage
- Starting Ppmu Force Current Ppmu Force 200ua
- Starting Ppmu Force Current Ppmu Force 2ma
- Starting Ppmu Measure 2ma
- Continuing Ppmu Measure Current chan 207
- Continuing Ppmu Measure Current chan 223
- Continuing Ppmu Measure Current chan 239
- Continuing Ppmu Measure Current chan 255
- Starting Ppmu Measure 200ua
- Continuing Ppmu Measure Current chan 207
- Continuing Ppmu Measure Current chan 223
- Continuing Ppmu Measure Current chan 239
- Continuing Ppmu Measure Current chan 255
- Starting Ppmu Measure Int 20ua
- Continuing Ppmu Measure Current chan 207
- Continuing Ppmu Measure Current chan 223
- Continuing Ppmu Measure Current chan 239
- Continuing Ppmu Measure Current chan 255
- Starting Ppmu Measure Int 2ua
- Continuing Ppmu Measure Current chan 207
- Continuing Ppmu Measure Current chan 223

- Continuing Ppmu Measure Current chan 239
- Continuing Ppmu Measure Current chan 255
- Starting Ppmu Measure Int 200na
- Continuing Ppmu Measure Current chan 207
- Continuing Ppmu Measure Current chan 223
- Continuing Ppmu Measure Current chan 239
- Continuing Ppmu Measure Current chan 255
- Finished Channel Calibration

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Channel\_Board\_DIB Calibration of slot 3 (802E71C) at 6:28:42 PM

 $\mbox{\sc MJOB\_START}$  - Beginning Channel\_Board\_DIB Calibration test on slot 4 at 6:28:48 PM on 2/29/2020

- Starting dib\_test
- Temperature at PE Ch00 is 59 deg C
- Temperature at PE Ch60 is  $40 \deg C$
- Temperature at Incoming Air is  $25 \deg C$
- Temperature at TG Ch00 is 46 deg C
- Starting BPMU Force Voltage
- Starting Bpmu Clamp Voltage
- Starting Bpmu Measure Voltage
- Starting Bpmu Measure Current
- Starting Bpmu Force current
- Starting Bpmu Limit current
- Starting Channel Drive Levels: Vih Vil
- Starting Channel Compare Levels: Voh Vol
- Starting Channel Clamps: Vch Vcl
- Starting Channel Loads: Ioh Iol
- Starting Channel Load Threshold: Vt
- Starting HV Channel Drive Levels
- Starting HV Channel Drive Current
- Ppmu Mi Warmup 6:31:00 PM
- Ppmu Mi Warmup 6:31:00 PM
- Starting Ppmu Force Voltage
- Starting Ppmu Measure Voltage

- Starting Ppmu Force Current Ppmu Force 200ua
- Starting Ppmu Force Current Ppmu Force 2ma
- Starting Ppmu Measure 2ma
- Continuing Ppmu Measure Current chan 271
- Continuing Ppmu Measure Current chan 287
- Continuing Ppmu Measure Current chan 303
- Continuing Ppmu Measure Current chan 319
- Starting Ppmu Measure 200ua
- Continuing Ppmu Measure Current chan 271
- Continuing Ppmu Measure Current chan 287
- Continuing Ppmu Measure Current chan 303
- Continuing Ppmu Measure Current chan 319
- Starting Ppmu Measure Int 20ua
- Continuing Ppmu Measure Current chan 271
- Continuing Ppmu Measure Current chan 287
- Continuing Ppmu Measure Current chan 303
- Continuing Ppmu Measure Current chan 319
- Starting Ppmu Measure Int 2ua
- Continuing Ppmu Measure Current chan 271
- Continuing Ppmu Measure Current chan 287
- Continuing Ppmu Measure Current chan 303
- Continuing Ppmu Measure Current chan 319
- Starting Ppmu Measure Int 200na
- Continuing Ppmu Measure Current chan 271
- Continuing Ppmu Measure Current chan 287
- Continuing Ppmu Measure Current chan 303
- Continuing Ppmu Measure Current chan 319
- Finished Channel Calibration

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Channel\_Board\_DIB Calibration of slot 4 (80170C3) at 6:34:06 PM

%JOB\_START - Beginning Channel\_Board\_DIB Calibration test on slot 5 at 6:34:11 PM on 2/29/2020

Rev 1137A

- Starting dib\_test
- Temperature at PE Ch00 is 62 deg C

- Temperature at PE Ch60 is 42 deg C
- Temperature at Incoming Air is 25 deg C
- Temperature at TG Ch00 is 44 deg C
- Starting BPMU Force Voltage
- Starting Bpmu Clamp Voltage
- Starting Bpmu Measure Voltage
- Starting Bpmu Measure Current
- Starting Bpmu Force current
- Starting Bpmu Limit current
- Starting Channel Drive Levels: Vih Vil
- Starting Channel Compare Levels: Voh Vol
- Starting Channel Clamps: Vch Vcl
- Starting Channel Loads: Ioh Iol
- Starting Channel Load Threshold: Vt
- Starting HV Channel Drive Levels
- Starting HV Channel Drive Current
- Ppmu Mi Warmup 6:36:22 PM
- Ppmu Mi Warmup 6:36:22 PM
- Starting Ppmu Force Voltage
- Starting Ppmu Measure Voltage
- Starting Ppmu Force Current Ppmu Force 200ua
- Starting Ppmu Force Current Ppmu Force 2ma
- Starting Ppmu Measure 2ma
- Continuing Ppmu Measure Current chan 335
- Continuing Ppmu Measure Current chan 351
- Continuing Ppmu Measure Current chan 367
- Continuing Ppmu Measure Current chan 383
- Starting Ppmu Measure 200ua
- Continuing Ppmu Measure Current chan 335
- Continuing Ppmu Measure Current chan 351
- Continuing Ppmu Measure Current chan 367
- Continuing Ppmu Measure Current chan 383
- Starting Ppmu Measure Int 20ua
- Continuing Ppmu Measure Current chan 335
- Continuing Ppmu Measure Current chan 351
- Continuing Ppmu Measure Current chan 367
- Continuing Ppmu Measure Current chan 383
- Starting Ppmu Measure Int 2ua

- Continuing Ppmu Measure Current chan 335
- Continuing Ppmu Measure Current chan 351
- Continuing Ppmu Measure Current chan 367
- Continuing Ppmu Measure Current chan 383
- Starting Ppmu Measure Int 200na
- Continuing Ppmu Measure Current chan 335
- Continuing Ppmu Measure Current chan 351
- Continuing Ppmu Measure Current chan 367
- Continuing Ppmu Measure Current chan 383
- Finished Channel Calibration

 $\% JOB\_END$  - \*\*\*\*PASSED\*\*\*\* Channel\_Board\_DIB Calibration of slot 5 (C00B7FC) at 6:39:28 PM

 $\mbox{\sc MJOB\_START}$  - Beginning Channel\_Board\_DIB Calibration test on slot 6 at 6:39:34 PM on 2/29/2020

- Starting dib\_test
- Temperature at PE Ch00 is 55 deg C
- Temperature at PE Ch60 is 42 deg C
- Temperature at Incoming Air is 25 deg C
- Temperature at TG Ch00 is 43 deg C
- Starting BPMU Force Voltage
- Starting Bpmu Clamp Voltage
- Starting Bpmu Measure Voltage
- Starting Bpmu Measure Current
- Starting Bpmu Force current
- Starting Bpmu Limit current
- Starting Channel Drive Levels: Vih Vil
- Starting Channel Compare Levels: Voh Vol
- Starting Channel Clamps: Vch Vcl
- Starting Channel Loads: Ioh Iol
- Starting Channel Load Threshold: Vt
- Starting HV Channel Drive Levels
- Starting HV Channel Drive Current
- Ppmu Mi Warmup 6:41:48 PM

- Ppmu Mi Warmup 6:41:48 PM
- Starting Ppmu Force Voltage
- Starting Ppmu Measure Voltage
- Starting Ppmu Force Current Ppmu Force 200ua
- Starting Ppmu Force Current Ppmu Force 2ma
- Starting Ppmu Measure 2ma
- Continuing Ppmu Measure Current chan 399
- Continuing Ppmu Measure Current chan 415
- Continuing Ppmu Measure Current chan 431
- Continuing Ppmu Measure Current chan 447
- Starting Ppmu Measure 200ua
- Continuing Ppmu Measure Current chan 399
- Continuing Ppmu Measure Current chan 415
- Continuing Ppmu Measure Current chan 431
- Continuing Ppmu Measure Current chan 447
- Starting Ppmu Measure Int 20ua
- Continuing Ppmu Measure Current chan 399
- Continuing Ppmu Measure Current chan 415
- Continuing Ppmu Measure Current chan 431
- Continuing Ppmu Measure Current chan 447
- Starting Ppmu Measure Int 2ua
- Continuing Ppmu Measure Current chan 399
- Continuing Ppmu Measure Current chan 415
- Continuing Ppmu Measure Current chan 431
- Continuing Ppmu Measure Current chan 447
- Starting Ppmu Measure Int 200na
- Continuing Ppmu Measure Current chan 399
- Continuing Ppmu Measure Current chan 415
- Continuing Ppmu Measure Current chan 431
- Continuing Ppmu Measure Current chan 447
- Finished Channel Calibration

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Channel\_Board\_DIB Calibration of slot 6 (C0EB261) at 6:44:54 PM

%JOB\_START - Beginning Channel\_Board\_DIB Calibration test on slot 7 at 6:45:00 PM on 2/29/2020

## Rev 1137A

- Starting dib\_test
- Temperature at PE Ch00 is  $55 \deg C$
- Temperature at PE Ch60 is 43 deg C
- Temperature at Incoming Air is 24 deg C
- Temperature at TG Ch00 is 43 deg C
- Starting BPMU Force Voltage
- Starting Bpmu Clamp Voltage
- Starting Bpmu Measure Voltage
- Starting Bpmu Measure Current
- Starting Bpmu Force current
- Starting Bpmu Limit current
- Starting Channel Drive Levels: Vih Vil
- Starting Channel Compare Levels: Voh Vol
- Starting Channel Clamps: Vch Vcl
- Starting Channel Loads: Ioh Iol
- Starting Channel Load Threshold: Vt
- Starting HV Channel Drive Levels
- Starting HV Channel Drive Current
- Ppmu Mi Warmup 6:47:12 PM
- Ppmu Mi Warmup 6:47:12 PM
- Starting Ppmu Force Voltage
- Starting Ppmu Measure Voltage
- Starting Ppmu Force Current Ppmu Force 200ua
- Starting Ppmu Force Current Ppmu Force 2ma
- Starting Ppmu Measure 2ma
- Continuing Ppmu Measure Current chan 463
- Continuing Ppmu Measure Current chan 479
- Continuing Ppmu Measure Current chan 495
- Continuing Ppmu Measure Current chan 511
- Starting Ppmu Measure 200ua
- Continuing Ppmu Measure Current chan 463
- Continuing Ppmu Measure Current chan 479
- Continuing Ppmu Measure Current chan 495
- Continuing Ppmu Measure Current chan 511
- Starting Ppmu Measure Int 20ua
- Continuing Ppmu Measure Current chan 463

- Continuing Ppmu Measure Current chan 479
- Continuing Ppmu Measure Current chan 495
- Continuing Ppmu Measure Current chan 511
- Starting Ppmu Measure Int 2ua
- Continuing Ppmu Measure Current chan 463
- Continuing Ppmu Measure Current chan 479
- Continuing Ppmu Measure Current chan 495
- Continuing Ppmu Measure Current chan 511
- Starting Ppmu Measure Int 200na
- Continuing Ppmu Measure Current chan 463
- Continuing Ppmu Measure Current chan 479
- Continuing Ppmu Measure Current chan 495
- Continuing Ppmu Measure Current chan 511
- Finished Channel Calibration

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Channel\_Board\_DIB Calibration of slot 7 (C00523D) at 6:50:18 PM

%JOB\_START - Beginning DPS\_DIB Calibration test on slot 21 at 6:50:24 PM on 2/29/2020 Workbook Rev V7.30.12\_0835\_MOUT IG-XL Version: 3.40.13 DIB # 305253E Rev 1137A

- Starting DPS Calibration on slot 21
- Calibrating DPS Voltage on slot 21
- Calibrating DPS Current Limit on slot 21
- Calibrating DPS Current Measure (50uA Range) on slot 21
- Calibrating DPS Current Measure (500uA Range) on slot 21
- Calibrating DPS Current Measure (10mA Range) on slot 21
- Calibrating DPS Current Measure (100mA Range) on slot 21
- Calibrating DPS Current Measure (1A Range) on slot 21
- Finished DPS Calibration on slot 21

%JOB\_END - \*\*\*\*PASSED\*\*\*\* DPS\_DIB Calibration of slot 21 (5009CD5) at 6:50:38 PM %JOB\_START - Beginning DPS\_DIB Calibration test on slot 22 at 6:50:44 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT IG-XL Version: 3.40.13 DIB # 305253E Rev 1137A

- Starting DPS Calibration on slot 22
- Calibrating DPS Voltage on slot 22
- Calibrating DPS Current Limit on slot 22
- Calibrating DPS Current Measure (50uA Range) on slot 22
- Calibrating DPS Current Measure (500uA Range) on slot 22
- Calibrating DPS Current Measure (10mA Range) on slot 22
- Calibrating DPS Current Measure (100mA Range) on slot 22
- Calibrating DPS Current Measure (1A Range) on slot 22
- Finished DPS Calibration on slot 22

%JOB\_END - \*\*\*\*PASSED\*\*\*\* DPS\_DIB Calibration of slot 22 (800E4C6) at 6:50:59 PM

%JOB\_START - Beginning DPS\_DIB Calibration test on slot 23 at 6:51:04 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT IG-XL Version: 3.40.13 DIB # 305253E

Rev 1137A

- Starting DPS Calibration on slot 23
- Calibrating DPS Voltage on slot 23
- Calibrating DPS Current Limit on slot 23
- Calibrating DPS Current Measure (50uA Range) on slot 23
- Calibrating DPS Current Measure (500uA Range) on slot 23
- Calibrating DPS Current Measure (10mA Range) on slot 23
- Calibrating DPS Current Measure (100mA Range) on slot 23
- Calibrating DPS Current Measure (1A Range) on slot 23
- Finished DPS Calibration on slot 23

%JOB\_END - \*\*\*\*PASSED\*\*\*\* DPS\_DIB Calibration of slot 23 (500B23A) at 6:51:19 PM %JOB\_START - Beginning DPS\_DIB Calibration test on slot 24 at 6:51:25 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT IG-XL Version: 3.40.13 DIB # 305253E Rev 1137A

- Starting DPS Calibration on slot 24
- Calibrating DPS Voltage on slot 24
- Calibrating DPS Current Limit on slot 24
- Calibrating DPS Current Measure (50uA Range) on slot 24
- Calibrating DPS Current Measure (500uA Range) on slot 24
- Calibrating DPS Current Measure (10mA Range) on slot 24
- Calibrating DPS Current Measure (100mA Range) on slot 24
- Calibrating DPS Current Measure (1A Range) on slot 24
- Finished DPS Calibration on slot 24

%JOB\_END - \*\*\*\*PASSED\*\*\*\* DPS\_DIB Calibration of slot 24 (5009DD2) at 6:51:39 PM

%JOB\_START - Beginning CTO\_DIB Calibration test on slot 17 at 6:51:45 PM on 2/29/2020 Workbook Rev V7.30.12\_0835\_MOUT IG-XL Version: 3.40.13 DIB # 305253E Rev 1137A

- Performing CTO Voltage Reference Calibration
- Performing CTO PPMU Force Voltage Calibration
- Performing CTO PPMU Measure Voltage Calibration
- Performing CTO PPMU Measure and Force Current Calibration on channel 0
- Performing CTO PPMU Measure and Force Current Calibration on channel 1
- Performing CTO PPMU Measure and Force Current Calibration on channel 2
- Performing CTO PPMU Measure and Force Current Calibration on channel 3
- Performing CTO PPMU Measure and Force Current Calibration on channel 4
- Performing CTO PPMU Measure and Force Current Calibration on channel 5
- Performing CTO PPMU Measure and Force Current Calibration on channel 6
- Performing CTO PPMU Measure and Force Current Calibration on channel 7

%JOB\_END - \*\*\*\*PASSED\*\*\*\* CTO\_DIB Calibration of slot 17 (C398747) at 6:53:16 PM

%JOB\_START - Beginning AC Calibration at 6:53:21 PM on 2/29/2020 in High Accuracy Mode Workbook Rev V7.30.12\_0835\_MOUT IG-XL Version: 3.40.13 DIB # 305253E

## Rev 1137A

- Beginning Digital Channel Timing Calibration
- Building List of Digital Channels
- Checking CalDIB AC Continuity for all Digital Channels
- Calibrating Super Linear Interpolator (SLI)
- Measuring Cub Round Trip Delay
- Measuring RF Matrix Delay
- Calibrating Drive Edges
- Calibrating Receive Edges
- Calibrating Receive Window
- Calibrating Mux Drive
- Calibrating Receive Mux
- Completed Digital Channel Timing Calibration

## %JOB\_END - \*\*\*\*PASSED\*\*\*\* AC Calibration at 7:30:01 PM

%JOB\_START - Beginning Channel\_Board Performance Verification test on slot 0 at 7:30:07 PM on 2/29/2020

- Starting dib\_test

Rev 1137A

- Temperature at PE Ch00 is 56 deg C
- Temperature at PE Ch60 is 40 deg C
- Temperature at Incoming Air is 26 deg C
- Temperature at TG Ch00 is 44 deg C
- Performing VIH/VIL level tests... at DGS=0mV
- Performing IOH/IOL level tests...
- Performing VT level tests...
- Starting Channel Comparator Performance Verification on slot 0
- Finished Channel Comparator Performance Verification on slot 0
- Performing Clamp level tests...
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests... DGS=0mV

- Starting BPMU Performance Verification on slot 0, DGS=-210.019 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 0, DGS= 9.560 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 0, DGS= 186.148 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Finished BPMU Performance Verification on slot 0
- Starting High Voltage DC Performance Verification on slot 0
- Finished High Voltage DC Performance Verification on slot 0

 $\% JOB\_END$  - \*\*\*\*PASSED\*\*\*\* Channel\_Board Performance Verification of slot 0 (500A534) at 7:38:31 PM

 $\mbox{\sc MJOB\_START}$  - Beginning Channel\_Board Performance Verification test on slot 1 at 7:38:37 PM on 2/29/2020

- Starting dib\_test
- Temperature at PE Ch00 is 53 deg C
- Temperature at PE Ch60 is 37 deg C

- Temperature at Incoming Air is  $25 \deg C$
- Temperature at TG Ch00 is 44 deg C
- Performing VIH/VIL level tests... at DGS=0mV
- Performing IOH/IOL level tests...
- Performing VT level tests...
- Starting Channel Comparator Performance Verification on slot 1
- Finished Channel Comparator Performance Verification on slot 1
- Performing Clamp level tests...
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests... DGS=0mV
- Starting BPMU Performance Verification on slot 1, DGS=-213.255 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 1, DGS= 6.364 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 1, DGS= 183.040 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Finished BPMU Performance Verification on slot 1
- Starting High Voltage DC Performance Verification on slot 1
- Finished High Voltage DC Performance Verification on slot 1

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Channel\_Board Performance Verification of slot 1 (5009E7C) at 7:47:02 PM

%JOB\_START - Beginning Channel\_Board Performance Verification test on slot 2 at 7:47:07 PM on 2/29/2020

- Starting dib\_test
- Temperature at PE Ch00 is 55 deg C
- Temperature at PE Ch60 is 38 deg C
- Temperature at Incoming Air is 25 deg C
- Temperature at TG Ch00 is 44 deg C
- Performing VIH/VIL level tests... at DGS=0mV
- Performing IOH/IOL level tests...
- Performing VT level tests...
- Starting Channel Comparator Performance Verification on slot 2
- Finished Channel Comparator Performance Verification on slot 2
- Performing Clamp level tests...
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests... DGS=0mV
- Starting BPMU Performance Verification on slot 2, DGS=-215.034 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 2, DGS= 4.594 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy

- Starting BPMU Performance Verification on slot 2, DGS= 181.065 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Finished BPMU Performance Verification on slot 2
- Starting High Voltage DC Performance Verification on slot 2
- Finished High Voltage DC Performance Verification on slot 2

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Channel\_Board Performance Verification of slot 2 (C00BA21) at 7:55:31 PM

 $\rm \%JOB\_START$  - Beginning Channel\_Board Performance Verification test on slot 3 at 7:55:37 PM on  $\rm 2/29/2020$ 

- Starting dib\_test
- Temperature at PE Ch00 is 52 deg C
- Temperature at PE Ch60 is 34 deg C
- Temperature at Incoming Air is 24 deg C
- Temperature at TG Ch00 is 42 deg C
- Performing VIH/VIL level tests... at DGS=0mV
- Performing IOH/IOL level tests...
- Performing VT level tests...
- Starting Channel Comparator Performance Verification on slot 3
- Finished Channel Comparator Performance Verification on slot 3
- Performing Clamp level tests...
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests... DGS=0mV
- Starting BPMU Performance Verification on slot 3, DGS=-222.688 mV
- Verifying BPMU Forced Voltage Accuracy

- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 3, DGS=- 3.148 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 3, DGS= 173.499 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Finished BPMU Performance Verification on slot 3
- Starting High Voltage DC Performance Verification on slot 3
- Finished High Voltage DC Performance Verification on slot 3

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Channel\_Board Performance Verification of slot 3 (802E71C) at 8:04:02 PM

%JOB\_START - Beginning Channel\_Board Performance Verification test on slot 4 at 8:04:07 PM on 2/29/2020

- Starting dib\_test
- Temperature at PE Ch00 is 52 deg C
- Temperature at PE Ch60 is 36 deg C
- Temperature at Incoming Air is 25 deg C
- Temperature at TG Ch00 is 44 deg C

- Performing VIH/VIL level tests... at DGS=0mV
- Performing IOH/IOL level tests...
- Performing VT level tests...
- Starting Channel Comparator Performance Verification on slot 4
- Finished Channel Comparator Performance Verification on slot 4
- Performing Clamp level tests...
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests... DGS=0mV
- Starting BPMU Performance Verification on slot 4, DGS=-218.006 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 4, DGS= 1.564 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 4, DGS= 178.622 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Finished BPMU Performance Verification on slot 4
- Starting High Voltage DC Performance Verification on slot 4
- Finished High Voltage DC Performance Verification on slot 4

 $\mbox{\sc MJOB\_START}$  - Beginning Channel\_Board Performance Verification test on slot 5 at 8:12:37 PM on 2/29/2020

- Starting dib\_test
- Temperature at PE Ch00 is 54 deg C
- Temperature at PE Ch60 is 38 deg C
- Temperature at Incoming Air is 25 deg C
- Temperature at TG Ch00 is 43 deg C
- Performing VIH/VIL level tests... at DGS=0mV
- Performing IOH/IOL level tests...
- Performing VT level tests...
- Starting Channel Comparator Performance Verification on slot 5
- Finished Channel Comparator Performance Verification on slot 5
- Performing Clamp level tests...
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests... DGS=0mV
- Starting BPMU Performance Verification on slot 5, DGS=-216.354 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 5, DGS= 3.343 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 5, DGS= 180.117 mV
- Verifying BPMU Forced Voltage Accuracy

- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Finished BPMU Performance Verification on slot 5
- Starting High Voltage DC Performance Verification on slot 5
- Finished High Voltage DC Performance Verification on slot 5

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Channel\_Board Performance Verification of slot 5 (C00B7FC) at 8:21:01 PM

 $\rm \%JOB\_START$  - Beginning Channel\_Board Performance Verification test on slot 6 at 8:21:06 PM on  $\rm 2/29/2020$ 

- Starting dib\_test
- Temperature at PE Ch00 is 49 deg C
- Temperature at PE Ch60 is 38 deg C
- Temperature at Incoming Air is 25 deg C
- Temperature at TG Ch00 is 43 deg C
- Performing VIH/VIL level tests... at DGS=0mV
- Performing IOH/IOL level tests...
- Performing VT level tests...
- Starting Channel Comparator Performance Verification on slot 6
- Finished Channel Comparator Performance Verification on slot 6
- Performing Clamp level tests...
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests... DGS=0mV
- Starting BPMU Performance Verification on slot 6, DGS=-216.842 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy

- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 6, DGS= 2.815 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 6, DGS= 179.765 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Finished BPMU Performance Verification on slot 6
- Starting High Voltage DC Performance Verification on slot 6
- Finished High Voltage DC Performance Verification on slot 6

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Channel\_Board Performance Verification of slot 6 (C0EB261) at 8:29:31 PM

 $\% JOB\_START$  - Beginning Channel\_Board Performance Verification test on slot 7 at 8:29:36 PM on 2/29/2020

- Starting dib\_test
- Temperature at PE Ch00 is 49 deg C
- Temperature at PE Ch60 is 39 deg C
- Temperature at Incoming Air is 25 deg C
- Temperature at TG Ch00 is 42 deg C
- Performing VIH/VIL level tests... at DGS=0mV
- Performing IOH/IOL level tests...

- Performing VT level tests...
- Starting Channel Comparator Performance Verification on slot 7
- Finished Channel Comparator Performance Verification on slot 7
- Performing Clamp level tests...
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests... DGS=0mV
- Starting BPMU Performance Verification on slot 7, DGS=-219.120 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 7, DGS= 772.238 uV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 7, DGS= 177.321 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Finished BPMU Performance Verification on slot 7
- Starting High Voltage DC Performance Verification on slot 7
- Finished High Voltage DC Performance Verification on slot 7

%JOB\_END - \*\*\*\*PASSED\*\*\*\* Channel\_Board Performance Verification of slot 7 (C00523D) at 8:38:01 PM

 $\mbox{\sc MJOB\_START}$  - Beginning DPS\_DIB Performance Verification test on slot 21 at 8:38:06 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT  $\,$  IG-XL Version: 3.40.13  $\,$  DIB # 305253E Rev 1137A

- Starting DPS Performance Verification on slot 21
- Verifying DPS Voltage Accuracy
- Verifying DPS Current Limit Accuracy
- Verifying DPS Current Measure Accuracy
- Channel 0
- Channel 1
- Channel 2
- Channel 3
- Channel 4
- Channel 5
- Channel 6
- Channel 7
- Verifying DPS DIB MOUT Output Impedance Test
- Verifying DPS DIB Current Measure Output Accuracy
- Finished DPS Performance Verification on slot 21

%JOB\_END - \*\*\*\*PASSED\*\*\*\* DPS\_DIB Performance Verification of slot 21 (5009CD5) at 8:39:00 PM

 $\% JOB\_START$  - Beginning DPS\_DIB Performance Verification test on slot 22 at 8:39:05 PM on 2/29/2020

- Starting DPS Performance Verification on slot 22
- Verifying DPS Voltage Accuracy
- Verifying DPS Current Limit Accuracy
- Verifying DPS Current Measure Accuracy

- Channel 0
- Channel 1
- Channel 2
- Channel 3
- Channel 4
- Channel 5
- Channel 6
- Channel 7
- Verifying DPS DIB MOUT Output Impedance Test
- Verifying DPS DIB Current Measure Output Accuracy
- Finished DPS Performance Verification on slot 22

 $\% JOB\_END$  - \*\*\*\*PASSED\*\*\*\* DPS\_DIB Performance Verification of slot 22 (800E4C6) at 8:39:58 PM

 $\% JOB\_START$  - Beginning DPS\_DIB Performance Verification test on slot 23 at 8:40:04 PM on 2/29/2020

- Starting DPS Performance Verification on slot 23
- Verifying DPS Voltage Accuracy
- Verifying DPS Current Limit Accuracy
- Verifying DPS Current Measure Accuracy
- Channel 0
- Channel 1
- Channel 2
- Channel 3
- Channel 4
- Channel 5
- Channel 6
- Channel 7
- Verifying DPS DIB MOUT Output Impedance Test

- Verifying DPS DIB Current Measure Output Accuracy
- Finished DPS Performance Verification on slot 23

%JOB\_END - \*\*\*\*PASSED\*\*\*\* DPS\_DIB Performance Verification of slot 23 (500B23A) at 8:40:57 PM

 $\mbox{\ensuremath{\mbox{MOB\_START}}}$  - Beginning DPS\_DIB Performance Verification test on slot 24 at 8:41:03 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT  $\,$  IG-XL Version: 3.40.13  $\,$  DIB # 305253E Rev 1137A

- Starting DPS Performance Verification on slot 24
- Verifying DPS Voltage Accuracy
- Verifying DPS Current Limit Accuracy
- Verifying DPS Current Measure Accuracy
- Channel 0
- Channel 1
- Channel 2
- Channel 3
- Channel 4
- Channel 5
- Channel 6
- Channel 7
- Verifying DPS DIB MOUT Output Impedance Test
- Verifying DPS DIB Current Measure Output Accuracy
- Finished DPS Performance Verification on slot 24

 $\% JOB\_END$  - \*\*\*\*PASSED\*\*\*\* DPS\_DIB Performance Verification of slot 24 (5009DD2) at 8:41:56 PM

 $\% JOB\_START$  - Beginning CTO\_DIB Performance Verification test on slot 17 at 8:42:01 PM on 2/29/2020

Workbook Rev V7.30.12\_0835\_MOUT  $\,$  IG-XL Version: 3.40.13  $\,$  DIB # 305253E Rev 1137A

- Performing source verification...
- Performing capture verification...
- Performing VREF verification...
- Performing PPMU Force V verification...
- Performing PPMU Measure V verification...
- Performing PPMU Force I verification...
- Performing PPMU Measure I verification...

%JOB\_END - \*\*\*\*PASSED\*\*\*\* CTO\_DIB Performance Verification of slot 17 (C398747) at 8:43:08 PM

 $\mbox{\sc Mode}$  - Beginning AC Performance Verification at 8:43:13 PM on 2/29/2020 in High Accuracy Mode

- Beginning Digital Channel Timing Performance Verification
- Started at 2/29/2020 8:43:13 PM
- Building List of Digital Channels
- Checking CalDIB AC Continuity for all Digital Channels
- Calibrating Super Linear Interpolator (SLI)
- Calibrating user DIB RTD
- Loading drive deskew registers
- Loading receive deskew registers
- Beginning of Drive Skew Test
- Acquiring Data for Slot 0
- Acquiring Data for Slot 1
- Acquiring Data for Slot 2
- Acquiring Data for Slot 3
- Acquiring Data for Slot 4

- Acquiring Data for Slot 5
- Acquiring Data for Slot 6
- Acquiring Data for Slot 7
- End of Drive Skew Test
- Beginning of Receive Skew Test
- Acquiring Data for Slot 0
- Acquiring Data for Slot 1
- Acquiring Data for Slot 2
- Acquiring Data for Slot 3
- Acquiring Data for Slot 4
- Acquiring Data for Slot 5
- Acquiring Data for Slot 6
- Acquiring Data for Slot 7
- End of Receive Skew Test
- Beginning of Drive Linearity Test
- Acquiring Data for Slot 0
- Acquiring Data for Slot 1
- Acquiring Data for Slot 2
- Acquiring Data for Slot 3
- Acquiring Data for Slot 4
- Acquiring Data for Slot 5
- Acquiring Data for Slot 6
- Acquiring Data for Slot 7
- End of Drive Linearity Test
- Beginning of Receive Linearity Test
- Acquiring Data for Slot 0
- Acquiring Data for Slot 1
- Acquiring Data for Slot 2
- Acquiring Data for Slot 3
- Acquiring Data for Slot 4
- Acquiring Data for Slot 5
- Acquiring Data for Slot 6
- Acquiring Data for Slot 7
- End of Receive Linearity Test

- Adding up error budget
- Completed Digital Channel Timing Performance Verification
- Finished at 2/29/2020 9:25:26 PM

 $\rm \%JOB\_END$  - \*\*\*\*PASSED\*\*\*\* AC Performance Verification at 9:25:26 PM

## Slot17\_CTO\_ExternalCal

- %JOB\_START Beginning CTO\_DIB External Calibration test on slot 17 at 4:51:11 PM on 2/29/2020 Workbook Rev V7.30.12\_0835\_MOUT IG-XL Version: 3.40.13 DIB # 305253E Rev 1137A
  - %PASS Slot 17 channel 0 3V Source offset in mV Measured: 0.7103 low limit: -50 high limit: 50
  - %PASS Slot 17 channel 0 3V Source gain in mV Measured: 0.9996 low limit: 0.98 high limit: 1.02
  - %PASS Slot 17 channel 1 3V Source offset in mV Measured: 0.1899 low limit: -50 high limit: 50
  - %PASS Slot 17 channel 1 3V Source gain in mV Measured: 0.9998 low limit: 0.98 high limit: 1.02
  - %PASS Slot 17 channel 2 3V Source offset in mV Measured: -4.654E-02 low limit: -50 high limit: 50
  - %PASS Slot 17 channel 2 3V Source gain in mV Measured: 0.9999 low limit: 0.98 high limit: 1.02
  - %PASS Slot 17 channel 3 3V Source offset in mV Measured: 0.3791 low limit: -50 high limit: 50
  - %PASS Slot 17 channel 3 3V Source gain in mV Measured: 0.9999 low limit: 0.98 high limit: 1.02
  - %PASS Slot 17 channel 4 3V Source offset in mV Measured: 0.5683 low limit: -50 high limit: 50
  - %PASS Slot 17 channel 4 3V Source gain in mV Measured: 1.000 low limit: 0.98 high limit: 1.02
  - %PASS Slot 17 channel 5 3V Source offset in mV Measured: 0.1426 low limit: -50 high limit: 50

- %PASS Slot 17 channel 5 3V Source gain in mV Measured: 0.9997 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 6 3V Source offset in mV Measured: 1.467 low limit: -50 high limit: 50
- %PASS Slot 17 channel 6 3V Source gain in mV Measured: 0.9995 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 7 3V Source offset in mV Measured: 0.9468 low limit: -50 high limit: 50
- %PASS Slot 17 channel 7 3V Source gain in mV Measured: 0.9995 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 0 6V Source offset in mV Measured: 1.326 low limit: -100 high limit: 100
- %PASS Slot 17 channel 0 6V Source gain in mV Measured: 0.9996 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 1 6V Source offset in mV Measured: 0.7583 low limit: -100 high limit: 100
- %PASS Slot 17 channel 1 6V Source gain in mV Measured: 0.9997 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 2 6V Source offset in mV Measured: 1.525E-03 low limit: -100 high limit: 100
- %PASS Slot 17 channel 2 6V Source gain in mV Measured: 0.9999 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 3 6V Source offset in mV Measured: 0.9475 low limit: -100 high limit: 100
- %PASS Slot 17 channel 3 6V Source gain in mV

- Measured: 0.9998 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 4 6V Source offset in mV Measured: 0.7583 low limit: -100 high limit: 100
- %PASS Slot 17 channel 4 6V Source gain in mV Measured: 1.000 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 5 6V Source offset in mV Measured: 0.2853 low limit: -100 high limit: 100
- %PASS Slot 17 channel 5 6V Source gain in mV Measured: 0.9997 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 6 6V Source offset in mV Measured: 2.082 low limit: -100 high limit: 100
- %PASS Slot 17 channel 6 6V Source gain in mV Measured: 0.9995 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 7 6V Source offset in mV Measured: 1.799 low limit: -100 high limit: 100
- %PASS Slot 17 channel 7 6V Source gain in mV Measured: 0.9994 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 0 at 0V on 3V range

  Measured: -0.00007341 low limit: -0.05 high limit: 0.05
- %PASS Slot 17 channel 0 at 0V on 3V range Measured: 1.221E-05 low limit: -0.05 high limit: 0.05
- %PASS Slot 17 channel 0 at 3V on 3V range Measured: 2.999 low limit: 2.95 high limit: 3.05
- %PASS Slot 17 channel 0 at 3V on 3V range Measured: 3.000 low limit: 2.95 high limit: 3.05

- %PASS Slot 17 channel 0 at 0V on 6V range
  Measured: -0.0001754 low limit: -0.1 high limit: 0.1
- %PASS Slot 17 channel 0 at 0V on 6V range Measured: -7.473E-05 low limit: -0.1 high limit: 0.1
- %PASS Slot 17 channel 0 at 6V on 6V range Measured: 5.999 low limit: 5.9 high limit: 6.1
- %PASS Slot 17 channel 0 at 6V on 6V range Measured: 6.000 low limit: 5.9 high limit: 6.1
- %PASS Slot 17 channel 1 at 0V on 3V range

  Measured: -0.00008546 low limit: -0.05 high limit: 0.05
- %PASS Slot 17 channel 1 at 0V on 3V range Measured: -3.700E-06 low limit: -0.05 high limit: 0.05
- %PASS Slot 17 channel 1 at 3V on 3V range Measured: 2.999 low limit: 2.95 high limit: 3.05
- %PASS Slot 17 channel 1 at 3V on 3V range Measured: 3.000 low limit: 2.95 high limit: 3.05
- %PASS Slot 17 channel 1 at 0V on 6V range
  Measured: -0.0001733 low limit: -0.1 high limit: 0.1
- %PASS Slot 17 channel 1 at 0V on 6V range  $\label{eq:measured: -7.475E-05 low limit: 0.1 high limit: 0.1}$
- %PASS Slot 17 channel 1 at 6V on 6V range Measured: 5.999 low limit: 5.9 high limit: 6.1
- %PASS Slot 17 channel 1 at 6V on 6V range Measured: 6.000 low limit: 5.9 high limit: 6.1
- %PASS Slot 17 channel 2 at 0V on 3V range Measured: -0.0001362 low limit: -0.05 high limit: 0.05

- %PASS Slot 17 channel 2 at 0V on 3V range Measured: -5.404E-05 low limit: -0.05 high limit: 0.05
- %PASS Slot 17 channel 2 at 3V on 3V range Measured: 2.999 low limit: 2.95 high limit: 3.05
- %PASS Slot 17 channel 2 at 3V on 3V range Measured: 3.000 low limit: 2.95 high limit: 3.05
- %PASS Slot 17 channel 2 at 0V on 6V range Measured: -0.0001492 low limit: -0.1 high limit: 0.1
- %PASS Slot 17 channel 2 at 0V on 6V range
  Measured: -6.736E-05 low limit: -0.1 high limit: 0.1
- %PASS Slot 17 channel 2 at 6V on 6V range Measured: 5.999 low limit: 5.9 high limit: 6.1
- %PASS Slot 17 channel 2 at 6V on 6V range Measured: 6.000 low limit: 5.9 high limit: 6.1
- %PASS Slot 17 channel 3 at 0V on 3V range
  Measured: -0.00007604 low limit: -0.05 high limit: 0.05
- %PASS Slot 17 channel 3 at 0V on 3V range Measured: 2.219E-05 low limit: -0.05 high limit: 0.05
- %PASS Slot 17 channel 3 at 3V on 3V range Measured: 2.999 low limit: 2.95 high limit: 3.05
- %PASS Slot 17 channel 3 at 3V on 3V range Measured: 3.000 low limit: 2.95 high limit: 3.05
- %PASS Slot 17 channel 3 at 0V on 6V range Measured: -0.0001702 low limit: -0.1 high limit: 0.1
- $\mbox{\%PASS}$  Slot 17 channel 3 at 0V on 6V range

- Measured: -8.064E-05 low limit: -0.1 high limit: 0.1
- %PASS Slot 17 channel 3 at 6V on 6V range Measured: 5.999 low limit: 5.9 high limit: 6.1
- %PASS Slot 17 channel 3 at 6V on 6V range Measured: 6.000 low limit: 5.9 high limit: 6.1
- %PASS Slot 17 channel 4 at 0V on 3V range
  Measured: -0.0001139 low limit: -0.05 high limit: 0.05
- %PASS Slot 17 channel 4 at 0V on 3V range
  Measured: -2.665E-05 low limit: -0.05 high limit: 0.05
- %PASS Slot 17 channel 4 at 3V on 3V range Measured: 2.999 low limit: 2.95 high limit: 3.05
- %PASS Slot 17 channel 4 at 3V on 3V range Measured: 3.000 low limit: 2.95 high limit: 3.05
- %PASS Slot 17 channel 4 at 0V on 6V range
  Measured: -0.0001600 low limit: -0.1 high limit: 0.1
- %PASS Slot 17 channel 4 at 0V on 6V range Measured: -9.697E-05 low limit: -0.1 high limit: 0.1
- %PASS Slot 17 channel 4 at 6V on 6V range Measured: 5.999 low limit: 5.9 high limit: 6.1
- %PASS Slot 17 channel 4 at 6V on 6V range Measured: 6.000 low limit: 5.9 high limit: 6.1
- %PASS Slot 17 channel 5 at 0V on 3V range Measured: -0.00009133 low limit: -0.05 high limit: 0.05
- %PASS Slot 17 channel 5 at 0V on 3V range Measured: 9.989E-06 low limit: -0.05 high limit: 0.05

- %PASS Slot 17 channel 5 at 3V on 3V range Measured: 2.999 low limit: 2.95 high limit: 3.05
- %PASS Slot 17 channel 5 at 3V on 3V range Measured: 3.000 low limit: 2.95 high limit: 3.05
- %PASS Slot 17 channel 5 at 0V on 6V range
  Measured: -0.0001152 low limit: -0.1 high limit: 0.1
- %PASS Slot 17 channel 5 at 0V on 6V range Measured: -6.954E-05 low limit: -0.1 high limit: 0.1
- %PASS Slot 17 channel 5 at 6V on 6V range Measured: 5.999 low limit: 5.9 high limit: 6.1
- %PASS Slot 17 channel 5 at 6V on 6V range Measured: 6.000 low limit: 5.9 high limit: 6.1
- %PASS Slot 17 channel 6 at 0V on 3V range
  Measured: -0.0001127 low limit: -0.05 high limit: 0.05
- %PASS Slot 17 channel 6 at 0V on 3V range Measured: 2.627E-05 low limit: -0.05 high limit: 0.05
- %PASS Slot 17 channel 6 at 3V on 3V range Measured: 2.999 low limit: 2.95 high limit: 3.05
- %PASS Slot 17 channel 6 at 3V on 3V range Measured: 3.000 low limit: 2.95 high limit: 3.05
- %PASS Slot 17 channel 6 at 0V on 6V range Measured: -0.00007946 low limit: -0.1 high limit: 0.1
- %PASS Slot 17 channel 6 at 0V on 6V range Measured: 2.812E-05 low limit: -0.1 high limit: 0.1
- %PASS Slot 17 channel 6 at 6V on 6V range Measured: 5.999 low limit: 5.9 high limit: 6.1

- %PASS Slot 17 channel 6 at 6V on 6V range Measured: 6.000 low limit: 5.9 high limit: 6.1
- %PASS Slot 17 channel 7 at 0V on 3V range
  Measured: -0.00009021 low limit: -0.05 high limit: 0.05
- %PASS Slot 17 channel 7 at 0V on 3V range Measured: 2.219E-05 low limit: -0.05 high limit: 0.05
- %PASS Slot 17 channel 7 at 3V on 3V range Measured: 2.999 low limit: 2.95 high limit: 3.05
- %PASS Slot 17 channel 7 at 3V on 3V range Measured: 3.000 low limit: 2.95 high limit: 3.05
- %PASS Slot 17 channel 7 at 0V on 6V range

  Measured: -0.0001291 low limit: -0.1 high limit: 0.1
- %PASS Slot 17 channel 7 at 0V on 6V range Measured: -6.658E-06 low limit: -0.1 high limit: 0.1
- %PASS Slot 17 channel 7 at 6V on 6V range Measured: 5.999 low limit: 5.9 high limit: 6.1
- %PASS Slot 17 channel 7 at 6V on 6V range Measured: 6.000 low limit: 5.9 high limit: 6.1

%JOB\_END - \*\*\*\*PASSED\*\*\*\* CTO\_DIB External Calibration of slot 17 (C398747) at 4:51:54 PM

## $Slot17\_CTO\_ExternalPV$

- $\% JOB\_START$  Beginning CTO\_DIB External Verification test on slot 17 at 4:52:17 PM on 2/29/2020
- Workbook Rev V7.30.12\_0835\_MOUT  $\,$  IG-XL Version: 3.40.13  $\,$  DIB # 305253E Rev 1137A
  - Performing source and capture verification...
  - %PASS Slot 17 channel 0 3V Source offset in mV Measured: 0.6368 low limit: -50 high limit: 50
  - %PASS Slot 17 channel 0 3V Source gain in mV Measured: 0.9996 low limit: 0.98 high limit: 1.02
  - %PASS Slot 17 channel 1 3V Source offset in mV Measured: 0.1045 low limit: -50 high limit: 50
  - %PASS Slot 17 channel 1 3V Source gain in mV Measured: 0.9997 low limit: 0.98 high limit: 1.02
  - %PASS Slot 17 channel 2 3V Source offset in mV Measured: -0.1827 low limit: -50 high limit: 50
  - %PASS Slot 17 channel 2 3V Source gain in mV Measured: 0.9999 low limit: 0.98 high limit: 1.02
  - %PASS Slot 17 channel 3 3V Source offset in mV Measured: 0.3031 low limit: -50 high limit: 50
  - %PASS Slot 17 channel 3 3V Source gain in mV Measured: 0.9999 low limit: 0.98 high limit: 1.02
  - %PASS Slot 17 channel 4 3V Source offset in mV Measured: 0.4544 low limit: -50 high limit: 50
  - %PASS Slot 17 channel 4 3V Source gain in mV Measured: 1.000 low limit: 0.98 high limit: 1.02

- %PASS Slot 17 channel 5 3V Source offset in mV Measured: 5.134E-02 low limit: -50 high limit: 50
- %PASS Slot 17 channel 5 3V Source gain in mV Measured: 0.9997 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 6 3V Source offset in mV Measured: 1.354 low limit: -50 high limit: 50
- %PASS Slot 17 channel 6 3V Source gain in mV Measured: 0.9995 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 7 3V Source offset in mV Measured: 0.8566 low limit: -50 high limit: 50
- %PASS Slot 17 channel 7 3V Source gain in mV Measured: 0.9995 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 0 6V Source offset in mV Measured: 1.150 low limit: -100 high limit: 100
- %PASS Slot 17 channel 0 6V Source gain in mV Measured: 0.9995 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 1 6V Source offset in mV Measured: 0.5850 low limit: -100 high limit: 100
- %PASS Slot 17 channel 1 6V Source gain in mV Measured: 0.9997 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 2 6V Source offset in mV Measured: -0.1477 low limit: -100 high limit: 100
- %PASS Slot 17 channel 2 6V Source gain in mV Measured: 0.9999 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 3 6V Source offset in mV Measured: 0.7773 low limit: -100 high limit: 100

- %PASS Slot 17 channel 3 6V Source gain in mV Measured: 0.9998 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 4 6V Source offset in mV Measured: 0.5983 low limit: -100 high limit: 100
- %PASS Slot 17 channel 4 6V Source gain in mV Measured: 1.000 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 5 6V Source offset in mV Measured: 0.1700 low limit: -100 high limit: 100
- %PASS Slot 17 channel 5 6V Source gain in mV Measured: 0.9996 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 6 6V Source offset in mV Measured: 2.003 low limit: -100 high limit: 100
- %PASS Slot 17 channel 6 6V Source gain in mV Measured: 0.9995 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 7 6V Source offset in mV Measured: 1.669 low limit: -100 high limit: 100
- %PASS Slot 17 channel 7 6V Source gain in mV Measured: 0.9994 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 0 source accuracy at 0V on 3V range Measured: 0.00002176773V low limit: -0.00018V high limit: 0.00018V
- %PASS Slot 17 channel 0 acquire accuracy at 0.00002176773644V on 3V range

  Measured: 1.453982E-05V low limit: -0.0001582322V high limit: 0.0002017677V
- %PASS Slot 17 channel 0 acquire accuracy at 0.5000252421V on 3V range

- Measured: 0.5000891V low limit: 0.4998452V high limit: 0.5002052V
- %PASS Slot 17 channel 0 source accuracy at 1V on 3V range Measured: 1.000024V low limit: 0.99982V high limit: 1.00018V
- %PASS Slot 17 channel 0 acquire accuracy at 1.000024031V on 3V range Measured: 1.000069V low limit: 0.9998440V high limit: 1.000204V
- %PASS Slot 17 channel 0 source accuracy at 1.5V on 3V range Measured: 1.499992V low limit: 1.49982V high limit: 1.50018V
- %PASS Slot 17 channel 0 acquire accuracy at 1.499992438V on 3V range Measured: 1.499995V low limit: 1.499812V high limit: 1.500172V
- %PASS Slot 17 channel 0 source accuracy at 2V on 3V range Measured: 2.000054V low limit: 1.99982V high limit: 2.00018V
- %PASS Slot 17 channel 0 acquire accuracy at 2.000054631V on 3V range Measured: 2.000008V low limit: 1.999874V high limit: 2.000234V
- %PASS Slot 17 channel 0 source accuracy at 2.5V on 3V range Measured: 2.500027V low limit: 2.49982V high limit: 2.50018V
- %PASS Slot 17 channel 0 acquire accuracy at 2.50002781V on 3V range Measured: 2.499952V low limit: 2.499847V high limit: 2.500207V
- %PASS Slot 17 channel 0 source accuracy at 3V on 3V range Measured: 2.999999V low limit: 2.99982V high limit: 3.00018V
- %PASS Slot 17 channel 0 acquire accuracy at 2.999999194V on 3V range Measured: 2.999985V low limit: 2.999819V high limit: 3.000179V
- %PASS Slot 17 channel 0 source accuracy at 0V on 6V range Measured: 0.00002317519V low limit: -0.00036V high limit: 0.00036V
- %PASS Slot 17 channel 0 acquire accuracy at 0.00002317519732V on 6V range

  Measured: 3.825139E-05V low limit: -0.0003368248V high limit: 0.0003831751V

- %PASS Slot 17 channel 0 source accuracy at 1V on 6V range Measured: 1.000076V low limit: 0.99964V high limit: 1.00036V
- %PASS Slot 17 channel 0 acquire accuracy at 1.000076704V on 6V range Measured: 1.000224V low limit: 0.9997167V high limit: 1.000436V
- %PASS Slot 17 channel 0 source accuracy at 2V on 6V range Measured: 2.000017V low limit: 1.99964V high limit: 2.00036V
- %PASS Slot 17 channel 0 acquire accuracy at 2.000017502V on 6V range Measured: 2.000191V low limit: 1.999657V high limit: 2.000377V
- %PASS Slot 17 channel 0 source accuracy at 3V on 6V range Measured: 2.999969V low limit: 2.99964V high limit: 3.00036V
- %PASS Slot 17 channel 0 acquire accuracy at 2.99996999V on 6V range Measured: 3.000011V low limit: 2.999609V high limit: 3.000329V
- %PASS Slot 17 channel 0 source accuracy at 4V on 6V range Measured: 4.000135V low limit: 3.99964V high limit: 4.00036V
- %PASS Slot 17 channel 0 acquire accuracy at 4.000135882V on 6V range Measured: 4.000138V low limit: 3.999775V high limit: 4.000495V
- %PASS Slot 17 channel 0 source accuracy at 5V on 6V range Measured: 5.000007V low limit: 4.99964V high limit: 5.00036V
- %PASS Slot 17 channel 0 acquire accuracy at 5.000007895V on 6V range Measured: 4.999910V low limit: 4.999647V high limit: 5.000367V
- %PASS Slot 17 channel 0 source accuracy at 6V on 6V range Measured: 6.000014V low limit: 5.99964V high limit: 6.00036V
- %PASS Slot 17 channel 0 acquire accuracy at 6.000014807V on 6V range Measured: 5.999983V low limit: 5.999654V high limit: 6.000374V
- %PASS Slot 17 channel 1 source accuracy at 0V on 3V range Measured: -0.000001669965V low limit: -0.00018V high limit: 0.00018V

- %PASS Slot 17 channel 1 acquire accuracy at -0.000001669965229V on 3V range Measured: 7.677679E-06V low limit: -0.0001816699V high limit: 0.0001783300V
- %PASS Slot 17 channel 1 source accuracy at 0.5V on 3V range Measured: 0.5000576V low limit: 0.49982V high limit: 0.50018V
- %PASS Slot 17 channel 1 acquire accuracy at 0.5000576097V on 3V range Measured: 0.5001335V low limit: 0.4998776V high limit: 0.5002376V
- %PASS Slot 17 channel 1 source accuracy at 1V on 3V range Measured: 1.000049V low limit: 0.99982V high limit: 1.00018V
- %PASS Slot 17 channel 1 acquire accuracy at 1.000049383V on 3V range Measured: 1.000061V low limit: 0.9998693V high limit: 1.000229V
- %PASS Slot 17 channel 1 source accuracy at 1.5V on 3V range Measured: 1.500017V low limit: 1.49982V high limit: 1.50018V
- %PASS Slot 17 channel 1 acquire accuracy at 1.500017964V on 3V range Measured: 1.500038V low limit: 1.499837V high limit: 1.500197V
- %PASS Slot 17 channel 1 source accuracy at 2V on 3V range Measured: 2.00012V low limit: 1.99982V high limit: 2.00018V
- %PASS Slot 17 channel 1 acquire accuracy at 2.00012V on 3V range Measured: 2.000108V low limit: 1.99994V high limit: 2.0003V
- %PASS Slot 17 channel 1 source accuracy at 2.5V on 3V range Measured: 2.500082V low limit: 2.49982V high limit: 2.50018V
- %PASS Slot 17 channel 1 acquire accuracy at 2.500082628V on 3V range Measured: 2.500018V low limit: 2.499902V high limit: 2.500262V
- %PASS Slot 17 channel 1 source accuracy at 3V on 3V range Measured: 3.000085V low limit: 2.99982V high limit: 3.00018V
- %PASS Slot 17 channel 1 acquire accuracy at 3.000085055V on 3V range

- Measured: 3.000061V low limit: 2.999905V high limit: 3.000265V
- %PASS Slot 17 channel 1 source accuracy at 0V on 6V range Measured: 0.000007640626V low limit: -0.00036V high limit: 0.00036V
- %PASS Slot 17 channel 1 acquire accuracy at 0.00000764062687V on 6V range

  Measured: 2.931365E-05V low limit: -0.0003523593V high limit: 0.0003676406V
- %PASS Slot 17 channel 1 source accuracy at 1V on 6V range
  Measured: 1.000001V low limit: 0.99964V high limit: 1.00036V
- %PASS Slot 17 channel 1 acquire accuracy at 1.000001439V on 6V range Measured: 1.000128V low limit: 0.9996414V high limit: 1.000361V
- %PASS Slot 17 channel 1 source accuracy at 2V on 6V range Measured: 1.999975V low limit: 1.99964V high limit: 2.00036V
- %PASS Slot 17 channel 1 acquire accuracy at 1.999975294V on 6V range Measured: 2.000078V low limit: 1.999615V high limit: 2.000335V
- %PASS Slot 17 channel 1 source accuracy at 3V on 6V range Measured: 2.999902V low limit: 2.99964V high limit: 3.00036V
- %PASS Slot 17 channel 1 acquire accuracy at 2.999902299V on 6V range Measured: 2.999988V low limit: 2.999542V high limit: 3.000262V
- %PASS Slot 17 channel 1 source accuracy at 4V on 6V range Measured: 4.000102V low limit: 3.99964V high limit: 4.00036V
- %PASS Slot 17 channel 1 acquire accuracy at 4.00010265V on 6V range Measured: 4.000199V low limit: 3.999742V high limit: 4.000462V
- %PASS Slot 17 channel 1 source accuracy at 5V on 6V range Measured: 5.000043V low limit: 4.99964V high limit: 5.00036V
- %PASS Slot 17 channel 1 acquire accuracy at 5.000043535V on 6V range Measured: 5.000019V low limit: 4.999683V high limit: 5.000403V

- %PASS Slot 17 channel 1 source accuracy at 6V on 6V range Measured: 5.999967V low limit: 5.99964V high limit: 6.00036V
- %PASS Slot 17 channel 1 acquire accuracy at 5.999967301V on 6V range Measured: 6.000029V low limit: 5.999607V high limit: 6.000327V
- %PASS Slot 17 channel 2 source accuracy at 0V on 3V range Measured: 0.000001795529V low limit: -0.00018V high limit: 0.00018V
- %PASS Slot 17 channel 2 acquire accuracy at 0.000001795529814V on 3V range Measured: 2.462632E-05V low limit: -0.0001782044V high limit: 0.0001817955V
- %PASS Slot 17 channel 2 source accuracy at 0.5V on 3V range Measured: 0.5000232V low limit: 0.49982V high limit: 0.50018V
- %PASS Slot 17 channel 2 acquire accuracy at 0.500023284V on 3V range Measured: 0.5000680V low limit: 0.4998432V high limit: 0.5002032V
- %PASS Slot 17 channel 2 source accuracy at 1V on 3V range Measured: 0.9999990V low limit: 0.99982V high limit: 1.00018V
- %PASS Slot 17 channel 2 acquire accuracy at 0.9999990657V on 3V range Measured: 1.000055V low limit: 0.9998190V high limit: 1.000179V
- %PASS Slot 17 channel 2 source accuracy at 1.5V on 3V range Measured: 1.500009V low limit: 1.49982V high limit: 1.50018V
- %PASS Slot 17 channel 2 acquire accuracy at 1.500009383V on 3V range Measured: 1.500036V low limit: 1.499829V high limit: 1.500189V
- %PASS Slot 17 channel 2 source accuracy at 2V on 3V range Measured: 2.000090V low limit: 1.99982V high limit: 2.00018V
- %PASS Slot 17 channel 2 acquire accuracy at 2.000090008V on 3V range Measured: 2.000100V low limit: 1.999910V high limit: 2.000270V
- %PASS Slot 17 channel 2 source accuracy at 2.5V on 3V range Measured: 2.500042V low limit: 2.49982V high limit: 2.50018V

- %PASS Slot 17 channel 2 acquire accuracy at 2.500042959V on 3V range Measured: 2.499989V low limit: 2.499862V high limit: 2.500222V
- %PASS Slot 17 channel 2 source accuracy at 3V on 3V range Measured: 3.000046V low limit: 2.99982V high limit: 3.00018V
- %PASS Slot 17 channel 2 acquire accuracy at 3.000046087V on 3V range Measured: 3.000042V low limit: 2.999866V high limit: 3.000226V
- %PASS Slot 17 channel 2 source accuracy at 0V on 6V range Measured: 0.00003400131V low limit: -0.00036V high limit: 0.00036V
- %PASS Slot 17 channel 2 acquire accuracy at 0.00003400131458V on 6V range

  Measured: 6.227955E-05V low limit: -0.0003259986V high limit: 0.0003940013V
- %PASS Slot 17 channel 2 source accuracy at 1V on 6V range Measured: 1.000024V low limit: 0.99964V high limit: 1.00036V
- %PASS Slot 17 channel 2 acquire accuracy at 1.000024469V on 6V range Measured: 1.000109V low limit: 0.9996644V high limit: 1.000384V
- %PASS Slot 17 channel 2 source accuracy at 2V on 6V range Measured: 2.000014V low limit: 1.99964V high limit: 2.00036V
- %PASS Slot 17 channel 2 acquire accuracy at 2.000014875V on 6V range Measured: 2.000133V low limit: 1.999654V high limit: 2.000374V
- %PASS Slot 17 channel 2 source accuracy at 3V on 6V range Measured: 2.999992V low limit: 2.99964V high limit: 3.00036V
- %PASS Slot 17 channel 2 acquire accuracy at 2.999992539V on 6V range Measured: 3.000015V low limit: 2.999632V high limit: 3.000352V
- %PASS Slot 17 channel 2 source accuracy at 4V on 6V range Measured: 4.000112V low limit: 3.99964V high limit: 4.00036V
- %PASS Slot 17 channel 2 acquire accuracy at 4.000112282V on 6V range

- Measured: 4.000134V low limit: 3.999752V high limit: 4.000472V
- %PASS Slot 17 channel 2 source accuracy at 5V on 6V range Measured: 5.000086V low limit: 4.99964V high limit: 5.00036V
- %PASS Slot 17 channel 2 acquire accuracy at 5.0000864V on 6V range Measured: 5.000004V low limit: 4.999726V high limit: 5.000446V
- %PASS Slot 17 channel 2 source accuracy at 6V on 6V range Measured: 6.000086V low limit: 5.99964V high limit: 6.00036V
- %PASS Slot 17 channel 2 acquire accuracy at 6.000086569V on 6V range Measured: 6.000057V low limit: 5.999726V high limit: 6.000446V
- %PASS Slot 17 channel 3 source accuracy at 0V on 3V range Measured: 0.00002167104V low limit: -0.00018V high limit: 0.00018V
- %PASS Slot 17 channel 3 acquire accuracy at 0.00002167104734V on 3V range

  Measured: 3.556941E-05V low limit: -0.0001583289V high limit: 0.0002016710V
- %PASS Slot 17 channel 3 source accuracy at 0.5V on 3V range Measured: 0.5000393V low limit: 0.49982V high limit: 0.50018V
- %PASS Slot 17 channel 3 acquire accuracy at 0.5000393605V on 3V range

  Measured: 0.5001272V low limit: 0.4998593V high limit: 0.5002193V
- %PASS Slot 17 channel 3 source accuracy at 1V on 3V range Measured: 1.000048V low limit: 0.99982V high limit: 1.00018V
- %PASS Slot 17 channel 3 acquire accuracy at 1.000048463V on 3V range Measured: 1.000103V low limit: 0.9998684V high limit: 1.000228V
- %PASS Slot 17 channel 3 source accuracy at 1.5V on 3V range Measured: 1.499996V low limit: 1.49982V high limit: 1.50018V
- %PASS Slot 17 channel 3 acquire accuracy at 1.499996598V on 3V range Measured: 1.500029V low limit: 1.499816V high limit: 1.500176V

- %PASS Slot 17 channel 3 source accuracy at 2V on 3V range Measured: 2.000044V low limit: 1.99982V high limit: 2.00018V
- %PASS Slot 17 channel 3 acquire accuracy at 2.000044779V on 3V range Measured: 2.000050V low limit: 1.999864V high limit: 2.000224V
- %PASS Slot 17 channel 3 source accuracy at 2.5V on 3V range Measured: 2.500043V low limit: 2.49982V high limit: 2.50018V
- %PASS Slot 17 channel 3 acquire accuracy at 2.500043353V on 3V range Measured: 2.499967V low limit: 2.499863V high limit: 2.500223V
- %PASS Slot 17 channel 3 source accuracy at 3V on 3V range Measured: 3.000029V low limit: 2.99982V high limit: 3.00018V
- %PASS Slot 17 channel 3 acquire accuracy at 3.000029536V on 3V range Measured: 3.000032V low limit: 2.999849V high limit: 3.000209V
- %PASS Slot 17 channel 3 source accuracy at 0V on 6V range Measured: 0.00003417806V low limit: -0.00036V high limit: 0.00036V
- %PASS Slot 17 channel 3 acquire accuracy at 0.00003417806751V on 6V range

  Measured: 9.383005E-05V low limit: -0.0003258219V high limit: 0.0003941780V
- %PASS Slot 17 channel 3 source accuracy at 1V on 6V range Measured: 1.000046V low limit: 0.99964V high limit: 1.00036V
- %PASS Slot 17 channel 3 acquire accuracy at 1.000046274V on 6V range Measured: 1.000281V low limit: 0.9996862V high limit: 1.000406V
- %PASS Slot 17 channel 3 source accuracy at 2V on 6V range Measured: 2.000046V low limit: 1.99964V high limit: 2.00036V
- %PASS Slot 17 channel 3 acquire accuracy at 2.000046136V on 6V range Measured: 2.000256V low limit: 1.999686V high limit: 2.000406V
- %PASS Slot 17 channel 3 source accuracy at 3V on 6V range Measured: 3.000017V low limit: 2.99964V high limit: 3.00036V

- %PASS Slot 17 channel 3 acquire accuracy at 3.000017102V on 6V range Measured: 3.000047V low limit: 2.999657V high limit: 3.000377V
- %PASS Slot 17 channel 3 source accuracy at 4V on 6V range Measured: 4.000107V low limit: 3.99964V high limit: 4.00036V
- %PASS Slot 17 channel 3 acquire accuracy at 4.000107291V on 6V range Measured: 4.000146V low limit: 3.999747V high limit: 4.000467V
- %PASS Slot 17 channel 3 source accuracy at 5V on 6V range Measured: 5.000087V low limit: 4.99964V high limit: 5.00036V
- %PASS Slot 17 channel 3 acquire accuracy at 5.000087976V on 6V range Measured: 4.999985V low limit: 4.999727V high limit: 5.000447V
- %PASS Slot 17 channel 3 source accuracy at 6V on 6V range Measured: 6.000052V low limit: 5.99964V high limit: 6.00036V
- %PASS Slot 17 channel 3 acquire accuracy at 6.000052986V on 6V range Measured: 6.000090V low limit: 5.999692V high limit: 6.000412V
- %PASS Slot 17 channel 4 source accuracy at 0V on 3V range
  Measured: -0.00001843961V low limit: -0.00018V high limit: 0.00018V
- %PASS Slot 17 channel 4 acquire accuracy at -0.00001843961868V on 3V range

  Measured: -2.078443E-05V low limit: -0.0001984396V high limit: 0.0001615603V
- %PASS Slot 17 channel 4 source accuracy at 0.5V on 3V range Measured: 0.4999760V low limit: 0.49982V high limit: 0.50018V
- %PASS Slot 17 channel 4 acquire accuracy at 0.4999760401V on 3V range

  Measured: 0.5000547V low limit: 0.4997960V high limit: 0.5001560V
- %PASS Slot 17 channel 4 source accuracy at 1V on 3V range Measured: 0.9999710V low limit: 0.99982V high limit: 1.00018V
- %PASS Slot 17 channel 4 acquire accuracy at 0.9999710523V on 3V range

- Measured: 1.000020V low limit: 0.9997910V high limit: 1.000151V
- %PASS Slot 17 channel 4 source accuracy at 1.5V on 3V range Measured: 1.499982V low limit: 1.49982V high limit: 1.50018V
- %PASS Slot 17 channel 4 acquire accuracy at 1.499982893V on 3V range Measured: 1.500022V low limit: 1.499802V high limit: 1.500162V
- %PASS Slot 17 channel 4 source accuracy at 2V on 3V range Measured: 2.000032V low limit: 1.99982V high limit: 2.00018V
- %PASS Slot 17 channel 4 acquire accuracy at 2.000032826V on 3V range Measured: 2.000056V low limit: 1.999852V high limit: 2.000212V
- %PASS Slot 17 channel 4 source accuracy at 2.5V on 3V range Measured: 2.500013V low limit: 2.49982V high limit: 2.50018V
- %PASS Slot 17 channel 4 acquire accuracy at 2.500013055V on 3V range Measured: 2.499939V low limit: 2.499833V high limit: 2.500193V
- %PASS Slot 17 channel 4 source accuracy at 3V on 3V range Measured: 2.999987V low limit: 2.99982V high limit: 3.00018V
- %PASS Slot 17 channel 4 acquire accuracy at 2.999987022V on 3V range Measured: 2.999973V low limit: 2.999807V high limit: 3.000167V
- %PASS Slot 17 channel 4 source accuracy at 0V on 6V range Measured: 0.00003393218V low limit: -0.00036V high limit: 0.00036V
- %PASS Slot 17 channel 4 acquire accuracy at 0.00003393218843V on 6V range

  Measured: 9.747458E-05V low limit: -0.0003260678V high limit: 0.0003939321V
- %PASS Slot 17 channel 4 source accuracy at 1V on 6V range Measured: 1.000019V low limit: 0.99964V high limit: 1.00036V
- %PASS Slot 17 channel 4 acquire accuracy at 1.000019303V on 6V range Measured: 1.000280V low limit: 0.9996593V high limit: 1.000379V

- %PASS Slot 17 channel 4 source accuracy at 2V on 6V range Measured: 2.000015V low limit: 1.99964V high limit: 2.00036V
- %PASS Slot 17 channel 4 acquire accuracy at 2.000015663V on 6V range Measured: 2.000184V low limit: 1.999655V high limit: 2.000375V
- %PASS Slot 17 channel 4 source accuracy at 3V on 6V range Measured: 2.999949V low limit: 2.99964V high limit: 3.00036V
- %PASS Slot 17 channel 4 acquire accuracy at 2.999949148V on 6V range Measured: 3.000070V low limit: 2.999589V high limit: 3.000309V
- %PASS Slot 17 channel 4 source accuracy at 4V on 6V range Measured: 4.000064V low limit: 3.99964V high limit: 4.00036V
- %PASS Slot 17 channel 4 acquire accuracy at 4.000064207V on 6V range Measured: 4.000164V low limit: 3.999704V high limit: 4.000424V
- %PASS Slot 17 channel 4 source accuracy at 5V on 6V range Measured: 5.000056V low limit: 4.99964V high limit: 5.00036V
- %PASS Slot 17 channel 4 acquire accuracy at 5.000056145V on 6V range Measured: 5.000027V low limit: 4.999696V high limit: 5.000416V
- %PASS Slot 17 channel 4 source accuracy at 6V on 6V range Measured: 6.000045V low limit: 5.99964V high limit: 6.00036V
- %PASS Slot 17 channel 4 acquire accuracy at 6.000045149V on 6V range Measured: 6.000180V low limit: 5.999685V high limit: 6.000405V
- %PASS Slot 17 channel 5 source accuracy at 0V on 3V range Measured: 0.000005387026V low limit: -0.00018V high limit: 0.00018V
- %PASS Slot 17 channel 5 acquire accuracy at 0.00000538702695V on 3V range

  Measured: 2.583452E-05V low limit: -0.0001746129V high limit: 0.0001853870V

- %PASS Slot 17 channel 5 acquire accuracy at 0.5000251326V on 3V range Measured: 0.5000717V low limit: 0.4998451V high limit: 0.5002051V
- %PASS Slot 17 channel 5 source accuracy at 1V on 3V range Measured: 1.000001V low limit: 0.99982V high limit: 1.00018V
- %PASS Slot 17 channel 5 acquire accuracy at 1.000001483V on 3V range Measured: 1.000037V low limit: 0.9998214V high limit: 1.000181V
- %PASS Slot 17 channel 5 source accuracy at 1.5V on 3V range Measured: 1.499996V low limit: 1.49982V high limit: 1.50018V
- %PASS Slot 17 channel 5 acquire accuracy at 1.499996641V on 3V range Measured: 1.500012V low limit: 1.499816V high limit: 1.500176V
- %PASS Slot 17 channel 5 source accuracy at 2V on 3V range Measured: 2.000059V low limit: 1.99982V high limit: 2.00018V
- %PASS Slot 17 channel 5 acquire accuracy at 2.000059491V on 3V range Measured: 2.000034V low limit: 1.999879V high limit: 2.000239V
- %PASS Slot 17 channel 5 source accuracy at 2.5V on 3V range Measured: 2.500022V low limit: 2.49982V high limit: 2.50018V
- %PASS Slot 17 channel 5 acquire accuracy at 2.500022687V on 3V range Measured: 2.499997V low limit: 2.499842V high limit: 2.500202V
- %PASS Slot 17 channel 5 source accuracy at 3V on 3V range Measured: 3.000017V low limit: 2.99982V high limit: 3.00018V
- %PASS Slot 17 channel 5 acquire accuracy at 3.000017277V on 3V range Measured: 3.000031V low limit: 2.999837V high limit: 3.000197V
- %PASS Slot 17 channel 5 source accuracy at 0V on 6V range
  Measured: -0.00001483630V low limit: -0.00036V high limit: 0.00036V
- %PASS Slot 17 channel 5 acquire accuracy at -0.00001483630885V on 6V range

- %PASS Slot 17 channel 5 source accuracy at 1V on 6V range Measured: 1.000010V low limit: 0.99964V high limit: 1.00036V
- %PASS Slot 17 channel 5 acquire accuracy at 1.000010064V on 6V range Measured: 1.000093V low limit: 0.9996500V high limit: 1.000370V
- %PASS Slot 17 channel 5 source accuracy at 2V on 6V range Measured: 1.999951V low limit: 1.99964V high limit: 2.00036V
- %PASS Slot 17 channel 5 acquire accuracy at 1.999951782V on 6V range Measured: 2.000089V low limit: 1.999591V high limit: 2.000311V
- %PASS Slot 17 channel 5 source accuracy at 3V on 6V range Measured: 2.999943V low limit: 2.99964V high limit: 3.00036V
- %PASS Slot 17 channel 5 acquire accuracy at 2.999943632V on 6V range Measured: 2.999949V low limit: 2.999583V high limit: 3.000303V
- %PASS Slot 17 channel 5 source accuracy at 4V on 6V range Measured: 3.999976V low limit: 3.99964V high limit: 4.00036V
- %PASS Slot 17 channel 5 acquire accuracy at 3.999976201V on 6V range Measured: 3.999981V low limit: 3.999616V high limit: 4.000336V
- %PASS Slot 17 channel 5 source accuracy at 5V on 6V range Measured: 4.999905V low limit: 4.99964V high limit: 5.00036V
- %PASS Slot 17 channel 5 acquire accuracy at 4.99990509V on 6V range Measured: 4.999841V low limit: 4.999545V high limit: 5.000265V
- %PASS Slot 17 channel 5 source accuracy at 6V on 6V range Measured: 5.999936V low limit: 5.99964V high limit: 6.00036V
- %PASS Slot 17 channel 5 acquire accuracy at 5.999936915V on 6V range Measured: 5.999890V low limit: 5.999576V high limit: 6.000296V

- %PASS Slot 17 channel 6 source accuracy at 0V on 3V range
  Measured: -0.00001703915V low limit: -0.00018V high limit: 0.00018V
- %PASS Slot 17 channel 6 acquire accuracy at -0.00001703915793V on 3V range

  Measured: -3.299633E-05V low limit: -0.0001970391V high limit: 0.0001629608V
- %PASS Slot 17 channel 6 source accuracy at 0.5V on 3V range Measured: 0.4999747V low limit: 0.49982V high limit: 0.50018V
- %PASS Slot 17 channel 6 acquire accuracy at 0.4999747216V on 3V range Measured: 0.5000325V low limit: 0.4997947V high limit: 0.5001547V
- %PASS Slot 17 channel 6 source accuracy at 1V on 3V range Measured: 0.9999818V low limit: 0.99982V high limit: 1.00018V
- %PASS Slot 17 channel 6 acquire accuracy at 0.9999818897V on 3V range Measured: 1.000035V low limit: 0.9998018V high limit: 1.000161V
- %PASS Slot 17 channel 6 source accuracy at 1.5V on 3V range Measured: 1.499951V low limit: 1.49982V high limit: 1.50018V
- %PASS Slot 17 channel 6 acquire accuracy at 1.499951456V on 3V range Measured: 1.499962V low limit: 1.499771V high limit: 1.500131V
- %PASS Slot 17 channel 6 source accuracy at 2V on 3V range Measured: 2.000011V low limit: 1.99982V high limit: 2.00018V
- %PASS Slot 17 channel 6 acquire accuracy at 2.000011459V on 3V range Measured: 2.000027V low limit: 1.999831V high limit: 2.000191V
- %PASS Slot 17 channel 6 source accuracy at 2.5V on 3V range Measured: 2.500005V low limit: 2.49982V high limit: 2.50018V
- %PASS Slot 17 channel 6 acquire accuracy at 2.50000548V on 3V range Measured: 2.499983V low limit: 2.499825V high limit: 2.500185V
- %PASS Slot 17 channel 6 source accuracy at 3V on 3V range Measured: 2.999983V low limit: 2.99982V high limit: 3.00018V

- %PASS Slot 17 channel 6 acquire accuracy at 2.999983037V on 3V range Measured: 3.000022V low limit: 2.999803V high limit: 3.000163V
- %PASS Slot 17 channel 6 source accuracy at 0V on 6V range Measured: 0.00002074265V low limit: -0.00036V high limit: 0.00036V
- %PASS Slot 17 channel 6 acquire accuracy at 0.00002074265692V on 6V range

  Measured: 2.030221E-05V low limit: -0.0003392573V high limit: 0.0003807426V
- %PASS Slot 17 channel 6 source accuracy at 1V on 6V range Measured: 0.9999472V low limit: 0.99964V high limit: 1.00036V
- %PASS Slot 17 channel 6 acquire accuracy at 0.9999472398V on 6V range Measured: 1.000134V low limit: 0.9995872V high limit: 1.000307V
- %PASS Slot 17 channel 6 source accuracy at 2V on 6V range Measured: 1.999914V low limit: 1.99964V high limit: 2.00036V
- %PASS Slot 17 channel 6 acquire accuracy at 1.999914127V on 6V range Measured: 2.000076V low limit: 1.999554V high limit: 2.000274V
- %PASS Slot 17 channel 6 source accuracy at 3V on 6V range Measured: 2.999809V low limit: 2.99964V high limit: 3.00036V
- %PASS Slot 17 channel 6 acquire accuracy at 2.999809433V on 6V range Measured: 2.999906V low limit: 2.999449V high limit: 3.000169V
- %PASS Slot 17 channel 6 source accuracy at 4V on 6V range Measured: 3.999985V low limit: 3.99964V high limit: 4.00036V
- %PASS Slot 17 channel 6 acquire accuracy at 3.999985615V on 6V range Measured: 4.000073V low limit: 3.999625V high limit: 4.000345V
- %PASS Slot 17 channel 6 source accuracy at 5V on 6V range Measured: 4.999960V low limit: 4.99964V high limit: 5.00036V
- %PASS Slot 17 channel 6 acquire accuracy at 4.999960477V on 6V range

- Measured: 4.999997V low limit: 4.999600V high limit: 5.000320V
- %PASS Slot 17 channel 6 source accuracy at 6V on 6V range Measured: 5.999910V low limit: 5.99964V high limit: 6.00036V
- %PASS Slot 17 channel 6 acquire accuracy at 5.999910294V on 6V range Measured: 5.999934V low limit: 5.999550V high limit: 6.000270V
- %PASS Slot 17 channel 7 source accuracy at 0V on 3V range
  Measured: 0.000004086755V low limit: -0.00018V high limit: 0.00018V
- %PASS Slot 17 channel 7 acquire accuracy at 0.000004086755359V on 3V range

  Measured: 1.103020E-05V low limit: -0.0001759132V high limit: 0.0001840867V
- %PASS Slot 17 channel 7 source accuracy at 0.5V on 3V range Measured: 0.4999727V low limit: 0.49982V high limit: 0.50018V
- %PASS Slot 17 channel 7 acquire accuracy at 0.4999727241V on 3V range Measured: 0.5000067V low limit: 0.4997927V high limit: 0.5001527V
- %PASS Slot 17 channel 7 source accuracy at 1V on 3V range Measured: 0.9999595V low limit: 0.99982V high limit: 1.00018V
- %PASS Slot 17 channel 7 acquire accuracy at 0.999959514V on 3V range Measured: 1.000032V low limit: 0.9997795V high limit: 1.000139V
- %PASS Slot 17 channel 7 source accuracy at 1.5V on 3V range Measured: 1.499959V low limit: 1.49982V high limit: 1.50018V
- %PASS Slot 17 channel 7 acquire accuracy at 1.499959688V on 3V range Measured: 1.499977V low limit: 1.499779V high limit: 1.500139V
- %PASS Slot 17 channel 7 source accuracy at 2V on 3V range Measured: 2.000049V low limit: 1.99982V high limit: 2.00018V
- %PASS Slot 17 channel 7 acquire accuracy at 2.000049114V on 3V range Measured: 2.000053V low limit: 1.999869V high limit: 2.000229V

- %PASS Slot 17 channel 7 source accuracy at 2.5V on 3V range Measured: 2.500023V low limit: 2.49982V high limit: 2.50018V
- %PASS Slot 17 channel 7 acquire accuracy at 2.500023388V on 3V range Measured: 2.500025V low limit: 2.499843V high limit: 2.500203V
- %PASS Slot 17 channel 7 source accuracy at 3V on 3V range Measured: 2.999983V low limit: 2.99982V high limit: 3.00018V
- %PASS Slot 17 channel 7 acquire accuracy at 2.999983037V on 3V range Measured: 2.999994V low limit: 2.999803V high limit: 3.000163V
- %PASS Slot 17 channel 7 source accuracy at 0V on 6V range
  Measured: -0.00003734430V low limit: -0.00036V high limit: 0.00036V
- %PASS Slot 17 channel 7 acquire accuracy at -0.00003734430759V on 6V range

  Measured: -3.381509E-05V low limit: -0.0003973443V high limit: 0.0003226556V
- %PASS Slot 17 channel 7 source accuracy at 1V on 6V range Measured: 0.9998870V low limit: 0.99964V high limit: 1.00036V
- %PASS Slot 17 channel 7 acquire accuracy at 0.9998870646V on 6V range Measured: 0.9999404V low limit: 0.9995270V high limit: 1.000247V
- %PASS Slot 17 channel 7 source accuracy at 2V on 6V range Measured: 1.999853V low limit: 1.99964V high limit: 2.00036V
- %PASS Slot 17 channel 7 acquire accuracy at 1.999853661V on 6V range Measured: 1.999956V low limit: 1.999493V high limit: 2.000213V
- %PASS Slot 17 channel 7 source accuracy at 3V on 6V range Measured: 2.999850V low limit: 2.99964V high limit: 3.00036V
- %PASS Slot 17 channel 7 acquire accuracy at 2.999850722V on 6V range Measured: 2.999806V low limit: 2.999490V high limit: 3.000210V

- %PASS Slot 17 channel 7 acquire accuracy at 4.00004052V on 6V range Measured: 4.000052V low limit: 3.999680V high limit: 4.000400V
- %PASS Slot 17 channel 7 source accuracy at 5V on 6V range Measured: 5.000018V low limit: 4.99964V high limit: 5.00036V
- %PASS Slot 17 channel 7 acquire accuracy at 5.000018491V on 6V range Measured: 4.999997V low limit: 4.999658V high limit: 5.000378V
- %PASS Slot 17 channel 7 source accuracy at 6V on 6V range Measured: 5.999999V low limit: 5.99964V high limit: 6.00036V
- %PASS Slot 17 channel 7 acquire accuracy at 5.999999526V on 6V range Measured: 5.999989V low limit: 5.999639V high limit: 6.000359V
- Performing source linearity verification...
- %PASS Slot 17 channel 0 3V Source offset in mV Measured: 0.6841 low limit: -50 high limit: 50
- %PASS Slot 17 channel 0 3V Source gain in mV Measured: 0.9996 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 1 3V Source offset in mV Measured: 0.1045 low limit: -50 high limit: 50
- %PASS Slot 17 channel 1 3V Source gain in mV Measured: 0.9997 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 2 3V Source offset in mV Measured: -0.2301 low limit: -50 high limit: 50
- %PASS Slot 17 channel 2 3V Source gain in mV Measured: 0.9999 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 3 3V Source offset in mV Measured: 0.3504 low limit: -50 high limit: 50

- %PASS Slot 17 channel 3 3V Source gain in mV Measured: 0.9999 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 4 3V Source offset in mV Measured: 0.4544 low limit: -50 high limit: 50
- %PASS Slot 17 channel 4 3V Source gain in mV Measured: 1.000 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 5 3V Source offset in mV Measured: 5.134E-02 low limit: -50 high limit: 50
- %PASS Slot 17 channel 5 3V Source gain in mV Measured: 0.9997 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 6 3V Source offset in mV Measured: 1.354 low limit: -50 high limit: 50
- %PASS Slot 17 channel 6 3V Source gain in mV Measured: 0.9995 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 7 3V Source offset in mV Measured: 0.8566 low limit: -50 high limit: 50
- %PASS Slot 17 channel 7 3V Source gain in mV Measured: 0.9995 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 0 6V Source offset in mV Measured: 1.150 low limit: -100 high limit: 100
- %PASS Slot 17 channel 0 6V Source gain in mV Measured: 0.9995 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 1 6V Source offset in mV Measured: 0.5850 low limit: -100 high limit: 100
- %PASS Slot 17 channel 1 6V Source gain in mV Measured: 0.9997 low limit: 0.98 high limit: 1.02

- %PASS Slot 17 channel 2 6V Source offset in mV Measured: -0.2423 low limit: -100 high limit: 100
- %PASS Slot 17 channel 2 6V Source gain in mV Measured: 0.9999 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 3 6V Source offset in mV Measured: 0.7773 low limit: -100 high limit: 100
- %PASS Slot 17 channel 3 6V Source gain in mV Measured: 0.9998 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 4 6V Source offset in mV Measured: 0.5983 low limit: -100 high limit: 100
- %PASS Slot 17 channel 4 6V Source gain in mV Measured: 1.000 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 5 6V Source offset in mV Measured: 0.1700 low limit: -100 high limit: 100
- %PASS Slot 17 channel 5 6V Source gain in mV Measured: 0.9996 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 6 6V Source offset in mV Measured: 2.003 low limit: -100 high limit: 100
- %PASS Slot 17 channel 6 6V Source gain in mV Measured: 0.9995 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 7 6V Source offset in mV Measured: 1.669 low limit: -100 high limit: 100
- %PASS Slot 17 channel 7 6V Source gain in mV Measured: 0.9994 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 0 linearity at 0V on 3V range

- %PASS Slot 17 channel 0 linearity at 0.125V on 3V range Measured: 0.1249928V low limit: 0.1248914V high limit: 0.1250714V
- %PASS Slot 17 channel 0 linearity at .25V on 3V range Measured: 0.2499801V low limit: 0.2498931V high limit: 0.2500731V
- %PASS Slot 17 channel 0 linearity at 375V on 3V range Measured: 0.3749885V low limit: 0.3748949V high limit: 0.3750749V
- %PASS Slot 17 channel 0 linearity at .5V on 3V range Measured: 0.4999755V low limit: 0.4998967V high limit: 0.5000767V
- %PASS Slot 17 channel 0 linearity at .625V on 3V range Measured: 0.6249911V low limit: 0.6248985V high limit: 0.6250785V
- %PASS Slot 17 channel 0 linearity at .75V on 3V range Measured: 0.7499688V low limit: 0.7499003V high limit: 0.7500803V
- %PASS Slot 17 channel 0 linearity at .875V on 3V range Measured: 0.8750096V low limit: 0.8749021V high limit: 0.8750821V
- %PASS Slot 17 channel 0 linearity at 1V on 3V range
  Measured: 0.9999662V low limit: 0.9999039V high limit: 1.000083V
- %PASS Slot 17 channel 0 linearity at 1.125V on 3V range Measured: 1.124982V low limit: 1.124905V high limit: 1.125085V
- %PASS Slot 17 channel 0 linearity at 1.25V on 3V range Measured: 1.249975V low limit: 1.249907V high limit: 1.250087V
- %PASS Slot 17 channel 0 linearity at 1.375V on 3V range Measured: 1.375005V low limit: 1.374909V high limit: 1.375089V
- %PASS Slot 17 channel 0 linearity at 1.5V on 3V range Measured: 1.499945V low limit: 1.499911V high limit: 1.500091V

- %PASS Slot 17 channel 0 linearity at 1.625V on 3V range
  Measured: 1.625042V low limit: 1.624912V high limit: 1.625092V
- %PASS Slot 17 channel 0 linearity at 1.75V on 3V range Measured: 1.750068V low limit: 1.749914V high limit: 1.750094V
- %PASS Slot 17 channel 0 linearity at 1.875V on 3V range Measured: 1.875025V low limit: 1.874916V high limit: 1.875096V
- %PASS Slot 17 channel 0 linearity at 2V on 3V range Measured: 2.000052V low limit: 1.999918V high limit: 2.000098V
- %PASS Slot 17 channel 0 linearity at 2.125V on 3V range
  Measured: 2.125017V low limit: 2.124919V high limit: 2.125099V
- %PASS Slot 17 channel 0 linearity at 2.25V on 3V range
  Measured: 2.250047V low limit: 2.249921V high limit: 2.250101V
- %PASS Slot 17 channel 0 linearity at 2.375V on 3V range Measured: 2.375001V low limit: 2.374923V high limit: 2.375103V
- %PASS Slot 17 channel 0 linearity at 2.5V on 3V range Measured: 2.500023V low limit: 2.499925V high limit: 2.500105V
- %PASS Slot 17 channel 0 linearity at 2.625V on 3V range Measured: 2.625006V low limit: 2.624927V high limit: 2.625107V
- %PASS Slot 17 channel 0 linearity at 2.75V on 3V range
  Measured: 2.750009V low limit: 2.749928V high limit: 2.750108V
- %PASS Slot 17 channel 0 linearity at 2.875V on 3V range Measured: 2.874982V low limit: 2.874930V high limit: 2.875110V
- %PASS Slot 17 channel 0 linearity at 3V on 3V range Measured: 2.999995V low limit: 2.999932V high limit: 3.000112V
- %PASS Slot 17 channel 0 maximum linearity error on 3V range Measured: 6.400108E-05V high limit: 0.00009V

- %PASS Slot 17 channel 0 linearity at 0V on 6V range Measured: 0.00002554954V low limit: -8.755847E-05V high limit: 2.724415E-04V
- %PASS Slot 17 channel 0 linearity at 0.25V on 6V range Measured: 0.2501580V low limit: 0.2499084V high limit: 0.2502684V
- %PASS Slot 17 channel 0 linearity at .5V on 6V range Measured: 0.5000796V low limit: 0.4999044V high limit: 0.5002644V
- %PASS Slot 17 channel 0 linearity at .75V on 6V range Measured: 0.7500988V low limit: 0.7499005V high limit: 0.7502605V
- %PASS Slot 17 channel 0 linearity at 1V on 6V range Measured: 1.000071V low limit: 0.9998965V high limit: 1.000256V
- %PASS Slot 17 channel 0 linearity at 1.25V on 6V range Measured: 1.250072V low limit: 1.249892V high limit: 1.250252V
- %PASS Slot 17 channel 0 linearity at 1.5V on 6V range Measured: 1.500003V low limit: 1.499888V high limit: 1.500248V
- %PASS Slot 17 channel 0 linearity at 1.75V on 6V range Measured: 1.750047V low limit: 1.749884V high limit: 1.750244V
- %PASS Slot 17 channel 0 linearity at 2V on 6V range
  Measured: 2.000013V low limit: 1.999880V high limit: 2.000240V
- %PASS Slot 17 channel 0 linearity at 2.25V on 6V range
  Measured: 2.250088V low limit: 2.249876V high limit: 2.250236V
- %PASS Slot 17 channel 0 linearity at 2.5V on 6V range Measured: 2.500063V low limit: 2.499872V high limit: 2.500232V
- %PASS Slot 17 channel 0 linearity at 2.75V on 6V range Measured: 2.750096V low limit: 2.749868V high limit: 2.750228V
- %PASS Slot 17 channel 0 linearity at 3V on 6V range

- Measured: 2.999970V low limit: 2.999864V high limit: 3.000224V
- %PASS Slot 17 channel 0 linearity at 3.25V on 6V range Measured: 3.250054V low limit: 3.249860V high limit: 3.250220V
- %PASS Slot 17 channel 0 linearity at 3.5V on 6V range Measured: 3.500098V low limit: 3.499856V high limit: 3.500216V
- %PASS Slot 17 channel 0 linearity at 3.75V on 6V range Measured: 3.750000V low limit: 3.749852V high limit: 3.750212V
- %PASS Slot 17 channel 0 linearity at 4V on 6V range Measured: 4.000139V low limit: 3.999848V high limit: 4.000208V
- %PASS Slot 17 channel 0 linearity at 4.25V on 6V range Measured: 4.250060V low limit: 4.249844V high limit: 4.250204V
- %PASS Slot 17 channel 0 linearity at 4.5V on 6V range Measured: 4.500088V low limit: 4.499840V high limit: 4.500200V
- %PASS Slot 17 channel 0 linearity at 4.75V on 6V range Measured: 4.750001V low limit: 4.749836V high limit: 4.750196V
- %PASS Slot 17 channel 0 linearity at 5V on 6V range Measured: 5.000002V low limit: 4.999832V high limit: 5.000192V
- %PASS Slot 17 channel 0 linearity at 5.25V on 6V range Measured: 5.249986V low limit: 5.249828V high limit: 5.250188V
- %PASS Slot 17 channel 0 linearity at 5.5V on 6V range Measured: 5.499975V low limit: 5.499824V high limit: 5.500184V
- %PASS Slot 17 channel 0 linearity at 5.75V on 6V range Measured: 5.749910V low limit: 5.749821V high limit: 5.750181V
- %PASS Slot 17 channel 0 linearity at 6V on 6V range Measured: 6.000011V low limit: 5.999817V high limit: 6.000177V

- %PASS Slot 17 channel 0 maximum linearity error on 6V range Measured: 1.104563E-04V high limit: 0.00018V
- %PASS Slot 17 channel 1 linearity at 0V on 3V range

  Measured: -0.0000007091992V low limit: -5.584443E-05V high limit: 1.241555E-04V
- %PASS Slot 17 channel 1 linearity at 0.25V on 3V range Measured: 0.2500066V low limit: 0.2499495V high limit: 0.2501295V
- %PASS Slot 17 channel 1 linearity at 375V on 3V range
  Measured: 0.3750546V low limit: 0.3749522V high limit: 0.3751322V
- %PASS Slot 17 channel 1 linearity at .5V on 3V range Measured: 0.5000585V low limit: 0.4999549V high limit: 0.5001349V
- %PASS Slot 17 channel 1 linearity at .625V on 3V range Measured: 0.6250281V low limit: 0.6249576V high limit: 0.6251376V
- %PASS Slot 17 channel 1 linearity at .875V on 3V range Measured: 0.8750492V low limit: 0.8749630V high limit: 0.8751430V
- %PASS Slot 17 channel 1 linearity at 1V on 3V range
  Measured: 1.000050V low limit: 0.9999657V high limit: 1.000145V
- %PASS Slot 17 channel 1 linearity at 1.125V on 3V range Measured: 1.125092V low limit: 1.124968V high limit: 1.125148V
- %PASS Slot 17 channel 1 linearity at 1.25V on 3V range
  Measured: 1.250041V low limit: 1.249971V high limit: 1.250151V
- %PASS Slot 17 channel 1 linearity at 1.375V on 3V range Measured: 1.375098V low limit: 1.374973V high limit: 1.375153V

- %PASS Slot 17 channel 1 linearity at 1.5V on 3V range Measured: 1.500019V low limit: 1.499976V high limit: 1.500156V
- %PASS Slot 17 channel 1 linearity at 1.625V on 3V range Measured: 1.625083V low limit: 1.624979V high limit: 1.625159V
- %PASS Slot 17 channel 1 linearity at 1.75V on 3V range
  Measured: 1.750130V low limit: 1.749981V high limit: 1.750161V
- %PASS Slot 17 channel 1 linearity at 1.875V on 3V range Measured: 1.875055V low limit: 1.874984V high limit: 1.875164V
- %PASS Slot 17 channel 1 linearity at 2V on 3V range
  Measured: 2.000119V low limit: 1.999987V high limit: 2.000167V
- %PASS Slot 17 channel 1 linearity at 2.125V on 3V range Measured: 2.125106V low limit: 2.124989V high limit: 2.125169V
- %PASS Slot 17 channel 1 linearity at 2.25V on 3V range Measured: 2.250096V low limit: 2.249992V high limit: 2.250172V
- %PASS Slot 17 channel 1 linearity at 2.375V on 3V range Measured: 2.375079V low limit: 2.374995V high limit: 2.375175V
- %PASS Slot 17 channel 1 linearity at  $\,$  2.5V on 3V range Measured: 2.500078V low limit: 2.499998V high limit: 2.500178V
- %PASS Slot 17 channel 1 linearity at 2.625V on 3V range Measured: 2.625072V low limit: 2.625000V high limit: 2.625180V
- %PASS Slot 17 channel 1 linearity at 2.75V on 3V range
  Measured: 2.750112V low limit: 2.750003V high limit: 2.750183V
- %PASS Slot 17 channel 1 linearity at 2.875V on 3V range
  Measured: 2.875047V low limit: 2.875006V high limit: 2.875186V
- %PASS Slot 17 channel 1 linearity at 3V on 3V range

- Measured: 3.000077V low limit: 3.000008V high limit: 3.000188V
- %PASS Slot 17 channel 1 maximum linearity error on 3V range Measured: 5.905354E-05V high limit: 0.00009V
- %PASS Slot 17 channel 1 linearity at 0V on 6V range Measured: 1.032517E-07V low limit: -1.333222E-04V high limit: 2.266777E-04V
- %PASS Slot 17 channel 1 linearity at 0.25V on 6V range Measured: 0.2500424V low limit: 0.2498631V high limit: 0.2502231V
- %PASS Slot 17 channel 1 linearity at .5V on 6V range Measured: 0.5000343V low limit: 0.4998596V high limit: 0.5002196V
- %PASS Slot 17 channel 1 linearity at .75V on 6V range Measured: 0.7501077V low limit: 0.7498561V high limit: 0.7502161V
- %PASS Slot 17 channel 1 linearity at 1V on 6V range Measured: 0.9999930V low limit: 0.9998526V high limit: 1.000212V
- %PASS Slot 17 channel 1 linearity at 1.25V on 6V range
  Measured: 1.250036V low limit: 1.249849V high limit: 1.250209V
- %PASS Slot 17 channel 1 linearity at 1.5V on 6V range Measured: 1.499979V low limit: 1.499845V high limit: 1.500205V
- %PASS Slot 17 channel 1 linearity at 1.75V on 6V range
  Measured: 1.750079V low limit: 1.749842V high limit: 1.750202V
- %PASS Slot 17 channel 1 linearity at 2V on 6V range
  Measured: 1.999968V low limit: 1.999838V high limit: 2.000198V
- %PASS Slot 17 channel 1 linearity at 2.25V on 6V range Measured: 2.250023V low limit: 2.249835V high limit: 2.250195V
- %PASS Slot 17 channel 1 linearity at 2.5V on 6V range Measured: 2.500006V low limit: 2.499831V high limit: 2.500191V

- %PASS Slot 17 channel 1 linearity at 2.75V on 6V range Measured: 2.750075V low limit: 2.749828V high limit: 2.750188V
- %PASS Slot 17 channel 1 linearity at 3V on 6V range Measured: 2.999895V low limit: 2.999824V high limit: 3.000184V
- %PASS Slot 17 channel 1 linearity at 3.25V on 6V range Measured: 3.250004V low limit: 3.249821V high limit: 3.250181V
- %PASS Slot 17 channel 1 linearity at 3.5V on 6V range Measured: 3.500085V low limit: 3.499817V high limit: 3.500177V
- %PASS Slot 17 channel 1 linearity at 3.75V on 6V range Measured: 3.749997V low limit: 3.749814V high limit: 3.750174V
- %PASS Slot 17 channel 1 linearity at 4V on 6V range Measured: 4.000089V low limit: 3.999810V high limit: 4.000170V
- %PASS Slot 17 channel 1 linearity at 4.25V on 6V range Measured: 4.249948V low limit: 4.249807V high limit: 4.250167V
- %PASS Slot 17 channel 1 linearity at 4.5V on 6V range Measured: 4.499994V low limit: 4.499803V high limit: 4.500163V
- %PASS Slot 17 channel 1 linearity at 4.75V on 6V range Measured: 4.749973V low limit: 4.749800V high limit: 4.750160V
- %PASS Slot 17 channel 1 linearity at 5V on 6V range Measured: 5.000028V low limit: 4.999796V high limit: 5.000156V
- %PASS Slot 17 channel 1 linearity at 5.25V on 6V range Measured: 5.249907V low limit: 5.249793V high limit: 5.250153V
- %PASS Slot 17 channel 1 linearity at 5.5V on 6V range Measured: 5.499974V low limit: 5.499789V high limit: 5.500149V
- %PASS Slot 17 channel 1 linearity at 5.75V on 6V range Measured: 5.749911V low limit: 5.749786V high limit: 5.750146V

- %PASS Slot 17 channel 1 linearity at 6V on 6V range Measured: 5.999960V low limit: 5.999782V high limit: 6.000142V
- %PASS Slot 17 channel 1 maximum linearity error on 6V range Measured: 1.096107E-04V high limit: 0.00018V
- %PASS Slot 17 channel 2 linearity at 0V on 3V range

  Measured: 0.00005190542V low limit: -2.671741E-05V high limit: 1.532825E-04V
- %PASS Slot 17 channel 2 linearity at .125V on 3V range Measured: 0.1250697V low limit: 0.1249729V high limit: 0.1251529V
- %PASS Slot 17 channel 2 linearity at 0.25V on 3V range Measured: 0.2500402V low limit: 0.2499726V high limit: 0.2501526V
- %PASS Slot 17 channel 2 linearity at  $.375\mathrm{V}$  on 3V range Measured:  $0.3750514\mathrm{V}$  low limit:  $0.3749723\mathrm{V}$  high limit:  $0.3751523\mathrm{V}$
- %PASS Slot 17 channel 2 linearity at 0.5V on 3V range
  Measured: 0.5000752V low limit: 0.4999720V high limit: 0.5001520V
- %PASS Slot 17 channel 2 linearity at .625V on 3V range Measured: 0.6250873V low limit: 0.6249717V high limit: 0.6251517V
- %PASS Slot 17 channel 2 linearity at .75V on 3V range Measured: 0.7500603V low limit: 0.7499713V high limit: 0.7501513V
- %PASS Slot 17 channel 2 linearity at .875V on 3V range Measured: 0.8750835V low limit: 0.8749710V high limit: 0.8751510V
- %PASS Slot 17 channel 2 linearity at 1V on 3V range
  Measured: 1.000047V low limit: 0.9999707V high limit: 1.000150V
- %PASS Slot 17 channel 2 linearity at 1.125V on 3V range
  Measured: 1.125062V low limit: 1.124970V high limit: 1.125150V
- %PASS Slot 17 channel 2 linearity at 1.25V on 3V range

- Measured: 1.250033V low limit: 1.249970V high limit: 1.250150V
- %PASS Slot 17 channel 2 linearity at 1.375V on 3V range Measured: 1.375053V low limit: 1.374969V high limit: 1.375149V
- %PASS Slot 17 channel 2 linearity at 1.5V on 3V range Measured: 1.500011V low limit: 1.499969V high limit: 1.500149V
- %PASS Slot 17 channel 2 linearity at 1.625V on 3V range Measured: 1.625084V low limit: 1.624969V high limit: 1.625149V
- %PASS Slot 17 channel 2 linearity at 1.75V on 3V range Measured: 1.750103V low limit: 1.749968V high limit: 1.750148V
- %PASS Slot 17 channel 2 linearity at 1.875V on 3V range Measured: 1.875065V low limit: 1.874968V high limit: 1.875148V
- %PASS Slot 17 channel 2 linearity at 2V on 3V range Measured: 2.000089V low limit: 1.999968V high limit: 2.000148V
- %PASS Slot 17 channel 2 linearity at 2.125V on 3V range Measured: 2.125051V low limit: 2.124967V high limit: 2.125147V
- %PASS Slot 17 channel 2 linearity at 2.25V on 3V range Measured: 2.250072V low limit: 2.249967V high limit: 2.250147V
- %PASS Slot 17 channel 2 linearity at 2.375V on 3V range Measured: 2.375033V low limit: 2.374967V high limit: 2.375147V
- %PASS Slot 17 channel 2 linearity at 2.5V on 3V range Measured: 2.500043V low limit: 2.499966V high limit: 2.500146V
- %PASS Slot 17 channel 2 linearity at 2.625V on 3V range Measured: 2.625061V low limit: 2.624966V high limit: 2.625146V
- %PASS Slot 17 channel 2 linearity at 2.75V on 3V range Measured: 2.750069V low limit: 2.749966V high limit: 2.750146V

- %PASS Slot 17 channel 2 linearity at 2.875V on 3V range Measured: 2.875035V low limit: 2.874966V high limit: 2.875146V
- %PASS Slot 17 channel 2 linearity at 3V on 3V range Measured: 3.000047V low limit: 2.999965V high limit: 3.000145V
- %PASS Slot 17 channel 2 maximum linearity error on 3V range Measured: 4.841366E-05V high limit: 0.00009V
- %PASS Slot 17 channel 2 linearity at 0V on 6V range

  Measured: 0.0001348581V low limit: -5.398257E-05V high limit: 3.060174E-04V
- %PASS Slot 17 channel 2 linearity at .25V on 6V range Measured: 0.2501493V low limit: 0.2499443V high limit: 0.2503043V
- %PASS Slot 17 channel 2 linearity at .5V on 6V range Measured: 0.5000857V low limit: 0.4999426V high limit: 0.5003026V
- %PASS Slot 17 channel 2 linearity at .75V on 6V range Measured: 0.7501851V low limit: 0.7499410V high limit: 0.7503010V
- %PASS Slot 17 channel 2 linearity at  $\,$  1V on 6V range  $\mbox{Measured: } 1.000115 \mbox{V low limit: } 0.9999393 \mbox{V high limit: } 1.000299 \mbox{V}$
- %PASS Slot 17 channel 2 linearity at 1.25V on 6V range
  Measured: 1.250136V low limit: 1.249937V high limit: 1.250297V
- %PASS Slot 17 channel 2 linearity at 1.5V on 6V range
  Measured: 1.500072V low limit: 1.499936V high limit: 1.500296V
- %PASS Slot 17 channel 2 linearity at 1.75V on 6V range Measured: 1.750100V low limit: 1.749934V high limit: 1.750294V
- %PASS Slot 17 channel 2 linearity at 2V on 6V range
  Measured: 2.000120V low limit: 1.999932V high limit: 2.000292V
- %PASS Slot 17 channel 2 linearity at 2.25V on 6V range
  Measured: 2.250129V low limit: 2.249931V high limit: 2.250291V

- %PASS Slot 17 channel 2 linearity at 2.5V on 6V range Measured: 2.500055V low limit: 2.499929V high limit: 2.500289V
- %PASS Slot 17 channel 2 linearity at 2.75V on 6V range Measured: 2.750099V low limit: 2.749927V high limit: 2.750287V
- %PASS Slot 17 channel 2 linearity at 3V on 6V range Measured: 2.999998V low limit: 2.999926V high limit: 3.000286V
- %PASS Slot 17 channel 2 linearity at 3.25V on 6V range Measured: 3.250137V low limit: 3.249924V high limit: 3.250284V
- %PASS Slot 17 channel 2 linearity at 3.5V on 6V range Measured: 3.500157V low limit: 3.499922V high limit: 3.500282V
- %PASS Slot 17 channel 2 linearity at 3.75V on 6V range Measured: 3.750074V low limit: 3.749921V high limit: 3.750281V
- %PASS Slot 17 channel 2 linearity at 4V on 6V range Measured: 4.000113V low limit: 3.999919V high limit: 4.000279V
- %PASS Slot 17 channel 2 linearity at 4.25V on 6V range Measured: 4.250127V low limit: 4.249917V high limit: 4.250277V
- %PASS Slot 17 channel 2 linearity at 4.5V on 6V range Measured: 4.500162V low limit: 4.499916V high limit: 4.500276V
- %PASS Slot 17 channel 2 linearity at 4.75V on 6V range Measured: 4.750077V low limit: 4.749914V high limit: 4.750274V
- %PASS Slot 17 channel 2 linearity at 5V on 6V range Measured: 5.000089V low limit: 4.999912V high limit: 5.000272V
- %PASS Slot 17 channel 2 linearity at 5.25V on 6V range Measured: 5.250033V low limit: 5.249911V high limit: 5.250271V
- %PASS Slot 17 channel 2 linearity at 5.5V on 6V range

- Measured: 5.500137V low limit: 5.499909V high limit: 5.500269V
- %PASS Slot 17 channel 2 linearity at 5.75V on 6V range Measured: 5.750067V low limit: 5.749907V high limit: 5.750267V
- %PASS Slot 17 channel 2 linearity at 6V on 6V range Measured: 6.000090V low limit: 5.999906V high limit: 6.000266V
- %PASS Slot 17 channel 2 maximum linearity error on 6V range Measured: 1.080604E-04V high limit: 0.00018V
- %PASS Slot 17 channel 3 linearity at 0V on 3V range

  Measured: -0.00001784592V low limit: -8.495090E-05V high limit: 9.504909E-05V
- %PASS Slot 17 channel 3 linearity at .125V on 3V range Measured: 0.1249955V low limit: 0.1249163V high limit: 0.1250963V
- %PASS Slot 17 channel 3 linearity at .25V on 3V range Measured: 0.2500044V low limit: 0.2499175V high limit: 0.2500975V
- %PASS Slot 17 channel 3 linearity at .375V on 3V range Measured: 0.3750255V low limit: 0.3749188V high limit: 0.3750988V
- %PASS Slot 17 channel 3 linearity at 0.5V on 3V range Measured: 0.4999939V low limit: 0.4999200V high limit: 0.5001000V
- %PASS Slot 17 channel 3 linearity at .625V on 3V range Measured: 0.6250036V low limit: 0.6249213V high limit: 0.6251013V
- %PASS Slot 17 channel 3 linearity at .75V on 3V range Measured: 0.7500221V low limit: 0.7499226V high limit: 0.7501026V
- %PASS Slot 17 channel 3 linearity at  $\,$  .875V on 3V range Measured: 0.8750393V low limit: 0.8749238V high limit: 0.8751038V
- %PASS Slot 17 channel 3 linearity at 1V on 3V range
  Measured: 1.000006V low limit: 0.9999251V high limit: 1.000105V

- %PASS Slot 17 channel 3 linearity at 1.125V on 3V range
  Measured: 1.125008V low limit: 1.124926V high limit: 1.125106V
- %PASS Slot 17 channel 3 linearity at 1.25V on 3V range
  Measured: 1.250037V low limit: 1.249927V high limit: 1.250107V
- %PASS Slot 17 channel 3 linearity at 1.375V on 3V range Measured: 1.375042V low limit: 1.374928V high limit: 1.375108V
- %PASS Slot 17 channel 3 linearity at 1.5V on 3V range Measured: 1.500001V low limit: 1.499930V high limit: 1.500110V
- %PASS Slot 17 channel 3 linearity at 1.625V on 3V range
  Measured: 1.625022V low limit: 1.624931V high limit: 1.625111V
- %PASS Slot 17 channel 3 linearity at 1.75V on 3V range
  Measured: 1.750027V low limit: 1.749932V high limit: 1.750112V
- %PASS Slot 17 channel 3 linearity at 1.875V on 3V range Measured: 1.875042V low limit: 1.874933V high limit: 1.875113V
- %PASS Slot 17 channel 3 linearity at 2V on 3V range Measured: 2.000049V low limit: 1.999935V high limit: 2.000115V
- %PASS Slot 17 channel 3 linearity at 2.125V on 3V range Measured: 2.125014V low limit: 2.124936V high limit: 2.125116V
- %PASS Slot 17 channel 3 linearity at 2.25V on 3V range
  Measured: 2.250024V low limit: 2.249937V high limit: 2.250117V
- %PASS Slot 17 channel 3 linearity at 2.375V on 3V range Measured: 2.375027V low limit: 2.374939V high limit: 2.375119V
- %PASS Slot 17 channel 3 linearity at 2.5V on 3V range Measured: 2.500050V low limit: 2.499940V high limit: 2.500120V
- %PASS Slot 17 channel 3 linearity at 2.625V on 3V range
  Measured: 2.625013V low limit: 2.624941V high limit: 2.625121V

- %PASS Slot 17 channel 3 linearity at 2.75V on 3V range Measured: 2.750012V low limit: 2.749942V high limit: 2.750122V
- %PASS Slot 17 channel 3 linearity at 2.875V on 3V range Measured: 2.875023V low limit: 2.874944V high limit: 2.875124V
- %PASS Slot 17 channel 3 linearity at 3V on 3V range Measured: 3.000034V low limit: 2.999945V high limit: 3.000125V
- %PASS Slot 17 channel 3 maximum linearity error on 3V range Measured: 2.546137E-05V high limit: 0.00009V
- %PASS Slot 17 channel 3 linearity at 0V on 6V range

  Measured: 0.00003731455V low limit: -8.680420E-05V high limit: 2.731957E-04V
- %PASS Slot 17 channel 3 linearity at 0.25V on 6V range Measured: 0.2501340V low limit: 0.2499106V high limit: 0.2502706V
- %PASS Slot 17 channel 3 linearity at .5V on 6V range Measured: 0.5000187V low limit: 0.4999080V high limit: 0.5002680V
- %PASS Slot 17 channel 3 linearity at .75V on 6V range Measured: 0.7501317V low limit: 0.7499054V high limit: 0.7502654V
- %PASS Slot 17 channel 3 linearity at 1V on 6V range
  Measured: 1.000043V low limit: 0.9999028V high limit: 1.000262V
- %PASS Slot 17 channel 3 linearity at 1.25V on 6V range Measured: 1.250127V low limit: 1.249900V high limit: 1.250260V
- %PASS Slot 17 channel 3 linearity at 1.5V on 6V range Measured: 1.500042V low limit: 1.499897V high limit: 1.500257V
- %PASS Slot 17 channel 3 linearity at 1.75V on 6V range Measured: 1.750133V low limit: 1.749895V high limit: 1.750255V
- %PASS Slot 17 channel 3 linearity at 2V on 6V range

- Measured: 2.000045V low limit: 1.999892V high limit: 2.000252V
- %PASS Slot 17 channel 3 linearity at 2.25V on 6V range Measured: 2.250122V low limit: 2.249889V high limit: 2.250249V
- %PASS Slot 17 channel 3 linearity at 2.5V on 6V range Measured: 2.500041V low limit: 2.499887V high limit: 2.500247V
- %PASS Slot 17 channel 3 linearity at 2.75V on 6V range Measured: 2.750124V low limit: 2.749884V high limit: 2.750244V
- %PASS Slot 17 channel 3 linearity at 3V on 6V range Measured: 3.000020V low limit: 2.999882V high limit: 3.000242V
- %PASS Slot 17 channel 3 linearity at 3.25V on 6V range Measured: 3.250030V low limit: 3.249879V high limit: 3.250239V
- %PASS Slot 17 channel 3 linearity at 3.5V on 6V range Measured: 3.500112V low limit: 3.499877V high limit: 3.500237V
- %PASS Slot 17 channel 3 linearity at 3.75V on 6V range Measured: 3.750016V low limit: 3.749874V high limit: 3.750234V
- %PASS Slot 17 channel 3 linearity at 4V on 6V range Measured: 4.000110V low limit: 3.999871V high limit: 4.000231V
- %PASS Slot 17 channel 3 linearity at 4.25V on 6V range Measured: 4.250010V low limit: 4.249869V high limit: 4.250229V
- %PASS Slot 17 channel 3 linearity at 4.5V on 6V range Measured: 4.500091V low limit: 4.499866V high limit: 4.500226V
- %PASS Slot 17 channel 3 linearity at 4.75V on 6V range Measured: 4.749994V low limit: 4.749864V high limit: 4.750224V
- %PASS Slot 17 channel 3 linearity at 5V on 6V range Measured: 5.000092V low limit: 4.999861V high limit: 5.000221V

- %PASS Slot 17 channel 3 linearity at 5.25V on 6V range Measured: 5.249991V low limit: 5.249858V high limit: 5.250218V
- %PASS Slot 17 channel 3 linearity at 5.5V on 6V range Measured: 5.500066V low limit: 5.499856V high limit: 5.500216V
- %PASS Slot 17 channel 3 linearity at 5.75V on 6V range Measured: 5.749956V low limit: 5.749853V high limit: 5.750213V
- %PASS Slot 17 channel 3 linearity at 6V on 6V range Measured: 6.000057V low limit: 5.999851V high limit: 6.000211V
- %PASS Slot 17 channel 3 maximum linearity error on 6V range Measured: 7.748115E-05V high limit: 0.00018V
- %PASS Slot 17 channel 4 linearity at 0V on 3V range

  Measured: -0.00001639733V low limit: -1.144284E-04V high limit: 6.557158E-05V
- %PASS Slot 17 channel 4 linearity at .125V on 3V range Measured: 0.1249827V low limit: 0.1248875V high limit: 0.1250675V
- %PASS Slot 17 channel 4 linearity at 0.25V on 3V range Measured: 0.2499799V low limit: 0.2498894V high limit: 0.2500694V
- %PASS Slot 17 channel 4 linearity at .375V on 3V range Measured: 0.3749735V low limit: 0.3748913V high limit: 0.3750713V
- %PASS Slot 17 channel 4 linearity at .5V on 3V range Measured: 0.4999784V low limit: 0.4998932V high limit: 0.5000732V
- %PASS Slot 17 channel 4 linearity at .625V on 3V range Measured: 0.6249722V low limit: 0.6248952V high limit: 0.6250752V
- %PASS Slot 17 channel 4 linearity at .75V on 3V range Measured: 0.7499732V low limit: 0.7498971V high limit: 0.7500771V
- %PASS Slot 17 channel 4 linearity at ..875V on 3V range Measured: 0.8749811V low limit: 0.8748990V high limit: 0.8750790V

- %PASS Slot 17 channel 4 linearity at 1V on 3V range
  Measured: 0.9999751V low limit: 0.9999010V high limit: 1.000081V
- %PASS Slot 17 channel 4 linearity at 1.125V on 3V range Measured: 1.124982V low limit: 1.124902V high limit: 1.125082V
- %PASS Slot 17 channel 4 linearity at 1.25V on 3V range Measured: 1.249983V low limit: 1.249904V high limit: 1.250084V
- %PASS Slot 17 channel 4 linearity at 1.375V on 3V range Measured: 1.374983V low limit: 1.374906V high limit: 1.375086V
- %PASS Slot 17 channel 4 linearity at 1.5V on 3V range Measured: 1.499984V low limit: 1.499908V high limit: 1.500088V
- %PASS Slot 17 channel 4 linearity at 1.625V on 3V range Measured: 1.625035V low limit: 1.624910V high limit: 1.625090V
- %PASS Slot 17 channel 4 linearity at 1.75V on 3V range
  Measured: 1.750035V low limit: 1.749912V high limit: 1.750092V
- %PASS Slot 17 channel 4 linearity at 1.875V on 3V range Measured: 1.875034V low limit: 1.874914V high limit: 1.875094V
- %PASS Slot 17 channel 4 linearity at 2V on 3V range
  Measured: 2.000036V low limit: 1.999916V high limit: 2.000096V
- %PASS Slot 17 channel 4 linearity at 2.125V on 3V range Measured: 2.125027V low limit: 2.124918V high limit: 2.125098V
- %PASS Slot 17 channel 4 linearity at 2.25V on 3V range
  Measured: 2.250032V low limit: 2.249920V high limit: 2.250100V
- %PASS Slot 17 channel 4 linearity at 2.375V on 3V range Measured: 2.375024V low limit: 2.374922V high limit: 2.375102V
- %PASS Slot 17 channel 4 linearity at 2.5V on 3V range

- Measured: 2.500013V low limit: 2.499924V high limit: 2.500104V
- %PASS Slot 17 channel 4 linearity at 2.625V on 3V range Measured: 2.625018V low limit: 2.624926V high limit: 2.625106V
- %PASS Slot 17 channel 4 linearity at 2.75V on 3V range Measured: 2.749998V low limit: 2.749928V high limit: 2.750108V
- %PASS Slot 17 channel 4 linearity at 2.875V on 3V range Measured: 2.874992V low limit: 2.874929V high limit: 2.875109V
- %PASS Slot 17 channel 4 linearity at 3V on 3V range Measured: 2.999985V low limit: 2.999931V high limit: 3.000111V
- %PASS Slot 17 channel 4 maximum linearity error on 3V range Measured: 3.644857E-05V high limit: 0.00009V
- %PASS Slot 17 channel 4 linearity at 0V on 6V range

  Measured: 0.00003565027V low limit: -1.334341E-04V high limit: 2.265658E-04V
- %PASS Slot 17 channel 4 linearity at 0.25V on 6V range Measured: 0.2500805V low limit: 0.2498655V high limit: 0.2502255V
- %PASS Slot 17 channel 4 linearity at 0.5V on 6V range Measured: 0.5000272V low limit: 0.4998645V high limit: 0.5002245V
- %PASS Slot 17 channel 4 linearity at .75V on 6V range Measured: 0.7500600V low limit: 0.7498635V high limit: 0.7502235V
- %PASS Slot 17 channel 4 linearity at 1V on 6V range
  Measured: 1.000022V low limit: 0.9998624V high limit: 1.000222V
- %PASS Slot 17 channel 4 linearity at 1.25V on 6V range
  Measured: 1.250054V low limit: 1.249861V high limit: 1.250221V
- %PASS Slot 17 channel 4 linearity at 1.5V on 6V range Measured: 1.500012V low limit: 1.499860V high limit: 1.500220V

- %PASS Slot 17 channel 4 linearity at 1.75V on 6V range
  Measured: 1.750076V low limit: 1.749859V high limit: 1.750219V
- %PASS Slot 17 channel 4 linearity at 2V on 6V range Measured: 2.000014V low limit: 1.999858V high limit: 2.000218V
- %PASS Slot 17 channel 4 linearity at 2.25V on 6V range Measured: 2.250075V low limit: 2.249857V high limit: 2.250217V
- %PASS Slot 17 channel 4 linearity at 2.5V on 6V range Measured: 2.500029V low limit: 2.499856V high limit: 2.500216V
- %PASS Slot 17 channel 4 linearity at 2.75V on 6V range Measured: 2.750083V low limit: 2.749855V high limit: 2.750215V
- %PASS Slot 17 channel 4 linearity at 3V on 6V range Measured: 2.999944V low limit: 2.999854V high limit: 3.000214V
- %PASS Slot 17 channel 4 linearity at 3.25V on 6V range Measured: 3.249999V low limit: 3.249853V high limit: 3.250213V
- %PASS Slot 17 channel 4 linearity at 3.5V on 6V range Measured: 3.500047V low limit: 3.499852V high limit: 3.500212V
- %PASS Slot 17 channel 4 linearity at 3.75V on 6V range Measured: 3.750005V low limit: 3.749851V high limit: 3.750211V
- %PASS Slot 17 channel 4 linearity at  $\,$  4V on 6V range Measured: 4.000062V low limit: 3.999850V high limit: 4.000210V
- %PASS Slot 17 channel 4 linearity at 4.25V on 6V range Measured: 4.250004V low limit: 4.249849V high limit: 4.250209V
- %PASS Slot 17 channel 4 linearity at 4.5V on 6V range Measured: 4.500068V low limit: 4.499848V high limit: 4.500208V
- %PASS Slot 17 channel 4 linearity at 4.75V on 6V range Measured: 4.750006V low limit: 4.749847V high limit: 4.750207V

- %PASS Slot 17 channel 4 linearity at 5V on 6V range Measured: 5.000051V low limit: 4.999846V high limit: 5.000206V
- %PASS Slot 17 channel 4 linearity at 5.25V on 6V range Measured: 5.250016V low limit: 5.249845V high limit: 5.250205V
- %PASS Slot 17 channel 4 linearity at 5.5V on 6V range Measured: 5.500039V low limit: 5.499844V high limit: 5.500204V
- %PASS Slot 17 channel 4 linearity at 5.75V on 6V range Measured: 5.749992V low limit: 5.749843V high limit: 5.750203V
- %PASS Slot 17 channel 4 linearity at 6V on 6V range Measured: 6.000044V low limit: 5.999842V high limit: 6.000202V
- %PASS Slot 17 channel 4 maximum linearity error on 6V range Measured: 8.953988E-05V high limit: 0.00018V
- %PASS Slot 17 channel 5 linearity at 0V on 3V range

  Measured: 0.000005450027V low limit: -7.238444E-05V high limit: 1.076155E-04V
- %PASS Slot 17 channel 5 linearity at 0.25V on 3V range Measured: 0.2499883V low limit: 0.2499288V high limit: 0.2501088V
- %PASS Slot 17 channel 5 linearity at 375V on 3V range
  Measured: 0.3750222V low limit: 0.3749293V high limit: 0.3751093V
- %PASS Slot 17 channel 5 linearity at .5V on 3V range Measured: 0.5000238V low limit: 0.4999299V high limit: 0.5001099V
- %PASS Slot 17 channel 5 linearity at .625V on 3V range Measured: 0.6250138V low limit: 0.6249305V high limit: 0.6251105V
- %PASS Slot 17 channel 5 linearity at .75V on 3V range

- Measured: 0.7500136V low limit: 0.7499311V high limit: 0.7501111V
- %PASS Slot 17 channel 5 linearity at .875V on 3V range Measured: 0.8750123V low limit: 0.8749317V high limit: 0.8751117V
- %PASS Slot 17 channel 5 linearity at 1V on 3V range
  Measured: 1.000000V low limit: 0.9999323V high limit: 1.000112V
- %PASS Slot 17 channel 5 linearity at 1.125V on 3V range Measured: 1.125050V low limit: 1.124932V high limit: 1.125112V
- %PASS Slot 17 channel 5 linearity at 1.25V on 3V range Measured: 1.250001V low limit: 1.249933V high limit: 1.250113V
- %PASS Slot 17 channel 5 linearity at 1.375V on 3V range Measured: 1.375038V low limit: 1.374934V high limit: 1.375114V
- %PASS Slot 17 channel 5 linearity at 1.5V on 3V range Measured: 1.499996V low limit: 1.499934V high limit: 1.500114V
- %PASS Slot 17 channel 5 linearity at 1.625V on 3V range Measured: 1.625033V low limit: 1.624935V high limit: 1.625115V
- %PASS Slot 17 channel 5 linearity at 1.75V on 3V range Measured: 1.750072V low limit: 1.749935V high limit: 1.750115V
- %PASS Slot 17 channel 5 linearity at 1.875V on 3V range Measured: 1.875030V low limit: 1.874936V high limit: 1.875116V
- %PASS Slot 17 channel 5 linearity at 2V on 3V range
  Measured: 2.000058V low limit: 1.999937V high limit: 2.000117V
- %PASS Slot 17 channel 5 linearity at 2.125V on 3V range Measured: 2.125043V low limit: 2.124937V high limit: 2.125117V
- %PASS Slot 17 channel 5 linearity at 2.25V on 3V range Measured: 2.250049V low limit: 2.249938V high limit: 2.250118V

- %PASS Slot 17 channel 5 linearity at 2.375V on 3V range Measured: 2.375034V low limit: 2.374938V high limit: 2.375118V
- %PASS Slot 17 channel 5 linearity at 2.5V on 3V range Measured: 2.500024V low limit: 2.499939V high limit: 2.500119V
- %PASS Slot 17 channel 5 linearity at 2.625V on 3V range Measured: 2.625019V low limit: 2.624940V high limit: 2.625120V
- %PASS Slot 17 channel 5 linearity at 2.75V on 3V range Measured: 2.750039V low limit: 2.749940V high limit: 2.750120V
- %PASS Slot 17 channel 5 linearity at 2.875V on 3V range Measured: 2.874979V low limit: 2.874941V high limit: 2.875121V
- %PASS Slot 17 channel 5 linearity at 3V on 3V range Measured: 3.000020V low limit: 2.999941V high limit: 3.000121V
- %PASS Slot 17 channel 5 maximum linearity error on 3V range Measured: 5.129102E-05V high limit: 0.00009V
- %PASS Slot 17 channel 5 linearity at 0V on 6V range

  Measured: -0.00001790804V low limit: -1.719853E-04V high limit: 1.880146E-04V
- %PASS Slot 17 channel 5 linearity at 0.25V on 6V range Measured: 0.2500349V low limit: 0.2498243V high limit: 0.2501843V
- %PASS Slot 17 channel 5 linearity at .5V on 6V range Measured: 0.4999858V low limit: 0.4998206V high limit: 0.5001806V
- %PASS Slot 17 channel 5 linearity at .75V on 6V range Measured: 0.7500288V low limit: 0.7498169V high limit: 0.7501769V
- %PASS Slot 17 channel 5 linearity at 1V on 6V range Measured: 1.000006V low limit: 0.9998132V high limit: 1.000173V
- %PASS Slot 17 channel 5 linearity at 1.25V on 6V range Measured: 1.249962V low limit: 1.249809V high limit: 1.250169V

- %PASS Slot 17 channel 5 linearity at 1.5V on 6V range Measured: 1.499935V low limit: 1.499805V high limit: 1.500165V
- %PASS Slot 17 channel 5 linearity at 1.75V on 6V range Measured: 1.750009V low limit: 1.749802V high limit: 1.750162V
- %PASS Slot 17 channel 5 linearity at 2V on 6V range
  Measured: 1.999950V low limit: 1.999798V high limit: 2.000158V
- %PASS Slot 17 channel 5 linearity at 2.25V on 6V range Measured: 2.250024V low limit: 2.249794V high limit: 2.250154V
- %PASS Slot 17 channel 5 linearity at 2.5V on 6V range Measured: 2.499898V low limit: 2.499791V high limit: 2.500151V
- %PASS Slot 17 channel 5 linearity at 2.75V on 6V range Measured: 2.749950V low limit: 2.749787V high limit: 2.750147V
- %PASS Slot 17 channel 5 linearity at 3V on 6V range Measured: 2.999943V low limit: 2.999783V high limit: 3.000143V
- %PASS Slot 17 channel 5 linearity at 3.25V on 6V range Measured: 3.249995V low limit: 3.249780V high limit: 3.250140V
- %PASS Slot 17 channel 5 linearity at 3.5V on 6V range Measured: 3.500053V low limit: 3.499776V high limit: 3.500136V
- %PASS Slot 17 channel 5 linearity at 3.75V on 6V range Measured: 3.749926V low limit: 3.749772V high limit: 3.750132V
- %PASS Slot 17 channel 5 linearity at 4V on 6V range Measured: 3.999973V low limit: 3.999769V high limit: 4.000129V
- %PASS Slot 17 channel 5 linearity at 4.25V on 6V range Measured: 4.249927V low limit: 4.249765V high limit: 4.250125V
- %PASS Slot 17 channel 5 linearity at 4.5V on 6V range

- Measured: 4.500011V low limit: 4.499761V high limit: 4.500121V
- %PASS Slot 17 channel 5 linearity at 4.75V on 6V range Measured: 4.749969V low limit: 4.749758V high limit: 4.750118V
- %PASS Slot 17 channel 5 linearity at 5V on 6V range Measured: 4.999904V low limit: 4.999754V high limit: 5.000114V
- %PASS Slot 17 channel 5 linearity at 5.25V on 6V range Measured: 5.249890V low limit: 5.249750V high limit: 5.250110V
- %PASS Slot 17 channel 5 linearity at 5.5V on 6V range Measured: 5.499909V low limit: 5.499747V high limit: 5.500107V
- %PASS Slot 17 channel 5 linearity at 5.75V on 6V range Measured: 5.749875V low limit: 5.749743V high limit: 5.750103V
- %PASS Slot 17 channel 5 linearity at 6V on 6V range Measured: 5.999943V low limit: 5.999739V high limit: 6.000099V
- %PASS Slot 17 channel 5 maximum linearity error on 6V range Measured: 9.729501E-05V high limit: 0.00018V
- %PASS Slot 17 channel 6 linearity at 0V on 3V range

  Measured: -0.00002481497V low limit: -9.864394E-05V high limit: 8.135605E-05V
- %PASS Slot 17 channel 6 linearity at .125V on 3V range Measured: 0.1250318V low limit: 0.1249004V high limit: 0.1250804V
- %PASS Slot 17 channel 6 linearity at .25V on 3V range Measured: 0.2499963V low limit: 0.2498996V high limit: 0.2500796V
- %PASS Slot 17 channel 6 linearity at 375V on 3V range Measured: 0.3749906V low limit: 0.3748987V high limit: 0.3750787V
- %PASS Slot 17 channel 6 linearity at .5V on 3V range Measured: 0.4999664V low limit: 0.4998978V high limit: 0.5000778V

- %PASS Slot 17 channel 6 linearity at ..625V on 3V range Measured: 0.6249734V low limit: 0.6248969V high limit: 0.6250769V
- %PASS Slot 17 channel 6 linearity at .75V on 3V range Measured: 0.7499436V low limit: 0.7498960V high limit: 0.7500760V
- %PASS Slot 17 channel 6 linearity at .875V on 3V range Measured: 0.8750080V low limit: 0.8748952V high limit: 0.8750752V
- %PASS Slot 17 channel 6 linearity at 1V on 3V range Measured: 0.9999743V low limit: 0.9998943V high limit: 1.000074V
- %PASS Slot 17 channel 6 linearity at 1.125V on 3V range
  Measured: 1.124994V low limit: 1.124893V high limit: 1.125073V
- %PASS Slot 17 channel 6 linearity at 1.25V on 3V range Measured: 1.249952V low limit: 1.249892V high limit: 1.250072V
- %PASS Slot 17 channel 6 linearity at 1.375V on 3V range Measured: 1.374976V low limit: 1.374891V high limit: 1.375071V
- %PASS Slot 17 channel 6 linearity at 1.5V on 3V range Measured: 1.499940V low limit: 1.499890V high limit: 1.500070V
- %PASS Slot 17 channel 6 linearity at 1.625V on 3V range Measured: 1.625002V low limit: 1.624889V high limit: 1.625069V
- %PASS Slot 17 channel 6 linearity at 1.75V on 3V range Measured: 1.750026V low limit: 1.749889V high limit: 1.750069V
- %PASS Slot 17 channel 6 linearity at 1.875V on 3V range Measured: 1.874991V low limit: 1.874888V high limit: 1.875068V
- %PASS Slot 17 channel 6 linearity at 2V on 3V range Measured: 1.999999V low limit: 1.999887V high limit: 2.000067V
- %PASS Slot 17 channel 6 linearity at 2.125V on 3V range
  Measured: 2.124962V low limit: 2.124886V high limit: 2.125066V

- %PASS Slot 17 channel 6 linearity at 2.25V on 3V range
  Measured: 2.250031V low limit: 2.249885V high limit: 2.250065V
- %PASS Slot 17 channel 6 linearity at 2.375V on 3V range Measured: 2.374985V low limit: 2.374884V high limit: 2.375064V
- %PASS Slot 17 channel 6 linearity at 2.5V on 3V range Measured: 2.499992V low limit: 2.499883V high limit: 2.500063V
- %PASS Slot 17 channel 6 linearity at 2.625V on 3V range Measured: 2.624966V low limit: 2.624882V high limit: 2.625062V
- %PASS Slot 17 channel 6 linearity at 2.75V on 3V range Measured: 2.749952V low limit: 2.749882V high limit: 2.750062V
- %PASS Slot 17 channel 6 linearity at 2.875V on 3V range Measured: 2.874917V low limit: 2.874881V high limit: 2.875061V
- %PASS Slot 17 channel 6 linearity at 3V on 3V range Measured: 2.999969V low limit: 2.999880V high limit: 3.000060V
- %PASS Slot 17 channel 6 maximum linearity error on 3V range Measured: 5.567531E-05V high limit: 0.00009V
- %PASS Slot 17 channel 6 linearity at 0V on 6V range

  Measured: 0.000008372576V low limit: -2.369816E-04V high limit: 1.230183E-04V
- %PASS Slot 17 channel 6 linearity at 0.25V on 6V range Measured: 0.2500111V low limit: 0.2497594V high limit: 0.2501194V
- %PASS Slot 17 channel 6 linearity at .5V on 6V range Measured: 0.4999254V low limit: 0.4997558V high limit: 0.5001158V
- %PASS Slot 17 channel 6 linearity at .75V on 6V range Measured: 0.7499019V low limit: 0.7497522V high limit: 0.7501122V
- %PASS Slot 17 channel 6 linearity at 1V on 6V range

- Measured: 0.9999361V low limit: 0.9997486V high limit: 1.000108V
- %PASS Slot 17 channel 6 linearity at 1.25V on 6V range
  Measured: 1.249935V low limit: 1.249745V high limit: 1.250105V
- %PASS Slot 17 channel 6 linearity at 1.5V on 6V range Measured: 1.499851V low limit: 1.499741V high limit: 1.500101V
- %PASS Slot 17 channel 6 linearity at 1.75V on 6V range Measured: 1.749893V low limit: 1.749737V high limit: 1.750097V
- %PASS Slot 17 channel 6 linearity at 2V on 6V range
  Measured: 1.999901V low limit: 1.999734V high limit: 2.000094V
- %PASS Slot 17 channel 6 linearity at 2.25V on 6V range Measured: 2.249924V low limit: 2.249730V high limit: 2.250090V
- %PASS Slot 17 channel 6 linearity at 2.5V on 6V range Measured: 2.499840V low limit: 2.499727V high limit: 2.500087V
- %PASS Slot 17 channel 6 linearity at 2.75V on 6V range Measured: 2.749874V low limit: 2.749723V high limit: 2.750083V
- %PASS Slot 17 channel 6 linearity at 3V on 6V range Measured: 2.999796V low limit: 2.999719V high limit: 3.000079V
- %PASS Slot 17 channel 6 linearity at 3.25V on 6V range Measured: 3.249909V low limit: 3.249716V high limit: 3.250076V
- %PASS Slot 17 channel 6 linearity at 3.5V on 6V range Measured: 3.499943V low limit: 3.499712V high limit: 3.500072V
- %PASS Slot 17 channel 6 linearity at 3.75V on 6V range Measured: 3.749866V low limit: 3.749709V high limit: 3.750069V
- %PASS Slot 17 channel 6 linearity at 4V on 6V range Measured: 3.999970V low limit: 3.999705V high limit: 4.000065V

- %PASS Slot 17 channel 6 linearity at 4.25V on 6V range Measured: 4.249889V low limit: 4.249701V high limit: 4.250061V
- %PASS Slot 17 channel 6 linearity at 4.5V on 6V range Measured: 4.499930V low limit: 4.499698V high limit: 4.500058V
- %PASS Slot 17 channel 6 linearity at 4.75V on 6V range Measured: 4.749832V low limit: 4.749694V high limit: 4.750054V
- %PASS Slot 17 channel 6 linearity at 5V on 6V range Measured: 4.999940V low limit: 4.999691V high limit: 5.000051V
- %PASS Slot 17 channel 6 linearity at 5.25V on 6V range Measured: 5.249883V low limit: 5.249687V high limit: 5.250047V
- %PASS Slot 17 channel 6 linearity at 5.5V on 6V range Measured: 5.499855V low limit: 5.499684V high limit: 5.500044V
- %PASS Slot 17 channel 6 linearity at 5.75V on 6V range Measured: 5.749783V low limit: 5.749680V high limit: 5.750040V
- %PASS Slot 17 channel 6 linearity at 6V on 6V range Measured: 5.999891V low limit: 5.999676V high limit: 6.000036V
- %PASS Slot 17 channel 6 maximum linearity error on 6V range Measured: 1.036442E-04V high limit: 0.00018V
- %PASS Slot 17 channel 7 linearity at 0V on 3V range

  Measured: 0.0000006431356V low limit: -1.066943E-04V high limit: 7.330562E-05V
- %PASS Slot 17 channel 7 linearity at .125V on 3V range Measured: 0.1250042V low limit: 0.1248940V high limit: 0.1250740V
- %PASS Slot 17 channel 7 linearity at 0.25V on 3V range Measured: 0.2499556V low limit: 0.2498947V high limit: 0.2500747V
- %PASS Slot 17 channel 7 linearity at  $.375\mathrm{V}$  on 3V range Measured:  $0.3750061\mathrm{V}$  low limit:  $0.3748954\mathrm{V}$  high limit:  $0.3750754\mathrm{V}$

- %PASS Slot 17 channel 7 linearity at 0.5V on 3V range Measured: 0.4999669V low limit: 0.4998960V high limit: 0.5000760V
- %PASS Slot 17 channel 7 linearity at .625V on 3V range Measured: 0.6249698V low limit: 0.6248967V high limit: 0.6250767V
- %PASS Slot 17 channel 7 linearity at .75V on 3V range Measured: 0.7499807V low limit: 0.7498974V high limit: 0.7500774V
- %PASS Slot 17 channel 7 linearity at  $\,$  .875V on 3V range Measured: 0.8749953V low limit: 0.8748981V high limit: 0.8750781V
- %PASS Slot 17 channel 7 linearity at 1V on 3V range Measured: 0.9999522V low limit: 0.9998988V high limit: 1.000078V
- %PASS Slot 17 channel 7 linearity at 1.125V on 3V range
  Measured: 1.125012V low limit: 1.124899V high limit: 1.125079V
- %PASS Slot 17 channel 7 linearity at 1.25V on 3V range Measured: 1.249978V low limit: 1.249900V high limit: 1.250080V
- %PASS Slot 17 channel 7 linearity at 1.375V on 3V range Measured: 1.374988V low limit: 1.374900V high limit: 1.375080V
- %PASS Slot 17 channel 7 linearity at 1.5V on 3V range Measured: 1.499953V low limit: 1.499901V high limit: 1.500081V
- %PASS Slot 17 channel 7 linearity at 1.625V on 3V range Measured: 1.625011V low limit: 1.624902V high limit: 1.625082V
- %PASS Slot 17 channel 7 linearity at 1.75V on 3V range
  Measured: 1.750021V low limit: 1.749903V high limit: 1.750083V
- %PASS Slot 17 channel 7 linearity at 1.875V on 3V range Measured: 1.874984V low limit: 1.874903V high limit: 1.875083V
- %PASS Slot 17 channel 7 linearity at 2V on 3V range

- Measured: 2.000037V low limit: 1.999904V high limit: 2.000084V
- %PASS Slot 17 channel 7 linearity at 2.125V on 3V range Measured: 2.124997V low limit: 2.124905V high limit: 2.125085V
- %PASS Slot 17 channel 7 linearity at 2.25V on 3V range Measured: 2.250009V low limit: 2.249905V high limit: 2.250085V
- %PASS Slot 17 channel 7 linearity at 2.375V on 3V range Measured: 2.375009V low limit: 2.374906V high limit: 2.375086V
- %PASS Slot 17 channel 7 linearity at 2.5V on 3V range Measured: 2.500014V low limit: 2.499907V high limit: 2.500087V
- %PASS Slot 17 channel 7 linearity at 2.625V on 3V range Measured: 2.624974V low limit: 2.624907V high limit: 2.625087V
- %PASS Slot 17 channel 7 linearity at 2.75V on 3V range Measured: 2.750020V low limit: 2.749908V high limit: 2.750088V
- %PASS Slot 17 channel 7 linearity at 2.875V on 3V range Measured: 2.874972V low limit: 2.874909V high limit: 2.875089V
- %PASS Slot 17 channel 7 linearity at 3V on 3V range Measured: 2.999974V low limit: 2.999910V high limit: 3.000090V
- %PASS Slot 17 channel 7 maximum linearity error on 3V range Measured: 4.346831E-05V high limit: 0.00009V
- %PASS Slot 17 channel 7 linearity at 0V on 6V range

  Measured: -0.00004639020V low limit: -3.106733E-04V high limit: 4.932667E-05V
- %PASS Slot 17 channel 7 linearity at 0.25V on 6V range Measured: 0.2499312V low limit: 0.2496951V high limit: 0.2500551V
- %PASS Slot 17 channel 7 linearity at .5V on 6V range Measured: 0.4999000V low limit: 0.4997010V high limit: 0.5000610V

- %PASS Slot 17 channel 7 linearity at 1V on 6V range
  Measured: 0.9998716V low limit: 0.9997127V high limit: 1.000072V
- %PASS Slot 17 channel 7 linearity at 1.25V on 6V range Measured: 1.249854V low limit: 1.249718V high limit: 1.250078V
- %PASS Slot 17 channel 7 linearity at 1.5V on 6V range Measured: 1.499850V low limit: 1.499724V high limit: 1.500084V
- %PASS Slot 17 channel 7 linearity at 1.75V on 6V range Measured: 1.749948V low limit: 1.749730V high limit: 1.750090V
- %PASS Slot 17 channel 7 linearity at 2V on 6V range Measured: 1.999836V low limit: 1.999736V high limit: 2.000096V
- %PASS Slot 17 channel 7 linearity at 2.25V on 6V range
  Measured: 2.249936V low limit: 2.249741V high limit: 2.250101V
- %PASS Slot 17 channel 7 linearity at 2.5V on 6V range Measured: 2.499842V low limit: 2.499747V high limit: 2.500107V
- %PASS Slot 17 channel 7 linearity at 2.75V on 6V range Measured: 2.749934V low limit: 2.749753V high limit: 2.750113V
- %PASS Slot 17 channel 7 linearity at  $\,$  3V on 6V range Measured: 2.999836V low limit: 2.999759V high limit: 3.000119V
- %PASS Slot 17 channel 7 linearity at 3.25V on 6V range Measured: 3.249936V low limit: 3.249765V high limit: 3.250125V
- %PASS Slot 17 channel 7 linearity at 3.5V on 6V range Measured: 3.500027V low limit: 3.499771V high limit: 3.500131V
- %PASS Slot 17 channel 7 linearity at 3.75V on 6V range Measured: 3.749933V low limit: 3.749777V high limit: 3.750137V

- %PASS Slot 17 channel 7 linearity at 4V on 6V range Measured: 4.000031V low limit: 3.999782V high limit: 4.000142V
- %PASS Slot 17 channel 7 linearity at 4.25V on 6V range Measured: 4.249926V low limit: 4.249788V high limit: 4.250148V
- %PASS Slot 17 channel 7 linearity at 4.5V on 6V range Measured: 4.500035V low limit: 4.499794V high limit: 4.500154V
- %PASS Slot 17 channel 7 linearity at 4.75V on 6V range Measured: 4.750017V low limit: 4.749800V high limit: 4.750160V
- %PASS Slot 17 channel 7 linearity at 5V on 6V range Measured: 5.000009V low limit: 4.999806V high limit: 5.000166V
- %PASS Slot 17 channel 7 linearity at 5.25V on 6V range Measured: 5.250017V low limit: 5.249812V high limit: 5.250172V
- %PASS Slot 17 channel 7 linearity at 5.5V on 6V range Measured: 5.500001V low limit: 5.499817V high limit: 5.500177V
- %PASS Slot 17 channel 7 linearity at 5.75V on 6V range Measured: 5.749987V low limit: 5.749823V high limit: 5.750183V
- %PASS Slot 17 channel 7 linearity at 6V on 6V range Measured: 5.999984V low limit: 5.999829V high limit: 6.000189V
- %PASS Slot 17 channel 7 maximum linearity error on 6V range Measured: 1.026691E-04V high limit: 0.00018V
- ...checking DAC code transitions...
- %PASS Slot 17 channel 0 raw DAC codes linearity at 3.05V on 3V range Measured: 3.049420V low limit: 3.049342V high limit: 3.049522V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 3.04995269703212V on 3V range Measured: 3.049383V low limit: 3.049294V high limit: 3.049474V

- %PASS Slot 17 channel 0 raw DAC codes linearity at 3.04990539406424V on 3V range Measured: 3.049336V low limit: 3.049247V high limit: 3.049427V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 3.04985809109636V on 3V range Measured: 3.049294V low limit: 3.049200V high limit: 3.049380V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 3.04981078812848V on 3V range Measured: 3.049240V low limit: 3.049152V high limit: 3.049332V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 3.04966887922484V on 3V range Measured: 3.049106V low limit: 3.049011V high limit: 3.049191V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 3.04962157625696V on 3V range Measured: 3.049052V low limit: 3.048963V high limit: 3.049143V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 3.0492904554818V on 3V range Measured: 3.048726V low limit: 3.048632V high limit: 3.048812V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 3.04924315251392V on 3V range Measured: 3.048669V low limit: 3.048585V high limit: 3.048765V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 3.04853360799573V on 3V range Measured: 3.047975V low limit: 3.047876V high limit: 3.048056V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 3.04848630502785V on 3V range Measured: 3.047915V low limit: 3.047828V high limit: 3.048008V
- %PASS Slot 17 channel 0 raw DAC codes linearity at  $\,$  3.04701991302358V on 3V range Measured: 3.046461V low limit: 3.046363V high limit: 3.046543V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 3.0469726100557V on 3V range Measured: 3.046402V low limit: 3.046315V high limit: 3.046495V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 3.04399252307927V on 3V range Measured: 3.043434V low limit: 3.043336V high limit: 3.043516V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 3.04394522011139V on 3V range Measured: 3.043375V low limit: 3.043289V high limit: 3.043469V

- %PASS Slot 17 channel 0 raw DAC codes linearity at 3.03793774319066V on 3V range Measured: 3.037379V low limit: 3.037284V high limit: 3.037464V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 3.03789044022278V on 3V range Measured: 3.037321V low limit: 3.037237V high limit: 3.037417V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 3.02582818341344V on 3V range Measured: 3.025272V low limit: 3.025179V high limit: 3.025359V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 3.02578088044556V on 3V range Measured: 3.025220V low limit: 3.025132V high limit: 3.025312V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 3.00160906385901V on 3V range Measured: 3.001064V low limit: 3.000970V high limit: 3.001150V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 3.00156176089113V on 3V range Measured: 3.001001V low limit: 3.000922V high limit: 3.001102V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 2.95317082475013V on 3V range Measured: 2.952640V low limit: 2.952550V high limit: 2.952730V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 2.95312352178225V on 3V range Measured: 2.952580V low limit: 2.952503V high limit: 2.952683V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 2.85629434653239V on 3V range Measured: 2.855798V low limit: 2.855712V high limit: 2.855892V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 2.85624704356451V on 3V range Measured: 2.855745V low limit: 2.855665V high limit: 2.855845V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 2.66254139009689V on 3V range Measured: 2.662125V low limit: 2.662035V high limit: 2.662215V
- %PASS Slot 17 channel 0 raw DAC codes linearity at  $\,$  2.66249408712902V on 3V range Measured: 2.662078V low limit: 2.661988V high limit: 2.662168V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 2.27503547722591V on 3V range

- Measured: 2.274782V low limit: 2.274681V high limit: 2.274861V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 2.27498817425803V on 3V range Measured: 2.274732V low limit: 2.274634V high limit: 2.274814V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 1.50002365148394V on 3V range Measured: 1.500083V low limit: 1.499974V high limit: 1.500154V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 1.49997634851606V on 3V range Measured: 1.500027V low limit: 1.499927V high limit: 1.500107V
- %PASS Slot 17 channel 0 raw DAC codes linearity at -.05V on 3V range
  Measured: -0.04936812V low limit: -4.944047E-02V high limit: -4.926047E-02V
- %PASS Slot 17 channel 0 raw DAC codes maximum linearity error on 3V range Measured: 1.877437E-05V high limit: 0.00009V
- %PASS Slot 17 channel 0 raw DAC code binary transition 0 to 1 on 3V range Measured: 3.704100E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 1 to 2 on 3V range Measured: 4.698000E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 2 to 3 on 3V range Measured: 4.273399E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 3 to 4 on 3V range Measured: 5.407299E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 7 to 8 on 3V range Measured: 5.473000E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 15 to 16 on 3V range Measured: 5.731399E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 31 to 32 on 3V range Measured: 5.963500E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

- %PASS Slot 17 channel 0 raw DAC code binary transition 63 to 64 on 3V range Measured: 5.897700E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 127 to 128 on 3V range Measured: 5.932799E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 255 to 256 on 3V range Measured: 5.867100E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 511 to 512 on 3V range Measured: 5.210399E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 1023 to 1024 on 3V range Measured: 6.287500E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 2047 to 2048 on 3V range Measured: 6.068499E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 4095 to 4096 on 3V range Measured: 5.385499E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 8191 to 8192 on 3V range Measured: 4.628000E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 16383 to 16384 on 3V range Measured: 5.004499E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 32767 to 32768 on 3V range Measured: 5.608800E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transitions maximum difference on 3V range Measured: 6.287500E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transitions minimum difference on 3V range Measured: 3.704100E-05V low limit: -4.269775E-05V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.1V on 6V range Measured: 6.098391V low limit: 6.098241V high limit: 6.098601V

- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.09990539406424V on 6V range Measured: 6.098321V low limit: 6.098146V high limit: 6.098506V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.09981078812848V on 6V range Measured: 6.098225V low limit: 6.098052V high limit: 6.098412V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.09971618219272V on 6V range Measured: 6.098146V low limit: 6.097957V high limit: 6.098317V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.09962157625696V on 6V range Measured: 6.098037V low limit: 6.097862V high limit: 6.098222V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.09933775844968V on 6V range Measured: 6.097771V low limit: 6.097579V high limit: 6.097939V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.09924315251392V on 6V range Measured: 6.097659V low limit: 6.097484V high limit: 6.097844V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.09858091096361V on 6V range Measured: 6.097017V low limit: 6.096822V high limit: 6.097182V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.09848630502785V on 6V range Measured: 6.096894V low limit: 6.096728V high limit: 6.097088V
- %PASS Slot 17 channel 0 raw DAC codes linearity at  $\,$  6.09706721599146V on 6V range Measured: 6.095505V low limit: 6.095309V high limit: 6.095669V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.0969726100557V on 6V range Measured: 6.095391V low limit: 6.095215V high limit: 6.095575V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.09403982604715V on 6V range Measured: 6.092482V low limit: 6.092283V high limit: 6.092643V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.09394522011139V on 6V range Measured: 6.092366V low limit: 6.092189V high limit: 6.092549V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.08798504615854V on 6V range

- Measured: 6.086435V low limit: 6.086231V high limit: 6.086591V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.08789044022278V on 6V range Measured: 6.086308V low limit: 6.086137V high limit: 6.086497V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.07587548638132V on 6V range Measured: 6.074321V low limit: 6.074127V high limit: 6.074487V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.07578088044556V on 6V range Measured: 6.074205V low limit: 6.074032V high limit: 6.074392V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.05165636682689V on 6V range Measured: 6.050114V low limit: 6.049919V high limit: 6.050279V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.05156176089113V on 6V range Measured: 6.050005V low limit: 6.049824V high limit: 6.050184V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.00321812771801V on 6V range Measured: 6.001693V low limit: 6.001502V high limit: 6.001862V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.00312352178225V on 6V range Measured: 6.001568V low limit: 6.001407V high limit: 6.001767V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 5.90634164950027V on 6V range Measured: 5.904853V low limit: 5.904669V high limit: 5.905029V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 5.90624704356451V on 6V range Measured: 5.904728V low limit: 5.904574V high limit: 5.904934V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 5.71258869306477V on 6V range Measured: 5.711173V low limit: 5.711002V high limit: 5.711362V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 5.71249408712902V on 6V range Measured: 5.711063V low limit: 5.710907V high limit: 5.711267V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 5.32508278019379V on 6V range Measured: 5.323840V low limit: 5.323668V high limit: 5.324028V

- %PASS Slot 17 channel 0 raw DAC codes linearity at 5.32498817425803V on 6V range Measured: 5.323749V low limit: 5.323574V high limit: 5.323934V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 4.55007095445182V on 6V range Measured: 4.549194V low limit: 4.549002V high limit: 4.549362V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 4.54997634851606V on 6V range Measured: 4.549098V low limit: 4.548907V high limit: 4.549267V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 3.00004730296788V on 6V range Measured: 2.999876V low limit: 2.999668V high limit: 3.000028V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 2.99995269703212V on 6V range Measured: 2.999760V low limit: 2.999574V high limit: 2.999934V
- %PASS Slot 17 channel 0 raw DAC codes linearity at -.1V on 6V range

  Measured: -0.09883692V low limit: -0.09899843V high limit: -0.09863843V
- %PASS Slot 17 channel 0 raw DAC codes maximum linearity error on 6V range Measured: 2.982594E-05V high limit: 0.00018V
- %PASS Slot 17 channel 0 raw DAC code binary transition 0 to 1 on 6V range Measured: 6.970400E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 1 to 2 on 6V range Measured: 9.663199E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 2 to 3 on 6V range Measured: 7.850499E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 3 to 4 on 6V range Measured: 1.090220E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 7 to 8 on 6V range Measured: 1.118250E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 15 to 16 on 6V range Measured: 1.222460E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

- %PASS Slot 17 channel 0 raw DAC code binary transition 31 to 32 on 6V range Measured: 1.141009E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 63 to 64 on 6V range Measured: 1.162910E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 127 to 128 on 6V range Measured: 1.265359E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 255 to 256 on 6V range Measured: 1.163350E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 511 to 512 on 6V range Measured: 1.090230E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 1023 to 1024 on 6V range Measured: 1.250910E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 2047 to 2048 on 6V range Measured: 1.242159E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 4095 to 4096 on 6V range Measured: 1.095919E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 8191 to 8192 on 6V range Measured: 9.023900E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 16383 to 16384 on 6V range Measured: 9.658799E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 32767 to 32768 on 6V range Measured: 1.155459E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transitions maximum difference on 6V range Measured: 1.265359E-04V high limit: 2.746044E-04V
- $\mbox{\%PASS}$  Slot 17 channel 0 raw DAC code binary transitions minimum difference on 6V range

## Measured: 6.970400E-05V low limit: -8.539550E-05V

- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.05V on 3V range Measured: 3.049424V low limit: 3.049347V high limit: 3.049527V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.04995269703212V on 3V range Measured: 3.049388V low limit: 3.049299V high limit: 3.049479V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.04990539406424V on 3V range Measured: 3.049339V low limit: 3.049252V high limit: 3.049432V
- %PASS Slot 17 channel 1 raw DAC codes linearity at  $\,$  3.04985809109636V on 3V range Measured: 3.049300V low limit: 3.049205V high limit: 3.049385V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.04981078812848V on 3V range Measured: 3.049248V low limit: 3.049158V high limit: 3.049338V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.04966887922484V on 3V range Measured: 3.049114V low limit: 3.049016V high limit: 3.049196V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.04962157625696V on 3V range Measured: 3.049057V low limit: 3.048968V high limit: 3.049148V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.0492904554818V on 3V range Measured: 3.048734V low limit: 3.048637V high limit: 3.048817V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.04924315251392V on 3V range Measured: 3.048675V low limit: 3.048590V high limit: 3.048770V
- %PASS Slot 17 channel 1 raw DAC codes linearity at  $\,$  3.04853360799573V on 3V range Measured: 3.047981V low limit: 3.047881V high limit: 3.048061V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.04848630502785V on 3V range Measured: 3.047921V low limit: 3.047833V high limit: 3.048013V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.04701991302358V on 3V range Measured: 3.046466V low limit: 3.046367V high limit: 3.046547V

- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.0469726100557V on 3V range Measured: 3.046406V low limit: 3.046320V high limit: 3.046500V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.04399252307927V on 3V range Measured: 3.043441V low limit: 3.043341V high limit: 3.043521V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.04394522011139V on 3V range Measured: 3.043379V low limit: 3.043293V high limit: 3.043473V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.03793774319066V on 3V range Measured: 3.037388V low limit: 3.037287V high limit: 3.037467V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.03789044022278V on 3V range Measured: 3.037321V low limit: 3.037240V high limit: 3.037420V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.02582818341344V on 3V range Measured: 3.025275V low limit: 3.025180V high limit: 3.025360V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.02578088044556V on 3V range Measured: 3.025216V low limit: 3.025133V high limit: 3.025313V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.00160906385901V on 3V range Measured: 3.001061V low limit: 3.000966V high limit: 3.001146V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.00156176089113V on 3V range Measured: 3.000997V low limit: 3.000919V high limit: 3.001099V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 2.95317082475013V on 3V range Measured: 2.952628V low limit: 2.952538V high limit: 2.952718V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 2.95312352178225V on 3V range Measured: 2.952573V low limit: 2.952491V high limit: 2.952671V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 2.85629434653239V on 3V range Measured: 2.855767V low limit: 2.855683V high limit: 2.855863V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 2.85624704356451V on 3V range Measured: 2.855712V low limit: 2.855636V high limit: 2.855816V

- %PASS Slot 17 channel 1 raw DAC codes linearity at 2.66254139009689V on 3V range Measured: 2.662060V low limit: 2.661972V high limit: 2.662152V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 2.66249408712902V on 3V range Measured: 2.662009V low limit: 2.661924V high limit: 2.662104V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 2.27503547722591V on 3V range Measured: 2.274650V low limit: 2.274549V high limit: 2.274729V
- %PASS Slot 17 channel 1 raw DAC codes linearity at  $\,$  2.27498817425803V on 3V range Measured: 2.274598V low limit: 2.274502V high limit: 2.274682V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 1.50002365148394V on 3V range Measured: 1.499812V low limit: 1.499705V high limit: 1.499885V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 1.49997634851606V on 3V range Measured: 1.499756V low limit: 1.499658V high limit: 1.499838V
- %PASS Slot 17 channel 1 raw DAC codes linearity at -.05V on 3V range Measured: -0.04990790V low limit: -4.98366E-02V high limit: -4.980366E-02V
- %PASS Slot 17 channel 1 raw DAC codes maximum linearity error on 3V range Measured: 1.705733E-05V high limit: 0.00009V
- %PASS Slot 17 channel 1 raw DAC code binary transition 0 to 1 on 3V range Measured: 3.599099E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 1 to 2 on 3V range Measured: 4.824999E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 2 to 3 on 3V range Measured: 3.931800E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 3 to 4 on 3V range Measured: 5.232200E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 7 to 8 on 3V range

- Measured: 5.683199E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 15 to 16 on 3V range Measured: 5.985299E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 31 to 32 on 3V range Measured: 5.998399E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 63 to 64 on 3V range Measured: 6.015999E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 127 to 128 on 3V range Measured: 6.195500E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 255 to 256 on 3V range Measured: 6.615800E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 511 to 512 on 3V range Measured: 5.954600E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 1023 to 1024 on 3V range Measured: 6.423100E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 2047 to 2048 on 3V range Measured: 5.451100E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 4095 to 4096 on 3V range Measured: 5.521199E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 8191 to 8192 on 3V range Measured: 5.127099E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 16383 to 16384 on 3V range Measured: 5.223500E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 32767 to 32768 on 3V range Measured: 5.626199E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

- %PASS Slot 17 channel 1 raw DAC code binary transitions maximum difference on 3V range Measured: 6.615800E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transitions minimum difference on 3V range Measured: 3.599099E-05V low limit: -4.269775E-05V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 6.1V on 6V range Measured: 6.098648V low limit: 6.098490V high limit: 6.098850V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 6.09990539406424V on 6V range Measured: 6.098576V low limit: 6.098395V high limit: 6.098755V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 6.09981078812848V on 6V range Measured: 6.098483V low limit: 6.098301V high limit: 6.098661V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 6.09971618219272V on 6V range Measured: 6.098403V low limit: 6.098206V high limit: 6.098566V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 6.09962157625696V on 6V range Measured: 6.098291V low limit: 6.098112V high limit: 6.098472V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 6.09933775844968V on 6V range Measured: 6.098024V low limit: 6.097828V high limit: 6.098188V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 6.09924315251392V on 6V range Measured: 6.097909V low limit: 6.097733V high limit: 6.098093V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 6.09858091096361V on 6V range Measured: 6.097269V low limit: 6.097071V high limit: 6.097431V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 6.09848630502785V on 6V range Measured: 6.097149V low limit: 6.096977V high limit: 6.097337V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 6.09706721599146V on 6V range Measured: 6.095761V low limit: 6.095558V high limit: 6.095918V
- %PASS Slot 17 channel 1 raw DAC codes linearity at  $\,$  6.0969726100557V on 6V range Measured: 6.095637V low limit: 6.095463V high limit: 6.095823V

- %PASS Slot 17 channel 1 raw DAC codes linearity at 6.09403982604715V on 6V range Measured: 6.092735V low limit: 6.092532V high limit: 6.092892V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 6.09394522011139V on 6V range Measured: 6.092613V low limit: 6.092437V high limit: 6.092797V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 6.08798504615854V on 6V range Measured: 6.086682V low limit: 6.086479V high limit: 6.086839V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 6.08789044022278V on 6V range Measured: 6.086557V low limit: 6.086384V high limit: 6.086744V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 6.07587548638132V on 6V range Measured: 6.074571V low limit: 6.074373V high limit: 6.074733V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 6.07578088044556V on 6V range Measured: 6.074444V low limit: 6.074278V high limit: 6.074638V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 6.05165636682689V on 6V range Measured: 6.050354V low limit: 6.050161V high limit: 6.050521V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 6.05156176089113V on 6V range Measured: 6.050234V low limit: 6.050067V high limit: 6.050427V
- %PASS Slot 17 channel 1 raw DAC codes linearity at  $\,$  6.00321812771801V on 6V range Measured: 6.001927V low limit: 6.001738V high limit: 6.002098V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 6.00312352178225V on 6V range Measured: 6.001798V low limit: 6.001643V high limit: 6.002003V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 5.90634164950027V on 6V range Measured: 5.905064V low limit: 5.904891V high limit: 5.905251V
- %PASS Slot 17 channel 1 raw DAC codes linearity at  $\,$  5.90624704356451V on 6V range Measured: 5.904957V low limit: 5.904797V high limit: 5.905157V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 5.71258869306477V on 6V range

- Measured: 5.711368V low limit: 5.711198V high limit: 5.711558V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 5.71249408712902V on 6V range Measured: 5.711256V low limit: 5.711103V high limit: 5.711463V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 5.32508278019379V on 6V range Measured: 5.323984V low limit: 5.323812V high limit: 5.324172V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 5.32498817425803V on 6V range Measured: 5.323883V low limit: 5.323717V high limit: 5.324077V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 4.55007095445182V on 6V range Measured: 4.549225V low limit: 4.549038V high limit: 4.549398V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 4.54997634851606V on 6V range Measured: 4.549124V low limit: 4.548944V high limit: 4.549304V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.00004730296788V on 6V range Measured: 2.999695V low limit: 2.999492V high limit: 2.999852V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 2.99995269703212V on 6V range Measured: 2.999581V low limit: 2.999398V high limit: 2.999758V
- %PASS Slot 17 channel 1 raw DAC codes linearity at -.1V on 6V range Measured: -0.09942784V low limit: -9.959909E-02V high limit: -9.923909E-02V
- %PASS Slot 17 channel 1 raw DAC codes maximum linearity error on 6V range Measured: 2.767125E-05V high limit: 0.00018V
- %PASS Slot 17 channel 1 raw DAC code binary transition 0 to 1 on 6V range Measured: 7.189400E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 1 to 2 on 6V range Measured: 9.242800E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 2 to 3 on 6V range Measured: 7.999400E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

- %PASS Slot 17 channel 1 raw DAC code binary transition 3 to 4 on 6V range Measured: 1.118679E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 7 to 8 on 6V range Measured: 1.151530E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 15 to 16 on 6V range Measured: 1.201440E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 31 to 32 on 6V range Measured: 1.239100E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 63 to 64 on 6V range Measured: 1.212820E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 127 to 128 on 6V range Measured: 1.245660E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 255 to 256 on 6V range Measured: 1.264929E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 511 to 512 on 6V range Measured: 1.197939E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 1023 to 1024 on 6V range Measured: 1.289880E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 2047 to 2048 on 6V range Measured: 1.065699E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 4095 to 4096 on 6V range Measured: 1.123070E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 8191 to 8192 on 6V range Measured: 1.007470E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 16383 to 16384 on 6V range Measured: 1.007469E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

- %PASS Slot 17 channel 1 raw DAC code binary transition 32767 to 32768 on 6V range Measured: 1.142760E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transitions maximum difference on 6V range Measured: 1.289880E-04V high limit: 2.746044E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transitions minimum difference on 6V range Measured: 7.189400E-05V low limit: -8.539550E-05V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 3.05V on 3V range Measured: 3.049647V low limit: 3.049556V high limit: 3.049736V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 3.04995269703212V on 3V range Measured: 3.049603V low limit: 3.049509V high limit: 3.049689V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 3.04990539406424V on 3V range Measured: 3.049556V low limit: 3.049461V high limit: 3.049641V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 3.04985809109636V on 3V range Measured: 3.049507V low limit: 3.049414V high limit: 3.049594V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 3.04981078812848V on 3V range Measured: 3.049455V low limit: 3.049367V high limit: 3.049547V
- %PASS Slot 17 channel 2 raw DAC codes linearity at  $\,$  3.04966887922484V on 3V range Measured: 3.049319V low limit: 3.049225V high limit: 3.049405V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 3.04962157625696V on 3V range Measured: 3.049264V low limit: 3.049178V high limit: 3.049358V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 3.0492904554818V on 3V range Measured: 3.048939V low limit: 3.048846V high limit: 3.049026V
- %PASS Slot 17 channel 2 raw DAC codes linearity at  $\,$  3.04924315251392V on 3V range Measured: 3.048886V low limit: 3.048799V high limit: 3.048979V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 3.04853360799573V on 3V range

- Measured: 3.048181V low limit: 3.048090V high limit: 3.048270V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 3.04848630502785V on 3V range Measured: 3.048130V low limit: 3.048042V high limit: 3.048222V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 3.04701991302358V on 3V range Measured: 3.046671V low limit: 3.046576V high limit: 3.046756V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 3.0469726100557V on 3V range Measured: 3.046617V low limit: 3.046529V high limit: 3.046709V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 3.04399252307927V on 3V range Measured: 3.043642V low limit: 3.043549V high limit: 3.043729V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 3.04394522011139V on 3V range Measured: 3.043589V low limit: 3.043501V high limit: 3.043681V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 3.03793774319066V on 3V range Measured: 3.037586V low limit: 3.037494V high limit: 3.037674V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 3.03789044022278V on 3V range Measured: 3.037530V low limit: 3.037447V high limit: 3.037627V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 3.02582818341344V on 3V range Measured: 3.025477V low limit: 3.025385V high limit: 3.025565V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 3.02578088044556V on 3V range Measured: 3.025424V low limit: 3.025338V high limit: 3.025518V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 3.00160906385901V on 3V range Measured: 3.001259V low limit: 3.001168V high limit: 3.001348V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 3.00156176089113V on 3V range Measured: 3.001204V low limit: 3.001120V high limit: 3.001300V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 2.95317082475013V on 3V range Measured: 2.952819V low limit: 2.952732V high limit: 2.952912V

- %PASS Slot 17 channel 2 raw DAC codes linearity at 2.95312352178225V on 3V range Measured: 2.952766V low limit: 2.952685V high limit: 2.952865V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 2.85629434653239V on 3V range Measured: 2.855946V low limit: 2.855860V high limit: 2.856040V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 2.85624704356451V on 3V range Measured: 2.855898V low limit: 2.855813V high limit: 2.855993V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 2.66254139009689V on 3V range Measured: 2.662205V low limit: 2.662118V high limit: 2.662298V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 2.66249408712902V on 3V range Measured: 2.662163V low limit: 2.662070V high limit: 2.662250V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 2.27503547722591V on 3V range Measured: 2.274728V low limit: 2.274632V high limit: 2.274812V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 2.27498817425803V on 3V range Measured: 2.274687V low limit: 2.274585V high limit: 2.274765V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 1.50002365148394V on 3V range Measured: 1.499755V low limit: 1.499661V high limit: 1.499841V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 1.49997634851606V on 3V range Measured: 1.499708V low limit: 1.499613V high limit: 1.499793V
- %PASS Slot 17 channel 2 raw DAC codes linearity at -.05V on 3V range

  Measured: -0.05019914V low limit: -5.028119E-02V high limit: -5.010119E-02V
- %PASS Slot 17 channel 2 raw DAC codes maximum linearity error on 3V range Measured: 1.246546E-05V high limit: 0.00009V
- %PASS Slot 17 channel 2 raw DAC code binary transition 0 to 1 on 3V range Measured: 4.308400E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 1 to 2 on 3V range Measured: 4.733100E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

- %PASS Slot 17 channel 2 raw DAC code binary transition 2 to 3 on 3V range Measured: 4.943199E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 3 to 4 on 3V range Measured: 5.131499E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 7 to 8 on 3V range Measured: 5.508099E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 15 to 16 on 3V range Measured: 5.280299E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 31 to 32 on 3V range Measured: 5.105299E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 63 to 64 on 3V range Measured: 5.354800E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 127 to 128 on 3V range Measured: 5.346000E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 255 to 256 on 3V range Measured: 5.678799E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 511 to 512 on 3V range Measured: 5.324199E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 1023 to 1024 on 3V range Measured: 5.529900E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 2047 to 2048 on 3V range Measured: 5.324199E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition  $\,$  4095 to  $\,$  4096 on 3V range Measured: 4.794399E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 8191 to 8192 on 3V range

- Measured: 4.229500E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 16383 to 16384 on 3V range Measured: 4.093799E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 32767 to 32768 on 3V range Measured: 4.676200E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transitions maximum difference on 3V range Measured: 5.678799E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transitions minimum difference on 3V range Measured: 4.093799E-05V low limit: -4.269775E-05V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 6.1V on 6V range Measured: 6.099268V low limit: 6.099108V high limit: 6.099468V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 6.09990539406424V on 6V range Measured: 6.099193V low limit: 6.099013V high limit: 6.099373V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 6.09981078812848V on 6V range Measured: 6.099104V low limit: 6.098919V high limit: 6.099279V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 6.09971618219272V on 6V range Measured: 6.099015V low limit: 6.098824V high limit: 6.099184V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 6.09962157625696V on 6V range Measured: 6.098915V low limit: 6.098729V high limit: 6.099089V
- %PASS Slot 17 channel 2 raw DAC codes linearity at  $\,$  6.09933775844968V on 6V range Measured: 6.098632V low limit: 6.098446V high limit: 6.098806V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 6.09924315251392V on 6V range Measured: 6.098533V low limit: 6.098351V high limit: 6.098711V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 6.09858091096361V on 6V range Measured: 6.097877V low limit: 6.097689V high limit: 6.098049V

- %PASS Slot 17 channel 2 raw DAC codes linearity at 6.09848630502785V on 6V range Measured: 6.097776V low limit: 6.097594V high limit: 6.097954V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 6.09706721599146V on 6V range Measured: 6.096370V low limit: 6.096175V high limit: 6.096535V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 6.0969726100557V on 6V range Measured: 6.096262V low limit: 6.096081V high limit: 6.096441V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 6.09403982604715V on 6V range Measured: 6.093344V low limit: 6.093148V high limit: 6.093508V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 6.09394522011139V on 6V range Measured: 6.093231V low limit: 6.093054V high limit: 6.093414V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 6.08798504615854V on 6V range Measured: 6.087286V low limit: 6.087094V high limit: 6.087454V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 6.08789044022278V on 6V range Measured: 6.087180V low limit: 6.086999V high limit: 6.087359V
- %PASS Slot 17 channel 2 raw DAC codes linearity at  $\,$  6.07587548638132V on 6V range Measured: 6.075175V low limit: 6.074985V high limit: 6.075345V
- %PASS Slot 17 channel 2 raw DAC codes linearity at  $6.07578088044556\mathrm{V}$  on 6V range Measured:  $6.075058\mathrm{V}$  low limit:  $6.074891\mathrm{V}$  high limit:  $6.075251\mathrm{V}$
- %PASS Slot 17 channel 2 raw DAC codes linearity at 6.05165636682689V on 6V range Measured: 6.050952V low limit: 6.050768V high limit: 6.051128V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 6.05156176089113V on 6V range Measured: 6.050852V low limit: 6.050674V high limit: 6.051034V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 6.00321812771801V on 6V range Measured: 6.002520V low limit: 6.002334V high limit: 6.002694V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 6.00312352178225V on 6V range Measured: 6.002402V low limit: 6.002240V high limit: 6.002600V

- %PASS Slot 17 channel 2 raw DAC codes linearity at 5.90634164950027V on 6V range Measured: 5.905646V low limit: 5.905466V high limit: 5.905826V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 5.90624704356451V on 6V range Measured: 5.905543V low limit: 5.905371V high limit: 5.905731V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 5.71258869306477V on 6V range Measured: 5.711898V low limit: 5.711729V high limit: 5.712089V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 5.71249408712902V on 6V range Measured: 5.711807V low limit: 5.711635V high limit: 5.711995V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 5.32508278019379V on 6V range Measured: 5.324425V low limit: 5.324256V high limit: 5.324616V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 5.32498817425803V on 6V range Measured: 5.324341V low limit: 5.324162V high limit: 5.324522V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 4.55007095445182V on 6V range Measured: 4.549483V low limit: 4.549310V high limit: 4.549670V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 4.54997634851606V on 6V range Measured: 4.549400V low limit: 4.549216V high limit: 4.549576V
- %PASS Slot 17 channel 2 raw DAC codes linearity at  $\,$  3.00004730296788V on 6V range Measured: 2.999587V low limit: 2.999418V high limit: 2.999778V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 2.99995269703212V on 6V range Measured: 2.999493V low limit: 2.999323V high limit: 2.999683V
- %PASS Slot 17 channel 2 raw DAC codes linearity at -.1V on 6V range
  Measured: -0.1001715V low limit: -0.1003661V high limit: -0.1000061V
- %PASS Slot 17 channel 2 raw DAC codes maximum linearity error on 6V range Measured: 2.000205E-05V high limit: 0.00018V
- %PASS Slot 17 channel 2 raw DAC code binary transition 0 to 1 on 6V range

- %PASS Slot 17 channel 2 raw DAC code binary transition 1 to 2 on 6V range Measured: 8.923199E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 2 to 3 on 6V range Measured: 8.870700E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 3 to 4 on 6V range Measured: 9.965299E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 7 to 8 on 6V range Measured: 9.912699E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 15 to 16 on 6V range Measured: 1.007480E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 31 to 32 on 6V range Measured: 1.081039E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 63 to 64 on 6V range Measured: 1.136199E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 127 to 128 on 6V range Measured: 1.067459E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 255 to 256 on 6V range Measured: 1.173850E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 511 to 512 on 6V range Measured: 9.991599E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 1023 to 1024 on 6V range Measured: 1.176919E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 2047 to 2048 on 6V range Measured: 1.025859E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

- %PASS Slot 17 channel 2 raw DAC code binary transition 4095 to 4096 on 6V range Measured: 9.164100E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 8191 to 8192 on 6V range Measured: 8.472199E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 16383 to 16384 on 6V range Measured: 8.358400E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 32767 to 32768 on 6V range Measured: 9.418000E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transitions maximum difference on 6V range Measured: 1.176919E-04V high limit: 2.746044E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transitions minimum difference on 6V range Measured: 7.495799E-05V low limit: -8.539550E-05V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.05V on 3V range Measured: 3.050055V low limit: 3.049972V high limit: 3.050152V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.04995269703212V on 3V range Measured: 3.050012V low limit: 3.049925V high limit: 3.050105V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.04990539406424V on 3V range Measured: 3.049964V low limit: 3.049878V high limit: 3.050058V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.04985809109636V on 3V range Measured: 3.049919V low limit: 3.049830V high limit: 3.050010V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.04981078812848V on 3V range Measured: 3.049870V low limit: 3.049783V high limit: 3.049963V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.04966887922484V on 3V range Measured: 3.049734V low limit: 3.049641V high limit: 3.049821V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.04962157625696V on 3V range Measured: 3.049681V low limit: 3.049594V high limit: 3.049774V

- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.0492904554818V on 3V range Measured: 3.049356V low limit: 3.049263V high limit: 3.049443V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.04924315251392V on 3V range Measured: 3.049304V low limit: 3.049215V high limit: 3.049395V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.04853360799573V on 3V range Measured: 3.048597V low limit: 3.048506V high limit: 3.048686V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.04848630502785V on 3V range Measured: 3.048549V low limit: 3.048459V high limit: 3.048639V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.04701991302358V on 3V range Measured: 3.047085V low limit: 3.046992V high limit: 3.047172V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.0469726100557V on 3V range Measured: 3.047033V low limit: 3.046945V high limit: 3.047125V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.04399252307927V on 3V range Measured: 3.044059V low limit: 3.043965V high limit: 3.044145V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.04394522011139V on 3V range Measured: 3.044005V low limit: 3.043918V high limit: 3.044098V
- %PASS Slot 17 channel 3 raw DAC codes linearity at  $\,$  3.03793774319066V on 3V range Measured: 3.038001V low limit: 3.037911V high limit: 3.038091V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.03789044022278V on 3V range Measured: 3.037953V low limit: 3.037864V high limit: 3.038044V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.02582818341344V on 3V range Measured: 3.025895V low limit: 3.025802V high limit: 3.025982V
- %PASS Slot 17 channel 3 raw DAC codes linearity at  $\,$  3.02578088044556V on 3V range Measured: 3.025838V low limit: 3.025755V high limit: 3.025935V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.00160906385901V on 3V range

- Measured: 3.001677V low limit: 3.001585V high limit: 3.001765V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.00156176089113V on 3V range Measured: 3.001626V low limit: 3.001538V high limit: 3.001718V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 2.95317082475013V on 3V range Measured: 2.953242V low limit: 2.953151V high limit: 2.953331V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 2.95312352178225V on 3V range Measured: 2.953188V low limit: 2.953104V high limit: 2.953284V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 2.85629434653239V on 3V range Measured: 2.856372V low limit: 2.856283V high limit: 2.856463V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 2.85624704356451V on 3V range Measured: 2.856322V low limit: 2.856236V high limit: 2.856416V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 2.66254139009689V on 3V range Measured: 2.662635V low limit: 2.662546V high limit: 2.662726V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 2.66249408712902V on 3V range Measured: 2.662594V low limit: 2.662499V high limit: 2.662679V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 2.27503547722591V on 3V range Measured: 2.275171V low limit: 2.275073V high limit: 2.275253V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 2.27498817425803V on 3V range Measured: 2.275125V low limit: 2.275026V high limit: 2.275206V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 1.50002365148394V on 3V range Measured: 1.500225V low limit: 1.500127V high limit: 1.500307V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 1.49997634851606V on 3V range Measured: 1.500184V low limit: 1.500080V high limit: 1.500260V
- %PASS Slot 17 channel 3 raw DAC codes linearity at -.05V on 3V range
  Measured: -0.04969097V low limit: -4.976516E-02V high limit: -4.958516E-02V

- %PASS Slot 17 channel 3 raw DAC codes maximum linearity error on 3V range Measured: 1.580310E-05V high limit: 0.00009V
- %PASS Slot 17 channel 3 raw DAC code binary transition 0 to 1 on 3V range Measured: 4.343400E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 1 to 2 on 3V range Measured: 4.768099E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 2 to 3 on 3V range Measured: 4.553600E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 3 to 4 on 3V range Measured: 4.851299E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 7 to 8 on 3V range Measured: 5.319700E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 15 to 16 on 3V range Measured: 5.153400E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 31 to 32 on 3V range Measured: 4.864500E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 63 to 64 on 3V range Measured: 5.205999E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 127 to 128 on 3V range Measured: 5.424899E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 255 to 256 on 3V range Measured: 4.846899E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 511 to 512 on 3V range Measured: 5.661199E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 1023 to 1024 on 3V range Measured: 5.030800E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

- %PASS Slot 17 channel 3 raw DAC code binary transition 2047 to 2048 on 3V range Measured: 5.359199E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 4095 to 4096 on 3V range Measured: 4.947599E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 8191 to 8192 on 3V range Measured: 4.089400E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 16383 to 16384 on 3V range Measured: 4.606099E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 32767 to 32768 on 3V range Measured: 4.133199E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transitions maximum difference on 3V range Measured: 5.661199E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transitions minimum difference on 3V range Measured: 4.089400E-05V low limit: -4.269775E-05V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 6.1V on 6V range Measured: 6.099629V low limit: 6.099456V high limit: 6.099816V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 6.09990539406424V on 6V range Measured: 6.099535V low limit: 6.099361V high limit: 6.099721V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 6.09981078812848V on 6V range Measured: 6.099452V low limit: 6.099267V high limit: 6.099627V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 6.09971618219272V on 6V range Measured: 6.099362V low limit: 6.099172V high limit: 6.099532V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 6.09962157625696V on 6V range Measured: 6.099260V low limit: 6.099078V high limit: 6.099438V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 6.09933775844968V on 6V range

- Measured: 6.098981V low limit: 6.098794V high limit: 6.099154V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 6.09924315251392V on 6V range Measured: 6.098883V low limit: 6.098699V high limit: 6.099059V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 6.09858091096361V on 6V range Measured: 6.098221V low limit: 6.098037V high limit: 6.098397V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 6.09848630502785V on 6V range Measured: 6.098121V low limit: 6.097943V high limit: 6.098303V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 6.09706721599146V on 6V range Measured: 6.096707V low limit: 6.096524V high limit: 6.096884V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 6.0969726100557V on 6V range Measured: 6.096608V low limit: 6.096429V high limit: 6.096789V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 6.09403982604715V on 6V range Measured: 6.093681V low limit: 6.093497V high limit: 6.093857V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 6.09394522011139V on 6V range Measured: 6.093582V low limit: 6.093402V high limit: 6.093762V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 6.08798504615854V on 6V range Measured: 6.087633V low limit: 6.087443V high limit: 6.087803V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 6.08789044022278V on 6V range Measured: 6.087527V low limit: 6.087349V high limit: 6.087709V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 6.07587548638132V on 6V range Measured: 6.075518V low limit: 6.075336V high limit: 6.075696V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 6.07578088044556V on 6V range Measured: 6.075422V low limit: 6.075242V high limit: 6.075602V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 6.05165636682689V on 6V range Measured: 6.051306V low limit: 6.051122V high limit: 6.051482V

- %PASS Slot 17 channel 3 raw DAC codes linearity at 6.05156176089113V on 6V range Measured: 6.051194V low limit: 6.051027V high limit: 6.051387V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 6.00321812771801V on 6V range Measured: 6.002875V low limit: 6.002693V high limit: 6.003053V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 6.00312352178225V on 6V range Measured: 6.002771V low limit: 6.002598V high limit: 6.002958V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 5.90634164950027V on 6V range Measured: 5.906014V low limit: 5.905835V high limit: 5.906195V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 5.90624704356451V on 6V range Measured: 5.905905V low limit: 5.905740V high limit: 5.906100V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 5.71258869306477V on 6V range Measured: 5.712285V low limit: 5.712119V high limit: 5.712479V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 5.71249408712902V on 6V range Measured: 5.712192V low limit: 5.712025V high limit: 5.712385V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 5.32508278019379V on 6V range Measured: 5.324859V low limit: 5.324688V high limit: 5.325048V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 5.32498817425803V on 6V range Measured: 5.324776V low limit: 5.324593V high limit: 5.324953V
- %PASS Slot 17 channel 3 raw DAC codes linearity at  $\,$  4.55007095445182V on 6V range Measured: 4.550009V low limit: 4.549825V high limit: 4.550185V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 4.54997634851606V on 6V range Measured: 4.549918V low limit: 4.549731V high limit: 4.550091V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.00004730296788V on 6V range Measured: 3.000293V low limit: 3.000100V high limit: 3.000460V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 2.99995269703212V on 6V range Measured: 3.000209V low limit: 3.000006V high limit: 3.000366V

- %PASS Slot 17 channel 3 raw DAC codes linearity at -.1V on 6V range Measured: -0.09918793V low limit: -9.934988E-02V high limit: -9.898988E-02V
- %PASS Slot 17 channel 3 raw DAC codes maximum linearity error on 6V range Measured: 2.391239E-05V high limit: 0.00018V
- %PASS Slot 17 channel 3 raw DAC code binary transition 0 to 1 on 6V range Measured: 9.400400E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 1 to 2 on 6V range Measured: 8.340900E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 2 to 3 on 6V range Measured: 9.023900E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 3 to 4 on 6V range Measured: 1.011859E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 7 to 8 on 6V range Measured: 9.807599E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 15 to 16 on 6V range Measured: 1.004849E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 31 to 32 on 6V range Measured: 9.912800E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 63 to 64 on 6V range Measured: 9.943400E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 127 to 128 on 6V range Measured: 1.056949E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 255 to 256 on 6V range Measured: 9.615000E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 511 to 512 on 6V range

- Measured: 1.122190E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 1023 to 1024 on 6V range Measured: 1.039439E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 2047 to 2048 on 6V range Measured: 1.086289E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 4095 to 4096 on 6V range Measured: 9.291000E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 8191 to 8192 on 6V range Measured: 8.305799E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 16383 to 16384 on 6V range Measured: 9.023900E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 32767 to 32768 on 6V range Measured: 8.358399E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transitions maximum difference on 6V range Measured: 1.122190E-04V high limit: 2.746044E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transitions minimum difference on 6V range Measured: 8.305799E-05V low limit: -8.539550E-05V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.05V on 3V range Measured: 3.051025V low limit: 3.050936V high limit: 3.051116V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.04995269703212V on 3V range Measured: 3.050976V low limit: 3.050889V high limit: 3.051069V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.04990539406424V on 3V range Measured: 3.050928V low limit: 3.050842V high limit: 3.051022V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.04985809109636V on 3V range Measured: 3.050881V low limit: 3.050794V high limit: 3.050974V

- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.04981078812848V on 3V range Measured: 3.050833V low limit: 3.050747V high limit: 3.050927V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.04966887922484V on 3V range Measured: 3.050693V low limit: 3.050605V high limit: 3.050785V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.04962157625696V on 3V range Measured: 3.050645V low limit: 3.050558V high limit: 3.050738V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.0492904554818V on 3V range Measured: 3.050315V low limit: 3.050227V high limit: 3.050407V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.04924315251392V on 3V range Measured: 3.050266V low limit: 3.050179V high limit: 3.050359V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.04853360799573V on 3V range Measured: 3.049557V low limit: 3.049470V high limit: 3.049650V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.04848630502785V on 3V range Measured: 3.049510V low limit: 3.049422V high limit: 3.049602V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.04701991302358V on 3V range Measured: 3.048041V low limit: 3.047956V high limit: 3.048136V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.0469726100557V on 3V range Measured: 3.047995V low limit: 3.047908V high limit: 3.048088V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.04399252307927V on 3V range Measured: 3.045017V low limit: 3.044928V high limit: 3.045108V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.04394522011139V on 3V range Measured: 3.044969V low limit: 3.044880V high limit: 3.045060V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.03793774319066V on 3V range Measured: 3.038956V low limit: 3.038872V high limit: 3.039052V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.03789044022278V on 3V range Measured: 3.038914V low limit: 3.038825V high limit: 3.039005V

- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.02582818341344V on 3V range Measured: 3.026847V low limit: 3.026760V high limit: 3.026940V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.02578088044556V on 3V range Measured: 3.026801V low limit: 3.026713V high limit: 3.026893V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.00160906385901V on 3V range Measured: 3.002624V low limit: 3.002537V high limit: 3.002717V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.00156176089113V on 3V range Measured: 3.002579V low limit: 3.002489V high limit: 3.002669V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 2.95317082475013V on 3V range Measured: 2.954180V low limit: 2.954090V high limit: 2.954270V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 2.95312352178225V on 3V range Measured: 2.954131V low limit: 2.954042V high limit: 2.954222V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 2.85629434653239V on 3V range Measured: 2.857287V low limit: 2.857196V high limit: 2.857376V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 2.85624704356451V on 3V range Measured: 2.857243V low limit: 2.857148V high limit: 2.857328V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 2.66254139009689V on 3V range Measured: 2.663500V low limit: 2.663408V high limit: 2.663588V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 2.66249408712902V on 3V range Measured: 2.663465V low limit: 2.663360V high limit: 2.663540V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 2.27503547722591V on 3V range Measured: 2.275934V low limit: 2.275831V high limit: 2.276011V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 2.27498817425803V on 3V range Measured: 2.275896V low limit: 2.275784V high limit: 2.275964V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 1.50002365148394V on 3V range

- Measured: 1.500777V low limit: 1.500679V high limit: 1.500859V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 1.49997634851606V on 3V range Measured: 1.500733V low limit: 1.500632V high limit: 1.500812V
- %PASS Slot 17 channel 4 raw DAC codes linearity at -.05V on 3V range
  Measured: -0.04955540V low limit: -4.962495E-02V high limit: -4.944495E-02V
- %PASS Slot 17 channel 4 raw DAC codes maximum linearity error on 3V range Measured: 2.211703E-05V high limit: 0.00009V
- %PASS Slot 17 channel 4 raw DAC code binary transition 0 to 1 on 3V range Measured: 4.877599E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 1 to 2 on 3V range Measured: 4.824999E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 2 to 3 on 3V range Measured: 4.702400E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 3 to 4 on 3V range Measured: 4.785600E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 7 to 8 on 3V range Measured: 4.816200E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 15 to 16 on 3V range Measured: 4.917000E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 31 to 32 on 3V range Measured: 4.645500E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 63 to 64 on 3V range Measured: 4.557900E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 127 to 128 on 3V range Measured: 4.820600E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

- %PASS Slot 17 channel 4 raw DAC code binary transition 255 to 256 on 3V range Measured: 4.185799E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 511 to 512 on 3V range Measured: 4.553600E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 1023 to 1024 on 3V range Measured: 4.457299E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 2047 to 2048 on 3V range Measured: 4.934499E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 4095 to 4096 on 3V range Measured: 4.395900E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 8191 to 8192 on 3V range Measured: 3.476500E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 16383 to 16384 on 3V range Measured: 3.809200E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 32767 to 32768 on 3V range Measured: 4.431000E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transitions maximum difference on 3V range Measured: 4.934499E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transitions minimum difference on 3V range Measured: 3.476500E-05V low limit: -4.269775E-05V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 6.1V on 6V range Measured: 6.103000V low limit: 6.102833V high limit: 6.103193V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 6.09990539406424V on 6V range Measured: 6.102912V low limit: 6.102738V high limit: 6.103098V
- %PASS Slot 17 channel 4 raw DAC codes linearity at  $\,$  6.09981078812848V on 6V range Measured: 6.102822V low limit: 6.102644V high limit: 6.103004V

- %PASS Slot 17 channel 4 raw DAC codes linearity at 6.09971618219272V on 6V range Measured: 6.102727V low limit: 6.102549V high limit: 6.102909V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 6.09962157625696V on 6V range Measured: 6.102636V low limit: 6.102454V high limit: 6.102814V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 6.09933775844968V on 6V range Measured: 6.102352V low limit: 6.102170V high limit: 6.102530V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 6.09924315251392V on 6V range Measured: 6.102257V low limit: 6.102076V high limit: 6.102436V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 6.09858091096361V on 6V range Measured: 6.101598V low limit: 6.101413V high limit: 6.101773V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 6.09848630502785V on 6V range Measured: 6.101498V low limit: 6.101319V high limit: 6.101679V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 6.09706721599146V on 6V range Measured: 6.100078V low limit: 6.099899V high limit: 6.100259V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 6.0969726100557V on 6V range Measured: 6.099986V low limit: 6.099804V high limit: 6.100164V
- %PASS Slot 17 channel 4 raw DAC codes linearity at  $\,$  6.09403982604715V on 6V range Measured: 6.097046V low limit: 6.096870V high limit: 6.097230V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 6.09394522011139V on 6V range Measured: 6.096952V low limit: 6.096776V high limit: 6.097136V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 6.08798504615854V on 6V range Measured: 6.090989V low limit: 6.090813V high limit: 6.091173V
- %PASS Slot 17 channel 4 raw DAC codes linearity at  $\,$  6.08789044022278V on 6V range Measured: 6.090900V low limit: 6.090719V high limit: 6.091079V
- % PASS Slot 17 channel 4 raw DAC codes linearity at  $\,$  6.07587548638132V on 6V range

- Measured: 6.078876V low limit: 6.078699V high limit: 6.079059V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 6.07578088044556V on 6V range Measured: 6.078787V low limit: 6.078604V high limit: 6.078964V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 6.05165636682689V on 6V range Measured: 6.054651V low limit: 6.054470V high limit: 6.054830V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 6.05156176089113V on 6V range Measured: 6.054562V low limit: 6.054376V high limit: 6.054736V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 6.00321812771801V on 6V range Measured: 6.006195V low limit: 6.006013V high limit: 6.006373V
- %PASS Slot 17 channel 4 raw DAC codes linearity at  $\,$  6.00312352178225V on 6V range Measured: 6.006100V low limit: 6.005918V high limit: 6.006278V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 5.90634164950027V on 6V range Measured: 5.909280V low limit: 5.909098V high limit: 5.909458V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 5.90624704356451V on 6V range Measured: 5.909179V low limit: 5.909003V high limit: 5.909363V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 5.71258869306477V on 6V range Measured: 5.715441V low limit: 5.715269V high limit: 5.715629V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 5.71249408712902V on 6V range Measured: 5.715353V low limit: 5.715174V high limit: 5.715534V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 5.32508278019379V on 6V range Measured: 5.327785V low limit: 5.327610V high limit: 5.327970V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 5.32498817425803V on 6V range Measured: 5.327713V low limit: 5.327515V high limit: 5.327875V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 4.55007095445182V on 6V range Measured: 4.552476V low limit: 4.552293V high limit: 4.552653V

- %PASS Slot 17 channel 4 raw DAC codes linearity at 4.54997634851606V on 6V range Measured: 4.552400V low limit: 4.552198V high limit: 4.552558V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.00004730296788V on 6V range Measured: 3.001831V low limit: 3.001658V high limit: 3.002018V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 2.99995269703212V on 6V range Measured: 3.001740V low limit: 3.001563V high limit: 3.001923V
- %PASS Slot 17 channel 4 raw DAC codes linearity at -.1V on 6V range Measured: -0.09943482V low limit: -9.961195E-02V high limit: -9.925195E-02V
- %PASS Slot 17 channel 4 raw DAC codes maximum linearity error on 6V range Measured: 2.247841E-05V high limit: 0.00018V
- %PASS Slot 17 channel 4 raw DAC code binary transition 0 to 1 on 6V range Measured: 8.818199E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 1 to 2 on 6V range Measured: 9.015100E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 2 to 3 on 6V range Measured: 9.518699E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 3 to 4 on 6V range Measured: 9.098400E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 7 to 8 on 6V range Measured: 9.518700E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 15 to 16 on 6V range Measured: 9.969599E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 31 to 32 on 6V range Measured: 9.251600E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 63 to 64 on 6V range Measured: 9.317199E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

- %PASS Slot 17 channel 4 raw DAC code binary transition 127 to 128 on 6V range Measured: 8.905700E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 255 to 256 on 6V range Measured: 8.848800E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 511 to 512 on 6V range Measured: 8.918900E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 1023 to 1024 on 6V range Measured: 9.461699E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 2047 to 2048 on 6V range Measured: 1.014040E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 4095 to 4096 on 6V range Measured: 8.748100E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 8191 to 8192 on 6V range Measured: 7.158699E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 16383 to 16384 on 6V range Measured: 7.517800E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition  $\,$  32767 to  $\,$  32768 on 6V range Measured: 9.146500E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transitions maximum difference on 6V range Measured: 1.014040E-04V high limit: 2.746044E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transitions minimum difference on 6V range Measured: 7.158699E-05V low limit: -8.539550E-05V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 3.05V on 3V range Measured: 3.049349V low limit: 3.049257V high limit: 3.049437V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 3.04995269703212V on 3V range

- Measured: 3.049297V low limit: 3.049210V high limit: 3.049390V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 3.04990539406424V on 3V range Measured: 3.049251V low limit: 3.049162V high limit: 3.049342V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 3.04985809109636V on 3V range Measured: 3.049202V low limit: 3.049115V high limit: 3.049295V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 3.04981078812848V on 3V range Measured: 3.049155V low limit: 3.049068V high limit: 3.049248V
- %PASS Slot 17 channel 5 raw DAC codes linearity at  $\,$  3.04966887922484V on 3V range Measured: 3.049012V low limit: 3.048926V high limit: 3.049106V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 3.04962157625696V on 3V range Measured: 3.048967V low limit: 3.048879V high limit: 3.049059V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 3.0492904554818V on 3V range Measured: 3.048632V low limit: 3.048548V high limit: 3.048728V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 3.04924315251392V on 3V range Measured: 3.048587V low limit: 3.048500V high limit: 3.048680V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 3.04853360799573V on 3V range Measured: 3.047876V low limit: 3.047791V high limit: 3.047971V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 3.04848630502785V on 3V range Measured: 3.047834V low limit: 3.047744V high limit: 3.047924V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 3.04701991302358V on 3V range Measured: 3.046362V low limit: 3.046278V high limit: 3.046458V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 3.0469726100557V on 3V range Measured: 3.046315V low limit: 3.046230V high limit: 3.046410V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 3.04399252307927V on 3V range Measured: 3.043334V low limit: 3.043251V high limit: 3.043431V

- %PASS Slot 17 channel 5 raw DAC codes linearity at 3.04394522011139V on 3V range Measured: 3.043291V low limit: 3.043204V high limit: 3.043384V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 3.03793774319066V on 3V range Measured: 3.037277V low limit: 3.037198V high limit: 3.037378V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 3.03789044022278V on 3V range Measured: 3.037242V low limit: 3.037150V high limit: 3.037330V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 3.02582818341344V on 3V range Measured: 3.025173V low limit: 3.025091V high limit: 3.025271V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 3.02578088044556V on 3V range Measured: 3.025133V low limit: 3.025044V high limit: 3.025224V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 3.00160906385901V on 3V range Measured: 3.000965V low limit: 3.000878V high limit: 3.001058V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 3.00156176089113V on 3V range Measured: 3.000919V low limit: 3.000830V high limit: 3.001010V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 2.95317082475013V on 3V range Measured: 2.952539V low limit: 2.952451V high limit: 2.952631V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 2.95312352178225V on 3V range Measured: 2.952493V low limit: 2.952404V high limit: 2.952584V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 2.85629434653239V on 3V range Measured: 2.855688V low limit: 2.855598V high limit: 2.855778V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 2.85624704356451V on 3V range Measured: 2.855649V low limit: 2.855551V high limit: 2.855731V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 2.66254139009689V on 3V range Measured: 2.661985V low limit: 2.661892V high limit: 2.662072V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 2.66249408712902V on 3V range Measured: 2.661953V low limit: 2.661845V high limit: 2.662025V

- %PASS Slot 17 channel 5 raw DAC codes linearity at 2.27503547722591V on 3V range Measured: 2.274584V low limit: 2.274481V high limit: 2.274661V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 2.27498817425803V on 3V range Measured: 2.274552V low limit: 2.274433V high limit: 2.274613V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 1.50002365148394V on 3V range Measured: 1.499761V low limit: 1.499657V high limit: 1.499837V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 1.49997634851606V on 3V range Measured: 1.499718V low limit: 1.499610V high limit: 1.499790V
- %PASS Slot 17 channel 5 raw DAC codes linearity at -.05V on 3V range
  Measured: -0.04992935V low limit: -4.98980E-02V high limit: -4.980980E-02V
- %PASS Slot 17 channel 5 raw DAC codes maximum linearity error on 3V range Measured: 2.955026E-05V high limit: 0.00009V
- %PASS Slot 17 channel 5 raw DAC code binary transition 0 to 1 on 3V range Measured: 5.140299E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 1 to 2 on 3V range Measured: 4.663000E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 2 to 3 on 3V range Measured: 4.903899E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 3 to 4 on 3V range Measured: 4.636699E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 7 to 8 on 3V range Measured: 4.562300E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition  $\,$  15 to  $\,$  16 on 3V range Measured: 4.492300E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- % PASS Slot 17 channel 5 raw DAC code binary transition  $\,$  31 to  $\,$  32 on 3V range

- Measured: 4.212000E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 63 to 64 on 3V range Measured: 4.702400E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 127 to 128 on 3V range Measured: 4.356499E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 255 to 256 on 3V range Measured: 3.550800E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 511 to 512 on 3V range Measured: 3.993099E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 1023 to 1024 on 3V range Measured: 4.614899E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 2047 to 2048 on 3V range Measured: 4.689199E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 4095 to 4096 on 3V range Measured: 3.905600E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 8191 to 8192 on 3V range Measured: 3.143700E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 16383 to 16384 on 3V range Measured: 3.248799E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 32767 to 32768 on 3V range Measured: 4.242699E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transitions maximum difference on 3V range Measured: 5.140299E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transitions minimum difference on 3V range Measured: 3.143700E-05V low limit: -4.269775E-05V

- %PASS Slot 17 channel 5 raw DAC codes linearity at 6.1V on 6V range Measured: 6.098220V low limit: 6.098032V high limit: 6.098392V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 6.09990539406424V on 6V range Measured: 6.098114V low limit: 6.097937V high limit: 6.098297V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 6.09981078812848V on 6V range Measured: 6.098024V low limit: 6.097843V high limit: 6.098203V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 6.09971618219272V on 6V range Measured: 6.097929V low limit: 6.097748V high limit: 6.098108V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 6.09962157625696V on 6V range Measured: 6.097833V low limit: 6.097653V high limit: 6.098013V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 6.09933775844968V on 6V range Measured: 6.097549V low limit: 6.097370V high limit: 6.097730V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 6.09924315251392V on 6V range Measured: 6.097457V low limit: 6.097275V high limit: 6.097635V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 6.09858091096361V on 6V range Measured: 6.096787V low limit: 6.096613V high limit: 6.096973V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 6.09848630502785V on 6V range Measured: 6.096700V low limit: 6.096519V high limit: 6.096879V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 6.09706721599146V on 6V range Measured: 6.095268V low limit: 6.095100V high limit: 6.095460V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 6.0969726100557V on 6V range Measured: 6.095183V low limit: 6.095005V high limit: 6.095365V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 6.09403982604715V on 6V range Measured: 6.092246V low limit: 6.092074V high limit: 6.092434V
- %PASS Slot 17 channel 5 raw DAC codes linearity at  $\,$  6.09394522011139V on 6V range Measured: 6.092159V low limit: 6.091979V high limit: 6.092339V

- %PASS Slot 17 channel 5 raw DAC codes linearity at 6.08798504615854V on 6V range Measured: 6.086186V low limit: 6.086021V high limit: 6.086381V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 6.08789044022278V on 6V range Measured: 6.086101V low limit: 6.085926V high limit: 6.086286V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 6.07587548638132V on 6V range Measured: 6.074080V low limit: 6.073915V high limit: 6.074275V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 6.07578088044556V on 6V range Measured: 6.074007V low limit: 6.073821V high limit: 6.074181V
- %PASS Slot 17 channel 5 raw DAC codes linearity at  $\,$  6.05165636682689V on 6V range Measured: 6.049875V low limit: 6.049704V high limit: 6.050064V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 6.05156176089113V on 6V range Measured: 6.049792V low limit: 6.049609V high limit: 6.049969V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 6.00321812771801V on 6V range Measured: 6.001458V low limit: 6.001282V high limit: 6.001642V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 6.00312352178225V on 6V range Measured: 6.001367V low limit: 6.001187V high limit: 6.001547V
- %PASS Slot 17 channel 5 raw DAC codes linearity at  $\,$  5.90634164950027V on 6V range Measured: 5.904612V low limit: 5.904437V high limit: 5.904797V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 5.90624704356451V on 6V range Measured: 5.904518V low limit: 5.904343V high limit: 5.904703V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 5.71258869306477V on 6V range Measured: 5.710918V low limit: 5.710748V high limit: 5.711108V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 5.71249408712902V on 6V range Measured: 5.710847V low limit: 5.710653V high limit: 5.711013V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 5.32508278019379V on 6V range

- Measured: 5.323547V low limit: 5.323369V high limit: 5.323729V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 5.32498817425803V on 6V range Measured: 5.323481V low limit: 5.323275V high limit: 5.323635V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 4.55007095445182V on 6V range Measured: 4.548801V low limit: 4.548613V high limit: 4.548973V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 4.54997634851606V on 6V range Measured: 4.548731V low limit: 4.548518V high limit: 4.548878V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 3.00004730296788V on 6V range Measured: 2.999287V low limit: 2.999099V high limit: 2.999459V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 2.99995269703212V on 6V range Measured: 2.999200V low limit: 2.999004V high limit: 2.999364V
- %PASS Slot 17 channel 5 raw DAC codes linearity at -.1V on 6V range
  Measured: -0.09977256V low limit: -9.992789E-02V high limit: -9.956789E-02V
- %PASS Slot 17 channel 5 raw DAC codes maximum linearity error on 6V range Measured: 3.283381E-05V high limit: 0.00018V
- %PASS Slot 17 channel 5 raw DAC code binary transition 0 to 1 on 6V range Measured: 1.065269E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 1 to 2 on 6V range Measured: 8.953900E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 2 to 3 on 6V range Measured: 9.562400E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 3 to 4 on 6V range Measured: 9.549399E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 7 to 8 on 6V range Measured: 9.168400E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

- %PASS Slot 17 channel 5 raw DAC code binary transition 15 to 16 on 6V range Measured: 8.677999E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 31 to 32 on 6V range Measured: 8.450300E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 63 to 64 on 6V range Measured: 8.695499E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 127 to 128 on 6V range Measured: 8.502799E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 255 to 256 on 6V range Measured: 7.276899E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 511 to 512 on 6V range Measured: 8.231400E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 1023 to 1024 on 6V range Measured: 9.089599E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 2047 to 2048 on 6V range Measured: 9.448600E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 4095 to 4096 on 6V range Measured: 7.136800E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 8191 to 8192 on 6V range Measured: 6.620200E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 16383 to 16384 on 6V range Measured: 7.023000E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 32767 to 32768 on 6V range Measured: 8.774400E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- % PASS Slot 17 channel 5 raw DAC code binary transitions maximum difference on 6V range Measured: 1.065269E-04V high limit: 2.746044E-04V

- %PASS Slot 17 channel 5 raw DAC code binary transitions minimum difference on 6V range Measured: 6.620200E-05V low limit: -8.539550E-05V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 3.05V on 3V range Measured: 3.049951V low limit: 3.049862V high limit: 3.050042V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 3.04995269703212V on 3V range Measured: 3.049900V low limit: 3.049814V high limit: 3.049994V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 3.04990539406424V on 3V range Measured: 3.049855V low limit: 3.049767V high limit: 3.049947V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 3.04985809109636V on 3V range Measured: 3.049806V low limit: 3.049720V high limit: 3.049900V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 3.04981078812848V on 3V range Measured: 3.049760V low limit: 3.049672V high limit: 3.049852V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 3.04966887922484V on 3V range Measured: 3.049619V low limit: 3.049531V high limit: 3.049711V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 3.04962157625696V on 3V range Measured: 3.049571V low limit: 3.049483V high limit: 3.049663V
- %PASS Slot 17 channel 6 raw DAC codes linearity at  $\,$  3.0492904554818V on 3V range Measured: 3.049243V low limit: 3.049152V high limit: 3.049332V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 3.04924315251392V on 3V range Measured: 3.049193V low limit: 3.049105V high limit: 3.049285V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 3.04853360799573V on 3V range Measured: 3.048484V low limit: 3.048396V high limit: 3.048576V
- %PASS Slot 17 channel 6 raw DAC codes linearity at  $\,$  3.04848630502785V on 3V range Measured: 3.048438V low limit: 3.048349V high limit: 3.048529V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 3.04701991302358V on 3V range

- Measured: 3.046969V low limit: 3.046883V high limit: 3.047063V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 3.0469726100557V on 3V range Measured: 3.046923V low limit: 3.046836V high limit: 3.047016V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 3.04399252307927V on 3V range Measured: 3.043943V low limit: 3.043857V high limit: 3.044037V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 3.04394522011139V on 3V range Measured: 3.043893V low limit: 3.043810V high limit: 3.043990V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 3.03793774319066V on 3V range Measured: 3.037887V low limit: 3.037805V high limit: 3.037985V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 3.03789044022278V on 3V range Measured: 3.037847V low limit: 3.037758V high limit: 3.037938V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 3.02582818341344V on 3V range Measured: 3.025788V low limit: 3.025701V high limit: 3.025881V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 3.02578088044556V on 3V range Measured: 3.025744V low limit: 3.025654V high limit: 3.025834V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 3.00160906385901V on 3V range Measured: 3.001585V low limit: 3.001493V high limit: 3.001673V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 3.00156176089113V on 3V range Measured: 3.001530V low limit: 3.001446V high limit: 3.001626V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 2.95317082475013V on 3V range Measured: 2.953165V low limit: 2.953077V high limit: 2.953257V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 2.95312352178225V on 3V range Measured: 2.953118V low limit: 2.953030V high limit: 2.953210V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 2.85629434653239V on 3V range Measured: 2.856335V low limit: 2.856245V high limit: 2.856425V

- %PASS Slot 17 channel 6 raw DAC codes linearity at 2.85624704356451V on 3V range Measured: 2.856291V low limit: 2.856198V high limit: 2.856378V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 2.66254139009689V on 3V range Measured: 2.662676V low limit: 2.662582V high limit: 2.662762V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 2.66249408712902V on 3V range Measured: 2.662641V low limit: 2.662535V high limit: 2.662715V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 2.27503547722591V on 3V range Measured: 2.275357V low limit: 2.275255V high limit: 2.275435V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 2.27498817425803V on 3V range Measured: 2.275321V low limit: 2.275207V high limit: 2.275387V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 1.50002365148394V on 3V range Measured: 1.500693V low limit: 1.500601V high limit: 1.500781V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 1.49997634851606V on 3V range Measured: 1.500650V low limit: 1.500553V high limit: 1.500733V
- %PASS Slot 17 channel 6 raw DAC codes linearity at -.05V on 3V range
  Measured: -0.04863320V low limit: -4.870719E-02V high limit: -4.852719E-02V
- %PASS Slot 17 channel 6 raw DAC codes maximum linearity error on 3V range Measured: 2.385359E-05V high limit: 0.00009V
- %PASS Slot 17 channel 6 raw DAC code binary transition 0 to 1 on 3V range Measured: 5.096499E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 1 to 2 on 3V range Measured: 4.518500E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 2 to 3 on 3V range Measured: 4.895099E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 3 to 4 on 3V range Measured: 4.597300E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

- %PASS Slot 17 channel 6 raw DAC code binary transition 7 to 8 on 3V range Measured: 4.794400E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 15 to 16 on 3V range Measured: 5.048299E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 31 to 32 on 3V range Measured: 4.658600E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 63 to 64 on 3V range Measured: 4.566700E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 127 to 128 on 3V range Measured: 4.973799E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 255 to 256 on 3V range Measured: 4.028200E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 511 to 512 on 3V range Measured: 4.404700E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 1023 to 1024 on 3V range Measured: 5.538699E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 2047 to 2048 on 3V range Measured: 4.693599E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 4095 to 4096 on 3V range Measured: 4.334700E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 8191 to 8192 on 3V range Measured: 3.489599E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition  $\,$  16383 to  $\,$  16384 on 3V range Measured: 3.555199E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 32767 to 32768 on 3V range

- Measured: 4.325800E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transitions maximum difference on 3V range Measured: 5.538699E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transitions minimum difference on 3V range Measured: 3.489599E-05V low limit: -4.269775E-05V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 6.1V on 6V range Measured: 6.098994V low limit: 6.098813V high limit: 6.099173V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 6.09990539406424V on 6V range Measured: 6.098908V low limit: 6.098718V high limit: 6.099078V
- %PASS Slot 17 channel 6 raw DAC codes linearity at  $\,$  6.09981078812848V on 6V range Measured: 6.098810V low limit: 6.098624V high limit: 6.098984V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 6.09971618219272V on 6V range Measured: 6.098717V low limit: 6.098529V high limit: 6.098889V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 6.09962157625696V on 6V range Measured: 6.098618V low limit: 6.098434V high limit: 6.098794V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 6.09933775844968V on 6V range Measured: 6.098335V low limit: 6.098151V high limit: 6.098511V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 6.09924315251392V on 6V range Measured: 6.098240V low limit: 6.098056V high limit: 6.098416V
- %PASS Slot 17 channel 6 raw DAC codes linearity at  $\,$  6.09858091096361V on 6V range Measured: 6.097580V low limit: 6.097394V high limit: 6.097754V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 6.09848630502785V on 6V range Measured: 6.097482V low limit: 6.097300V high limit: 6.097660V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 6.09706721599146V on 6V range Measured: 6.096063V low limit: 6.095881V high limit: 6.096241V

- %PASS Slot 17 channel 6 raw DAC codes linearity at 6.0969726100557V on 6V range Measured: 6.095969V low limit: 6.095787V high limit: 6.096147V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 6.09403982604715V on 6V range Measured: 6.093036V low limit: 6.092855V high limit: 6.093215V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 6.09394522011139V on 6V range Measured: 6.092942V low limit: 6.092761V high limit: 6.093121V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 6.08798504615854V on 6V range Measured: 6.086980V low limit: 6.086804V high limit: 6.087164V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 6.08789044022278V on 6V range Measured: 6.086884V low limit: 6.086709V high limit: 6.087069V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 6.07587548638132V on 6V range Measured: 6.074867V low limit: 6.074700V high limit: 6.075060V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 6.07578088044556V on 6V range Measured: 6.074790V low limit: 6.074605V high limit: 6.074965V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 6.05165636682689V on 6V range Measured: 6.050667V low limit: 6.050493V high limit: 6.050853V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 6.05156176089113V on 6V range Measured: 6.050587V low limit: 6.050398V high limit: 6.050758V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 6.00321812771801V on 6V range Measured: 6.002262V low limit: 6.002078V high limit: 6.002438V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 6.00312352178225V on 6V range Measured: 6.002150V low limit: 6.001984V high limit: 6.002344V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 5.90634164950027V on 6V range Measured: 5.905423V low limit: 5.905249V high limit: 5.905609V
- %PASS Slot 17 channel 6 raw DAC codes linearity at  $\,$  5.90624704356451V on 6V range Measured: 5.905323V low limit: 5.905155V high limit: 5.905515V

- %PASS Slot 17 channel 6 raw DAC codes linearity at 5.71258869306477V on 6V range Measured: 5.711762V low limit: 5.711591V high limit: 5.711951V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 5.71249408712902V on 6V range Measured: 5.711673V low limit: 5.711497V high limit: 5.711857V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 5.32508278019379V on 6V range Measured: 5.324444V low limit: 5.324275V high limit: 5.324635V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 5.32498817425803V on 6V range Measured: 5.324376V low limit: 5.324180V high limit: 5.324540V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 4.55007095445182V on 6V range Measured: 4.549818V low limit: 4.549642V high limit: 4.550002V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 4.54997634851606V on 6V range Measured: 4.549749V low limit: 4.549548V high limit: 4.549908V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 3.00004730296788V on 6V range Measured: 3.000536V low limit: 3.000377V high limit: 3.000737V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 2.99995269703212V on 6V range Measured: 3.000453V low limit: 3.000283V high limit: 3.000643V
- %PASS Slot 17 channel 6 raw DAC codes linearity at -.1V on 6V range Measured: -0.09795954V low limit: -9.815192E-02V high limit: -9.779192E-02V
- %PASS Slot 17 channel 6 raw DAC codes maximum linearity error on 6V range Measured: 2.181655E-05V high limit: 0.00018V
- %PASS Slot 17 channel 6 raw DAC code binary transition 0 to 1 on 6V range Measured: 8.612299E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 1 to 2 on 6V range Measured: 9.720099E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- $\mbox{\%PASS}$  Slot 17 channel 6 raw DAC code binary transition ~2 to ~3 on 6V range

- %PASS Slot 17 channel 6 raw DAC code binary transition 3 to 4 on 6V range Measured: 9.938999E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 7 to 8 on 6V range Measured: 9.514300E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 15 to 16 on 6V range Measured: 9.846999E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 31 to 32 on 6V range Measured: 9.404900E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 63 to 64 on 6V range Measured: 9.361000E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 127 to 128 on 6V range Measured: 9.628099E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 255 to 256 on 6V range Measured: 7.622800E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 511 to 512 on 6V range Measured: 7.929299E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 1023 to 1024 on 6V range Measured: 1.120429E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 2047 to 2048 on 6V range Measured: 9.991600E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 4095 to 4096 on 6V range Measured: 8.853200E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 8191 to 8192 on 6V range Measured: 6.821599E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

- %PASS Slot 17 channel 6 raw DAC code binary transition 16383 to 16384 on 6V range Measured: 6.944099E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 32767 to 32768 on 6V range Measured: 8.244499E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transitions maximum difference on 6V range Measured: 1.120429E-04V high limit: 2.746044E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transitions minimum difference on 6V range Measured: 6.821599E-05V low limit: -8.539550E-05V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.05V on 3V range Measured: 3.049338V low limit: 3.049253V high limit: 3.049433V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.04995269703212V on 3V range Measured: 3.049291V low limit: 3.049206V high limit: 3.049386V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.04990539406424V on 3V range Measured: 3.049245V low limit: 3.049158V high limit: 3.049338V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.04985809109636V on 3V range Measured: 3.049198V low limit: 3.049111V high limit: 3.049291V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.04981078812848V on 3V range Measured: 3.049150V low limit: 3.049064V high limit: 3.049244V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.04966887922484V on 3V range Measured: 3.049012V low limit: 3.048922V high limit: 3.049102V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.04962157625696V on 3V range Measured: 3.048965V low limit: 3.048875V high limit: 3.049055V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.0492904554818V on 3V range Measured: 3.048631V low limit: 3.048544V high limit: 3.048724V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.04924315251392V on 3V range Measured: 3.048581V low limit: 3.048497V high limit: 3.048677V

- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.04853360799573V on 3V range Measured: 3.047874V low limit: 3.047787V high limit: 3.047967V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.04848630502785V on 3V range Measured: 3.047827V low limit: 3.047740V high limit: 3.047920V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.04701991302358V on 3V range Measured: 3.046360V low limit: 3.046274V high limit: 3.046454V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.0469726100557V on 3V range Measured: 3.046313V low limit: 3.046227V high limit: 3.046407V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.04399252307927V on 3V range Measured: 3.043334V low limit: 3.043249V high limit: 3.043429V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.04394522011139V on 3V range Measured: 3.043285V low limit: 3.043201V high limit: 3.043381V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.03793774319066V on 3V range Measured: 3.037284V low limit: 3.037197V high limit: 3.037377V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.03789044022278V on 3V range Measured: 3.037235V low limit: 3.037149V high limit: 3.037329V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.02582818341344V on 3V range Measured: 3.025181V low limit: 3.025093V high limit: 3.025273V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.02578088044556V on 3V range Measured: 3.025133V low limit: 3.025046V high limit: 3.025226V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.00160906385901V on 3V range Measured: 3.000978V low limit: 3.000886V high limit: 3.001066V
- %PASS Slot 17 channel 7 raw DAC codes linearity at  $\,$  3.00156176089113V on 3V range Measured: 3.000926V low limit: 3.000839V high limit: 3.001019V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 2.95317082475013V on 3V range

- Measured: 2.952563V low limit: 2.952472V high limit: 2.952652V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 2.95312352178225V on 3V range Measured: 2.952517V low limit: 2.952425V high limit: 2.952605V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 2.85629434653239V on 3V range Measured: 2.855737V low limit: 2.855645V high limit: 2.855825V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 2.85624704356451V on 3V range Measured: 2.855694V low limit: 2.855597V high limit: 2.855777V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 2.66254139009689V on 3V range Measured: 2.662088V low limit: 2.661989V high limit: 2.662169V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 2.66249408712902V on 3V range Measured: 2.662050V low limit: 2.661942V high limit: 2.662122V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 2.27503547722591V on 3V range Measured: 2.274784V low limit: 2.274678V high limit: 2.274858V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 2.27498817425803V on 3V range Measured: 2.274747V low limit: 2.274631V high limit: 2.274811V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 1.50002365148394V on 3V range Measured: 1.500147V low limit: 1.500056V high limit: 1.500236V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 1.49997634851606V on 3V range Measured: 1.500105V low limit: 1.500009V high limit: 1.500189V
- %PASS Slot 17 channel 7 raw DAC codes linearity at -.05V on 3V range
  Measured: -0.04911573V low limit: -4.918768E-02V high limit: -4.900768E-02V
- %PASS Slot 17 channel 7 raw DAC codes maximum linearity error on 3V range Measured: 2.645815E-05V high limit: 0.00009V
- %PASS Slot 17 channel 7 raw DAC code binary transition 0 to 1 on 3V range Measured: 4.785600E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

- %PASS Slot 17 channel 7 raw DAC code binary transition 1 to 2 on 3V range Measured: 4.588599E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 2 to 3 on 3V range Measured: 4.658600E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 3 to 4 on 3V range Measured: 4.803200E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 7 to 8 on 3V range Measured: 4.706799E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 15 to 16 on 3V range Measured: 5.052700E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 31 to 32 on 3V range Measured: 4.641100E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 63 to 64 on 3V range Measured: 4.741799E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 127 to 128 on 3V range Measured: 4.991399E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 255 to 256 on 3V range Measured: 4.925800E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 511 to 512 on 3V range Measured: 4.790000E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 1023 to 1024 on 3V range Measured: 5.127099E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 2047 to 2048 on 3V range Measured: 4.662999E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 4095 to 4096 on 3V range Measured: 4.387099E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

- %PASS Slot 17 channel 7 raw DAC code binary transition 8191 to 8192 on 3V range Measured: 3.752300E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 16383 to 16384 on 3V range Measured: 3.625300E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 32767 to 32768 on 3V range Measured: 4.255799E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transitions maximum difference on 3V range Measured: 5.127099E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transitions minimum difference on 3V range Measured: 3.625300E-05V low limit: -4.269775E-05V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 6.1V on 6V range Measured: 6.098139V low limit: 6.097972V high limit: 6.098332V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 6.09990539406424V on 6V range Measured: 6.098056V low limit: 6.097878V high limit: 6.098238V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 6.09981078812848V on 6V range Measured: 6.097964V low limit: 6.097783V high limit: 6.098143V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 6.09971618219272V on 6V range Measured: 6.097871V low limit: 6.097689V high limit: 6.098049V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 6.09962157625696V on 6V range Measured: 6.097779V low limit: 6.097594V high limit: 6.097954V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 6.09933775844968V on 6V range Measured: 6.097494V low limit: 6.097310V high limit: 6.097670V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 6.09924315251392V on 6V range Measured: 6.097404V low limit: 6.097216V high limit: 6.097576V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 6.09858091096361V on 6V range

- Measured: 6.096732V low limit: 6.096554V high limit: 6.096914V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 6.09848630502785V on 6V range Measured: 6.096639V low limit: 6.096459V high limit: 6.096819V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 6.09706721599146V on 6V range Measured: 6.095228V low limit: 6.095041V high limit: 6.095401V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 6.0969726100557V on 6V range Measured: 6.095133V low limit: 6.094947V high limit: 6.095307V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 6.09403982604715V on 6V range Measured: 6.092204V low limit: 6.092015V high limit: 6.092375V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 6.09394522011139V on 6V range Measured: 6.092104V low limit: 6.091921V high limit: 6.092281V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 6.08798504615854V on 6V range Measured: 6.086149V low limit: 6.085964V high limit: 6.086324V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 6.08789044022278V on 6V range Measured: 6.086054V low limit: 6.085870V high limit: 6.086230V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 6.07587548638132V on 6V range Measured: 6.074045V low limit: 6.073862V high limit: 6.074222V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 6.07578088044556V on 6V range Measured: 6.073947V low limit: 6.073767V high limit: 6.074127V
- %PASS Slot 17 channel 7 raw DAC codes linearity at  $\,$  6.05165636682689V on 6V range Measured: 6.049836V low limit: 6.049656V high limit: 6.050016V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 6.05156176089113V on 6V range Measured: 6.049740V low limit: 6.049562V high limit: 6.049922V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 6.00321812771801V on 6V range Measured: 6.001425V low limit: 6.001246V high limit: 6.001606V

- %PASS Slot 17 channel 7 raw DAC codes linearity at 6.00312352178225V on 6V range Measured: 6.001328V low limit: 6.001151V high limit: 6.001511V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 5.90634164950027V on 6V range Measured: 5.904601V low limit: 5.904425V high limit: 5.904785V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 5.90624704356451V on 6V range Measured: 5.904506V low limit: 5.904331V high limit: 5.904691V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 5.71258869306477V on 6V range Measured: 5.710954V low limit: 5.710784V high limit: 5.711144V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 5.71249408712902V on 6V range Measured: 5.710872V low limit: 5.710689V high limit: 5.711049V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 5.32508278019379V on 6V range Measured: 5.323676V low limit: 5.323501V high limit: 5.323861V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 5.32498817425803V on 6V range Measured: 5.323598V low limit: 5.323406V high limit: 5.323766V
- %PASS Slot 17 channel 7 raw DAC codes linearity at  $\,$  4.55007095445182V on 6V range Measured: 4.549105V low limit: 4.548935V high limit: 4.549295V
- %PASS Slot 17 channel 7 raw DAC codes linearity at  $4.54997634851606\mathrm{V}$  on 6V range Measured:  $4.549029\mathrm{V}$  low limit:  $4.548840\mathrm{V}$  high limit:  $4.549200\mathrm{V}$
- %PASS Slot 17 channel 7 raw DAC codes linearity at  $\,$  3.00004730296788V on 6V range Measured: 2.999954V low limit: 2.999803V high limit: 3.000163V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 2.99995269703212V on 6V range Measured: 2.999866V low limit: 2.999708V high limit: 3.000068V
- %PASS Slot 17 channel 7 raw DAC codes linearity at -.1V on 6V range
  Measured: -0.09825575V low limit: -9.846120E-02V high limit: -9.810120E-02V
- %PASS Slot 17 channel 7 raw DAC codes maximum linearity error on 6V range Measured: 2.889452E-05V high limit: 0.00018V

- %PASS Slot 17 channel 7 raw DAC code binary transition 0 to 1 on 6V range Measured: 8.332200E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 1 to 2 on 6V range Measured: 9.185900E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 2 to 3 on 6V range Measured: 9.295399E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 3 to 4 on 6V range Measured: 9.207799E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 7 to 8 on 6V range Measured: 8.988899E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 15 to 16 on 6V range Measured: 9.225399E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 31 to 32 on 6V range Measured: 9.487999E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 63 to 64 on 6V range Measured: 1.002219E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 127 to 128 on 6V range Measured: 9.483599E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 255 to 256 on 6V range Measured: 9.794500E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 511 to 512 on 6V range Measured: 9.628100E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 1023 to 1024 on 6V range Measured: 9.728900E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 2047 to 2048 on 6V range

- Measured: 9.461800E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 4095 to 4096 on 6V range Measured: 8.218299E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 8191 to 8192 on 6V range Measured: 7.780500E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 16383 to 16384 on 6V range Measured: 7.605299E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 32767 to 32768 on 6V range Measured: 8.769999E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transitions maximum difference on 6V range Measured: 1.002219E-04V high limit: 2.746044E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transitions minimum difference on 6V range Measured: 7.605299E-05V low limit: -8.539550E-05V

%JOB\_END - \*\*\*\*PASSED\*\*\*\* CTO\_DIB External Verification of slot 17 (C398747) at 5:01:28 PM

## Slot18\_CALCUB\_ExternalCal

- %JOB\_START Beginning CUB External Calibration test on slot 18 at 4:39:54 PM on 2/29/2020 Workbook Rev V7.30.12\_0835\_MOUT IG-XL Version: 3.40.13 DIB # 305253E Rev 1137A
  - %PASS CALCUB Vforce external calibration test, -1 volts.

    Measured: -0.9997 low limit: -1.200 high limit: -0.7999
  - %PASS CALCUB Vforce external calibration test, 0 volts.

    Measured: 0.0007760 low limit: -0.2000 high limit: 0.2000
  - %PASS CALCUB Vforce external calibration test, 0 volts.

    Measured: 0.0007757 low limit: -0.2000 high limit: 0.2000
  - %PASS CALCUB Vforce external calibration test, 2 volts.

    Measured: 2.000 low limit: 1.799 high limit: 2.200
  - %PASS CALCUB Vforce external calibration test, 3 volts.

    Measured: 3.000 low limit: 2.799 high limit: 3.200
  - %PASS CALCUB Vforce external calibration test, 5 volts.

    Measured: 5.000 low limit: 4.799 high limit: 5.200
  - %PASS CALCUB Vforce external calibration test, 7 volts.

    Measured: 7.000 low limit: 6.799 high limit: 7.200
  - %PASS CALCUB Vforce external calibration test, 9 volts.

    Measured: 9.000 low limit: 8.799 high limit: 9.200
  - %PASS CALCUB Vforce external calibration test, 10 volts.

    Measured: 10.000 low limit: 9.799 high limit: 10.200
  - %PASS CALCUB Vforce external calibration test, 24 volts.

    Measured: 24.000 low limit: 23.799 high limit: 24.200
  - %PASS CALCUB Vforce external calibration test, -2 volts.

    Measured: -1.999 low limit: -2.200 high limit: -1.799

- %PASS CALCUB Vforce external calibration test, -5 volts.

  Measured: -4.999 low limit: -5.200 high limit: -4.799
- %PASS CALCUB Vforce external calibration test, -7 volts.

  Measured: -6.999 low limit: -7.200 high limit: -6.799
- %PASS CALCUB Vforce external calibration test, -10 volts.

  Measured: -9.999 low limit: -10.200 high limit: -9.799
- %PASS CALCUB Vforce external calibration test, -24 volts.

  Measured: -23.999 low limit: -24.200 high limit: -23.799
- %PASS CALCUB Vforce external calibration test, -9 volts.

  Measured: -8.999 low limit: -9.200 high limit: -8.799
- %PASS CALCUB Vforce external calibration test, 1 volts.

  Measured: 1.000 low limit: 0.7999 high limit: 1.200
- %PASS CALCUB Vforce external calibration test, 19 volts.

  Measured: 19.000 low limit: 18.799 high limit: 19.200
- %PASS CALCUB Vforce external calibration test, 21 volts.

  Measured: 21.000 low limit: 20.799 high limit: 21.200
- %PASS CALCUB Vforce external calibration test, 4 volts. Measured: 4.000 low limit: 3.799 high limit: 4.200
- %PASS CALCUB Vforce external calibration test, 6 volts.

  Measured: 6.000 low limit: 5.799 high limit: 6.200
- %PASS CALCUB Vforce external calibration test, 20 volts.

  Measured: 20.000 low limit: 19.799 high limit: 20.200
- %PASS CALCUB Vforce external calibration test, -19 volts.

  Measured: -18.999 low limit: -19.200 high limit: -18.799
- %PASS CALCUB Vforce external calibration test, -20 volts.

- Measured: -19.999 low limit: -20.200 high limit: -19.799
- %PASS CALCUB Vforce external calibration test, -21 volts.

  Measured: -20.999 low limit: -21.200 high limit: -20.799
- %PASS CALCUB Vforce external calibration test, 23 volts.

  Measured: 23.000 low limit: 22.799 high limit: 23.200
- %PASS CALCUB Vforce external calibration test, 22 volts.

  Measured: 22.000 low limit: 21.799 high limit: 22.200
- %PASS CALCUB Vforce external calibration test, 18 volts.

  Measured: 18.000 low limit: 17.799 high limit: 18.200
- %PASS CALCUB Vforce external calibration test, 17 volts.

  Measured: 17.000 low limit: 16.799 high limit: 17.200
- %PASS CALCUB Vforce external calibration test, 16 volts.

  Measured: 16.000 low limit: 15.799 high limit: 16.200
- %PASS CALCUB Vforce external calibration test, 15 volts.

  Measured: 15.000 low limit: 14.799 high limit: 15.200
- %PASS CALCUB Vforce external calibration test, 14 volts.

  Measured: 14.000 low limit: 13.799 high limit: 14.200
- %PASS CALCUB Vforce external calibration test, 13 volts.

  Measured: 13.000 low limit: 12.799 high limit: 13.200
- %PASS CALCUB Vforce external calibration test, 12 volts.

  Measured: 12.000 low limit: 11.799 high limit: 12.200
- %PASS CALCUB Vforce external calibration test, 11 volts.

  Measured: 11.000 low limit: 10.799 high limit: 11.200
- %PASS CALCUB Vforce external calibration test, 8 volts.

  Measured: 8.000 low limit: 7.799 high limit: 8.200

- %PASS CALCUB Vforce external calibration test, -3 volts.

  Measured: -2.999 low limit: -3.200 high limit: -2.799
- %PASS CALCUB Vforce external calibration test, -4 volts.

  Measured: -3.999 low limit: -4.200 high limit: -3.799
- %PASS CALCUB Vforce external calibration test, -6 volts.

  Measured: -5.999 low limit: -6.200 high limit: -5.799
- %PASS CALCUB Vforce external calibration test, -8 volts.

  Measured: -7.999 low limit: -8.200 high limit: -7.799
- %PASS CALCUB Vforce external calibration test, -11 volts.

  Measured: -10.999 low limit: -11.200 high limit: -10.799
- %PASS CALCUB Vforce external calibration test, -12 volts.

  Measured: -11.999 low limit: -12.200 high limit: -11.799
- %PASS CALCUB Vforce external calibration test, -13 volts.

  Measured: -12.999 low limit: -13.200 high limit: -12.799
- %PASS CALCUB Vforce external calibration test, -14 volts.

  Measured: -13.999 low limit: -14.200 high limit: -13.799
- %PASS CALCUB Vforce external calibration test, -15 volts.

  Measured: -14.999 low limit: -15.200 high limit: -14.799
- %PASS CALCUB Vforce external calibration test, -16 volts.

  Measured: -15.999 low limit: -16.200 high limit: -15.799
- %PASS CALCUB Vforce external calibration test, -17 volts.

  Measured: -16.999 low limit: -17.200 high limit: -16.799
- %PASS CALCUB Vforce external calibration test, -18 volts.

  Measured: -17.999 low limit: -18.200 high limit: -17.799
- %PASS CALCUB Vforce external calibration test, -22 volts.

  Measured: -21.999 low limit: -22.200 high limit: -21.799

- %PASS CALCUB Vforce external calibration test, -23 volts.

  Measured: -22.999 low limit: -23.200 high limit: -22.799
- %PASS CALCUB IForce external calibration test, 200 na.

  Measured: 0.000001999 low limit: 1.998E-06 high limit: 2.001E-06
- %PASS CALCUB IForce external calibration test, 200 na.

  Measured: -0.000002000 low limit: -2.001E-06 high limit: -1.998E-06
- %PASS CALCUB IForce external calibration test, 2 ua.

  Measured: 0.00002000 low limit: 1.998E-05 high limit: 2.001E-05
- %PASS CALCUB IForce external calibration test, 2 ua.

  Measured: -0.00001999 low limit: -2.001E-05 high limit: -1.998E-05
- %PASS CALCUB IForce external calibration test, 20 ua.

  Measured: 0.0001999 low limit: 1.998E-04 high limit: 2.001E-04
- %PASS CALCUB IForce external calibration test, 20 ua.

  Measured: -0.0001999 low limit: -2.001E-04 high limit: -1.998E-04
- %PASS CALCUB IForce external calibration test, 200 ua.

  Measured: 0.002000 low limit: 1.998E-03 high limit: 2.001E-03
- %PASS CALCUB IForce external calibration test, 200 ua.

  Measured: -0.001999 low limit: -2.001E-03 high limit: -1.998E-03
- %PASS CALCUB IForce external calibration test, 2 ma.

  Measured: 0.01999 low limit: 1.998E-02 high limit: 2.001E-02
- %PASS CALCUB IForce external calibration test, 2 ma.

  Measured: -0.01999 low limit: -2.001E-02 high limit: -1.998E-02
- %PASS CALCUB IForce external calibration test, 20 ma.

  Measured: 0.09999 low limit: 9.990E-02 high limit: 0.1000
- %PASS CALCUB IForce external calibration test, 20 ma.

- Measured: -0.09999 low limit: -0.1000 high limit: -9.990E-02
- %PASS CALCUB IForce external calibration test, 200 ma.

  Measured: 0.1999 low limit: 0.1994 high limit: 0.2005
- %PASS CALCUB IForce external calibration test, 200 ma.

  Measured: -0.2000 low limit: -0.2005 high limit: -0.1994
- %PASS Flash readback error of force DAC record 0 Measured: 8.697E-04 expected: 8.697E-04
- %PASS Flash readback error of external measurement record 0 Measured: 0.0007760 expected: 0.0007760
- %PASS Flash readback error of internal measurement record 0 Measured: 6.768E-04 expected: 6.768E-04
- %PASS Flash readback error of voltage flag record 0 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 1 Measured: 2.000 expected: 2.000
- %PASS Flash readback error of external measurement record 1 Measured: 2.000 expected: 2.000
- %PASS Flash readback error of internal measurement record 1 Measured: 1.998 expected: 1.998
- %PASS Flash readback error of voltage flag record 1 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 2 Measured: 5.000 expected: 5.000
- %PASS Flash readback error of external measurement record 2 Measured: 5.000 expected: 5.000

- %PASS Flash readback error of internal measurement record 2 Measured: 4.994 expected: 4.994
- %PASS Flash readback error of voltage flag record 2 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 3 Measured: 7.000 expected: 7.000
- %PASS Flash readback error of external measurement record 3 Measured: 7.000 expected: 7.000
- %PASS Flash readback error of internal measurement record 3 Measured: 6.993 expected: 6.993
- %PASS Flash readback error of voltage flag record 3 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 4 Measured: 10.000 expected: 10.000
- %PASS Flash readback error of external measurement record 4 Measured: 10.000 expected: 10.000
- %PASS Flash readback error of internal measurement record 4 Measured: 9.987 expected: 9.987
- %PASS Flash readback error of voltage flag record 4 Measured: 1 expected: 1  $\,$
- %PASS Flash readback error of force DAC record 5 Measured: 24.001 expected: 24.001
- %PASS Flash readback error of external measurement record 5 Measured: 24.000 expected: 24.000
- %PASS Flash readback error of internal measurement record 5 Measured: 23.973 expected: 23.973

- %PASS Flash readback error of voltage flag record 5 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 6 Measured: -2.000 expected: -2.000
- %PASS Flash readback error of external measurement record 6 Measured: -1.999 expected: -1.999
- %PASS Flash readback error of internal measurement record 6 Measured: -1.997 expected: -1.997
- %PASS Flash readback error of voltage flag record 6 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 7 Measured: -5.000 expected: -5.000
- %PASS Flash readback error of external measurement record 7 Measured: -4.999 expected: -4.999
- %PASS Flash readback error of internal measurement record 7 Measured: -4.994 expected: -4.994
- %PASS Flash readback error of voltage flag record 7 Measured: 1 expected: 1  $\,$
- %PASS Flash readback error of force DAC record 8 Measured: -7.001 expected: -7.001
- %PASS Flash readback error of external measurement record 8 Measured: -6.999 expected: -6.999
- %PASS Flash readback error of internal measurement record 8 Measured: -6.992 expected: -6.992
- $\mbox{\%PASS}$  Flash readback error of voltage flag record 8

- Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 9 Measured: -10.000 expected: -10.000
- %PASS Flash readback error of external measurement record 9 Measured: -9.999 expected: -9.999
- %PASS Flash readback error of internal measurement record 9 Measured: -9.988 expected: -9.988
- %PASS Flash readback error of voltage flag record 9 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 10 Measured: -24.001 expected: -24.001
- %PASS Flash readback error of external measurement record 10 Measured: -23.999 expected: -23.999
- %PASS Flash readback error of internal measurement record 10 Measured: -23.972 expected: -23.972
- %PASS Flash readback error of voltage flag record 10 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 11 Measured: 9999 expected: 9999
- %PASS Flash readback error of external measurement record 11 Measured: 0 expected: 0
- %PASS Flash readback error of internal measurement record 11 Measured: 7.121E-05 expected: 7.121E-05
- %PASS Flash readback error of voltage flag record 11 Measured: 0 expected: 0

- %PASS Flash readback error of force DAC record 12 Measured: 0.7787 expected: 0.7787
- %PASS Flash readback error of external measurement record 12 Measured: 0.2 expected: 0.2
- %PASS Flash readback error of internal measurement record 12 Measured: 0.1996 expected: 0.1996
- %PASS Flash readback error of voltage flag record 12 Measured: 0 expected: 0
- %PASS Flash readback error of force DAC record 13 Measured: 9999 expected: 9999
- %PASS Flash readback error of external measurement record 13 Measured: 0 expected: 0
- %PASS Flash readback error of internal measurement record 13 Measured: 7.986E-06 expected: 7.986E-06
- %PASS Flash readback error of voltage flag record 13 Measured: 0 expected: 0
- %PASS Flash readback error of force DAC record 14 Measured: 2.176 expected: 2.176
- %PASS Flash readback error of external measurement record 14 Measured: 0.02 expected: 0.02
- %PASS Flash readback error of internal measurement record 14 Measured: 2.003E-02 expected: 2.003E-02
- %PASS Flash readback error of voltage flag record 14 Measured: 0 expected: 0
- $\rm \%PASS$  Flash readback error of force DAC record 15 Measured: 9999 expected: 9999

- %PASS Flash readback error of external measurement record 15 Measured: 0 expected: 0  $\,$
- %PASS Flash readback error of internal measurement record 15 Measured: 1.003E-06 expected: 1.003E-06
- %PASS Flash readback error of voltage flag record 15 Measured: 0 expected: 0
- %PASS Flash readback error of force DAC record 16 Measured: 3.997 expected: 3.997
- %PASS Flash readback error of external measurement record 16 Measured: 0.002 expected: 0.002
- %PASS Flash readback error of internal measurement record 16 Measured: 1.999E-03 expected: 1.999E-03
- %PASS Flash readback error of voltage flag record 16 Measured: 0 expected: 0  $\,$
- %PASS Flash readback error of force DAC record 17 Measured: 9999 expected: 9999
- %PASS Flash readback error of external measurement record 17 Measured: 0 expected: 0
- %PASS Flash readback error of internal measurement record 17 Measured: 1.053E-07 expected: 1.053E-07
- %PASS Flash readback error of voltage flag record 17 Measured: 0 expected: 0
- %PASS Flash readback error of force DAC record 18 Measured: 3.958 expected: 3.958
- %PASS Flash readback error of external measurement record 18

- Measured: 0.0002 expected: 0.0002
- %PASS Flash readback error of internal measurement record 18 Measured: 1.998E-04 expected: 1.998E-04
- %PASS Flash readback error of voltage flag record 18 Measured: 0 expected: 0
- %PASS Flash readback error of force DAC record 19 Measured: 9999 expected: 9999
- %PASS Flash readback error of external measurement record 19 Measured: 0 expected: 0  $\,$
- %PASS Flash readback error of internal measurement record 19 Measured: 1.121E-08 expected: 1.121E-08
- %PASS Flash readback error of voltage flag record 19 Measured: 0 expected: 0
- %PASS Flash readback error of force DAC record 20 Measured: 3.955 expected: 3.955
- %PASS Flash readback error of external measurement record 20 Measured: 0.00002 expected: 0.00002
- %PASS Flash readback error of internal measurement record 20 Measured: 1.999E-05 expected: 1.999E-05
- %PASS Flash readback error of voltage flag record 20 Measured: 0 expected: 0
- %PASS Flash readback error of force DAC record 21 Measured: 9999 expected: 9999
- %PASS Flash readback error of external measurement record 21 Measured: 0 expected: 0

- %PASS Flash readback error of internal measurement record 21 Measured: 8.238E-10 expected: 8.238E-10
- %PASS Flash readback error of voltage flag record 21 Measured: 0 expected: 0
- %PASS Flash readback error of force DAC record 22 Measured: 3.952 expected: 3.952
- %PASS Flash readback error of external measurement record 22 Measured: 0.000002 expected: 0.000002
- %PASS Flash readback error of internal measurement record 22 Measured: 1.998E-06 expected: 1.998E-06
- %PASS Flash readback error of voltage flag record 22 Measured: 0 expected: 0
- %PASS Flash readback error of force DAC record 23 Measured: 9999 expected: 9999
- %PASS Flash readback error of external measurement record 23 Measured: 0 expected: 0  $\,$
- %PASS Flash readback error of internal measurement record 23 Measured: 3.840E-10 expected: 3.840E-10
- %PASS Flash readback error of voltage flag record 23 Measured: 0 expected: 0
- %PASS Flash readback error of force DAC record 24 Measured: 3.945 expected: 3.945
- %PASS Flash readback error of external measurement record 24 Measured: 0.0000002 expected: 0.0000002
- %PASS Flash readback error of internal measurement record 24 Measured: 2.001E-07 expected: 2.001E-07

- %PASS Flash readback error of voltage flag record 24 Measured: 0 expected: 0
- %PASS Flash readback error of force DAC record 25 Measured: 3.000 expected: 3.000
- %PASS Flash readback error of external measurement record 25 Measured: 3.000 expected: 3.000
- %PASS Flash readback error of internal measurement record 25 Measured: 2.997 expected: 2.997
- %PASS Flash readback error of voltage flag record 25 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 26 Measured: -0.9999 expected: -0.9999
- %PASS Flash readback error of external measurement record 26 Measured: -0.9997 expected: -0.9997
- %PASS Flash readback error of internal measurement record 26 Measured: -0.9982 expected: -0.9982
- %PASS Flash readback error of voltage flag record 26 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 27 Measured: 9.001 expected: 9.001
- %PASS Flash readback error of external measurement record 27 Measured: 9.000 expected: 9.000
- %PASS Flash readback error of internal measurement record 27 Measured: 8.990 expected: 8.990
- $\ensuremath{\mathrm{\%PASS}}$  Flash readback error of voltage flag record 27

- Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 28 Measured: 9999 expected: 9999
- %PASS Flash readback error of external measurement record 28 Measured: 0.0007757 expected: 0.0007757
- %PASS Flash readback error of internal measurement record 28 Measured: 3.073E-05 expected: 3.073E-05
- %PASS Flash readback error of voltage flag record 28 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 29 Measured: -9.001 expected: -9.001
- %PASS Flash readback error of external measurement record 29 Measured: -8.999 expected: -8.999
- %PASS Flash readback error of internal measurement record 29 Measured: -8.990 expected: -8.990
- %PASS Flash readback error of voltage flag record 29 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 30 Measured: 1.000 expected: 1.000
- %PASS Flash readback error of external measurement record 30 Measured: 1.000 expected: 1.000
- %PASS Flash readback error of internal measurement record 30 Measured: 0.9993 expected: 0.9993
- %PASS Flash readback error of voltage flag record 30 Measured: 1 expected: 1

- %PASS Flash readback error of force DAC record 31 Measured: 19.001 expected: 19.001
- %PASS Flash readback error of external measurement record 31 Measured: 19.000 expected: 19.000
- %PASS Flash readback error of internal measurement record 31 Measured: 18.979 expected: 18.979
- %PASS Flash readback error of voltage flag record 31 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 32 Measured: 21.001 expected: 21.001
- %PASS Flash readback error of external measurement record 32 Measured: 21.000 expected: 21.000
- %PASS Flash readback error of internal measurement record 32 Measured: 20.976 expected: 20.976
- %PASS Flash readback error of voltage flag record 32 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 33 Measured: 4.000 expected: 4.000
- %PASS Flash readback error of external measurement record 33 Measured: 4.000 expected: 4.000
- %PASS Flash readback error of internal measurement record 33 Measured: 3.996 expected: 3.996
- %PASS Flash readback error of voltage flag record 33 Measured: 1 expected: 1
- $\mbox{\%PASS}$  Flash readback error of force DAC record 34 Measured: 6.001 expected: 6.001

- %PASS Flash readback error of external measurement record 34 Measured: 6.000 expected: 6.000
- $\mbox{\it \%PASS}$  Flash readback error of internal measurement record 34 Measured: 5.994 expected: 5.994
- %PASS Flash readback error of voltage flag record 34 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 35 Measured: 20.000 expected: 20.000
- %PASS Flash readback error of external measurement record 35 Measured: 20.000 expected: 20.000
- %PASS Flash readback error of internal measurement record 35 Measured: 19.976 expected: 19.976
- %PASS Flash readback error of voltage flag record 35 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 36 Measured: -19.001 expected: -19.001
- %PASS Flash readback error of external measurement record 36 Measured: -18.999 expected: -18.999
- %PASS Flash readback error of internal measurement record 36 Measured: -18.978 expected: -18.978
- %PASS Flash readback error of voltage flag record 36 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 37 Measured: -20.001 expected: -20.001
- $\mbox{\%PASS}$  Flash readback error of external measurement record 37

- Measured: -19.999 expected: -19.999
- %PASS Flash readback error of internal measurement record 37 Measured: -19.978 expected: -19.978
- %PASS Flash readback error of voltage flag record 37 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 38 Measured: -21.001 expected: -21.001
- %PASS Flash readback error of external measurement record 38 Measured: -20.999 expected: -20.999
- %PASS Flash readback error of internal measurement record 38 Measured: -20.975 expected: -20.975
- %PASS Flash readback error of voltage flag record 38 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 39 Measured: 23.001 expected: 23.001
- %PASS Flash readback error of external measurement record 39 Measured: 23.000 expected: 23.000
- %PASS Flash readback error of internal measurement record 39 Measured: 22.974 expected: 22.974
- %PASS Flash readback error of voltage flag record 39 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 40 Measured: 22.001 expected: 22.001
- %PASS Flash readback error of external measurement record 40 Measured: 22.000 expected: 22.000

- %PASS Flash readback error of internal measurement record 40 Measured: 21.975 expected: 21.975
- %PASS Flash readback error of voltage flag record 40 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 41 Measured: 18.001 expected: 18.001
- %PASS Flash readback error of external measurement record 41 Measured: 18.000 expected: 18.000
- %PASS Flash readback error of internal measurement record 41 Measured: 17.980 expected: 17.980
- %PASS Flash readback error of voltage flag record 41 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 42 Measured: 17.001 expected: 17.001
- %PASS Flash readback error of external measurement record 42 Measured: 17.000 expected: 17.000
- %PASS Flash readback error of internal measurement record 42 Measured: 16.981 expected: 16.981
- %PASS Flash readback error of voltage flag record 42 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 43 Measured: 16.000 expected: 16.000
- %PASS Flash readback error of external measurement record 43 Measured: 16.000 expected: 16.000
- %PASS Flash readback error of internal measurement record 43 Measured: 15.982 expected: 15.982

- %PASS Flash readback error of voltage flag record 43 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 44 Measured: 15.001 expected: 15.001
- %PASS Flash readback error of external measurement record 44 Measured: 15.000 expected: 15.000
- %PASS Flash readback error of internal measurement record 44 Measured: 14.983 expected: 14.983
- %PASS Flash readback error of voltage flag record 44 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 45 Measured: 14.001 expected: 14.001
- %PASS Flash readback error of external measurement record 45 Measured: 14.000 expected: 14.000
- %PASS Flash readback error of internal measurement record 45 Measured: 13.985 expected: 13.985
- %PASS Flash readback error of voltage flag record 45 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 46 Measured: 13.000 expected: 13.000
- %PASS Flash readback error of external measurement record 46 Measured: 13.000 expected: 13.000
- %PASS Flash readback error of internal measurement record 46 Measured: 12.984 expected: 12.984
- $\ensuremath{\mathrm{\%PASS}}$  Flash readback error of voltage flag record 46

- Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 47 Measured: 12.000 expected: 12.000
- %PASS Flash readback error of external measurement record 47 Measured: 12.000 expected: 12.000
- %PASS Flash readback error of internal measurement record 47 Measured: 11.986 expected: 11.986
- %PASS Flash readback error of voltage flag record 47 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 48 Measured: 11.000 expected: 11.000
- %PASS Flash readback error of external measurement record 48 Measured: 11.000 expected: 11.000
- %PASS Flash readback error of internal measurement record 48 Measured: 10.986 expected: 10.986
- %PASS Flash readback error of voltage flag record 48 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 49 Measured: 8.001 expected: 8.001
- %PASS Flash readback error of external measurement record 49 Measured: 8.000 expected: 8.000
- %PASS Flash readback error of internal measurement record 49 Measured: 7.992 expected: 7.992
- %PASS Flash readback error of voltage flag record 49 Measured: 1 expected: 1

- %PASS Flash readback error of force DAC record 50 Measured: -2.999 expected: -2.999
- %PASS Flash readback error of external measurement record 50 Measured: -2.999 expected: -2.999
- %PASS Flash readback error of internal measurement record 50 Measured: -2.996 expected: -2.996
- %PASS Flash readback error of voltage flag record 50 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 51 Measured: -4.000 expected: -4.000
- %PASS Flash readback error of external measurement record 51 Measured: -3.999 expected: -3.999
- %PASS Flash readback error of internal measurement record 51 Measured: -3.995 expected: -3.995
- %PASS Flash readback error of voltage flag record 51 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 52 Measured: -6.000 expected: -6.000
- %PASS Flash readback error of external measurement record 52 Measured: -5.999 expected: -5.999
- %PASS Flash readback error of internal measurement record 52 Measured: -5.993 expected: -5.993
- %PASS Flash readback error of voltage flag record 52 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 53 Measured: -8.000 expected: -8.000  $\,$

- %PASS Flash readback error of external measurement record 53 Measured: -7.999 expected: -7.999
- %PASS Flash readback error of internal measurement record 53 Measured: -7.991 expected: -7.991
- %PASS Flash readback error of voltage flag record 53 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 54 Measured: -11.000 expected: -11.000
- %PASS Flash readback error of external measurement record 54 Measured: -10.999 expected: -10.999
- %PASS Flash readback error of internal measurement record 54 Measured: -10.985 expected: -10.985
- %PASS Flash readback error of voltage flag record 54 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 55 Measured: -12.000 expected: -12.000
- %PASS Flash readback error of external measurement record 55 Measured: -11.999 expected: -11.999
- %PASS Flash readback error of internal measurement record 55 Measured: -11.984 expected: -11.984
- %PASS Flash readback error of voltage flag record 55 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 56 Measured: -13.000 expected: -13.000
- $\ensuremath{\mathrm{\%PASS}}$  Flash readback error of external measurement record 56

- Measured: -12.999 expected: -12.999
- %PASS Flash readback error of internal measurement record 56 Measured: -12.984 expected: -12.984
- %PASS Flash readback error of voltage flag record 56 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 57 Measured: -14.000 expected: -14.000
- %PASS Flash readback error of external measurement record 57 Measured: -13.999 expected: -13.999
- %PASS Flash readback error of internal measurement record 57 Measured: -13.983 expected: -13.983
- %PASS Flash readback error of voltage flag record 57 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 58 Measured: -15.001 expected: -15.001
- %PASS Flash readback error of external measurement record 58 Measured: -14.999 expected: -14.999
- %PASS Flash readback error of internal measurement record 58 Measured: -14.982 expected: -14.982
- %PASS Flash readback error of voltage flag record 58 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 59 Measured: -16.001 expected: -16.001
- %PASS Flash readback error of external measurement record 59 Measured: -15.999 expected: -15.999

- %PASS Flash readback error of internal measurement record 59 Measured: -15.981 expected: -15.981
- %PASS Flash readback error of voltage flag record 59 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 60 Measured: -17.000 expected: -17.000
- %PASS Flash readback error of external measurement record 60 Measured: -16.999 expected: -16.999
- %PASS Flash readback error of internal measurement record 60 Measured: -16.980 expected: -16.980
- %PASS Flash readback error of voltage flag record 60 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 61 Measured: -18.001 expected: -18.001
- %PASS Flash readback error of external measurement record 61 Measured: -17.999 expected: -17.999
- %PASS Flash readback error of internal measurement record 61 Measured: -17.979 expected: -17.979
- %PASS Flash readback error of voltage flag record 61 Measured: 1 expected: 1  $\,$
- %PASS Flash readback error of force DAC record 62 Measured: -22.001 expected: -22.001
- %PASS Flash readback error of external measurement record 62 Measured: -21.999 expected: -21.999
- %PASS Flash readback error of internal measurement record 62 Measured: -21.974 expected: -21.974

- %PASS Flash readback error of voltage flag record 62 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 63 Measured: -23.001 expected: -23.001
- %PASS Flash readback error of external measurement record 63 Measured: -22.999 expected: -22.999
- %PASS Flash readback error of internal measurement record 63 Measured: -22.974 expected: -22.974
- %PASS Flash readback error of voltage flag record 63 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 64 Measured: 9999 expected: 9999
- %PASS Flash readback error of external measurement record 64 Measured: 9999 expected: 9999
- %PASS Flash readback error of internal measurement record 64 Measured: 9999 expected: 9999
- %PASS Flash readback error of voltage flag record 64 Measured: 9999 expected: 9999
- %PASS Flash readback error of force DAC record 65 Measured: 9999 expected: 9999
- % PASS Flash readback error of external measurement record 65 Measured: 9999 expected: 9999
- %PASS Flash readback error of internal measurement record 65 Measured: 9999 expected: 9999
- $\ensuremath{\mathrm{\%PASS}}$  Flash readback error of voltage flag record 65

- Measured: 9999 expected: 9999
- %PASS Flash readback error of force DAC record 66 Measured: 9999 expected: 9999
- %PASS Flash readback error of external measurement record 66 Measured: 9999 expected: 9999
- %PASS Flash readback error of internal measurement record 66 Measured: 9999 expected: 9999
- %PASS Flash readback error of voltage flag record 66 Measured: 9999 expected: 9999
- %PASS Flash readback error of force DAC record 67 Measured: 9999 expected: 9999
- %PASS Flash readback error of external measurement record 67 Measured: 9999 expected: 9999
- %PASS Flash readback error of internal measurement record 67 Measured: 9999 expected: 9999
- %PASS Flash readback error of voltage flag record 67 Measured: 9999 expected: 9999
- %PASS Flash readback error of force DAC record 68 Measured: 9999 expected: 9999
- %PASS Flash readback error of external measurement record 68 Measured: 9999 expected: 9999
- %PASS Flash readback error of internal measurement record 68 Measured: 9999 expected: 9999
- %PASS Flash readback error of voltage flag record 68 Measured: 9999 expected: 9999

- %PASS Flash readback error of force DAC record 69 Measured: 9999 expected: 9999
- %PASS Flash readback error of external measurement record 69 Measured: 9999 expected: 9999
- %PASS Flash readback error of internal measurement record 69 Measured: 9999 expected: 9999
- %PASS Flash readback error of voltage flag record 69 Measured: 9999 expected: 9999
- %PASS Flash readback error of force DAC record 70 Measured: 9999 expected: 9999
- %PASS Flash readback error of external measurement record 70 Measured: 9999 expected: 9999
- %PASS Flash readback error of internal measurement record 70 Measured: 9999 expected: 9999
- %PASS Flash readback error of voltage flag record 70 Measured: 9999 expected: 9999
- %PASS Flash readback error of force DAC record 71 Measured: 9999 expected: 9999
- $\rm \%PASS$  Flash readback error of external measurement record 71 Measured: 9999 expected: 9999
- %PASS Flash readback error of internal measurement record 71 Measured: 9999 expected: 9999
- %PASS Flash readback error of voltage flag record 71 Measured: 9999 expected: 9999
- %PASS Flash readback error of force DAC record 72 Measured: 9999 expected: 9999

- %PASS Flash readback error of external measurement record 72 Measured: 9999 expected: 9999
- %PASS Flash readback error of internal measurement record 72 Measured: 9999 expected: 9999
- %PASS Flash readback error of voltage flag record 72 Measured: 9999 expected: 9999
- %PASS Flash readback error of force DAC record 73 Measured: 9999 expected: 9999
- %PASS Flash readback error of external measurement record 73 Measured: 9999 expected: 9999
- %PASS Flash readback error of internal measurement record 73 Measured: 9999 expected: 9999
- %PASS Flash readback error of voltage flag record 73 Measured: 9999 expected: 9999
- %PASS Flash readback error of force DAC record 74 Measured: 9999 expected: 9999
- %PASS Flash readback error of external measurement record 74 Measured: 9999 expected: 9999
- %PASS Flash readback error of internal measurement record 74 Measured: 9999 expected: 9999
- %PASS Flash readback error of voltage flag record 74 Measured: 9999 expected: 9999

## ${\bf Slot 18\_CALCUB\_ExternalPV}$

- %JOB\_START Beginning CUB External Verification test on slot 18 at 5:02:56 PM on 2/29/2020 Workbook Rev V7.30.12\_0835\_MOUT IG-XL Version: 3.40.13 DIB # 305253E Rev 1137A
  - Performing source and measure voltage verification...
  - %PASS CALCUB test of source voltage at -24V Measured: -23.999741 low limit: -24.005417 high limit: -23.994582
  - %PASS CALCUB test of voltage measure at -24V Measured: -24.000028 low limit: -24.002793 high limit: -23.996689
  - %PASS CALCUB test of source voltage at -23V Measured: -22.999707 low limit: -23.005217 high limit: -22.994782
  - %PASS CALCUB test of voltage measure at -23V Measured: -22.999847 low limit: -23.002758 high limit: -22.996655
  - %PASS CALCUB test of source voltage at -22V Measured: -21.999500 low limit: -22.005017 high limit: -21.994982
  - %PASS CALCUB test of voltage measure at -22V Measured: -21.999731 low limit: -22.002551 high limit: -21.996448
  - %PASS CALCUB test of source voltage at -21V Measured: -20.999719 low limit: -21.004817 high limit: -20.995182
  - %PASS CALCUB test of voltage measure at -21V Measured: -20.999810 low limit: -21.002771 high limit: -20.996667
  - %PASS CALCUB test of source voltage at -20V Measured: -19.999842 low limit: -20.004617 high limit: -19.995382
  - %PASS CALCUB test of voltage measure at -20V Measured: -19.999993 low limit: -20.002894 high limit: -19.996791
  - %PASS CALCUB test of source voltage at -19V

- %PASS CALCUB test of voltage measure at -19V Measured: -19.000159 low limit: -19.003068 high limit: -18.996964
- %PASS CALCUB test of source voltage at -18V Measured: -17.999776 low limit: -18.004217 high limit: -17.995782
- %PASS CALCUB test of voltage measure at -18V Measured: -17.999851 low limit: -18.002828 high limit: -17.996724
- %PASS CALCUB test of source voltage at -17V Measured: -16.999967 low limit: -17.004017 high limit: -16.995982
- %PASS CALCUB test of voltage measure at -17V Measured: -16.999913 low limit: -17.003019 high limit: -16.996915
- %PASS CALCUB test of source voltage at -16V Measured: -16.000052 low limit: -16.003817 high limit: -15.996182
- %PASS CALCUB test of voltage measure at -16V

  Measured: -16.000096 low limit: -16.003103 high limit: -15.997000
- %PASS CALCUB test of source voltage at -15V Measured: -14.999866 low limit: -15.003617 high limit: -14.996382
- %PASS CALCUB test of voltage measure at -15V Measured: -14.999963 low limit: -15.002918 high limit: -14.996814
- %PASS CALCUB test of source voltage at -14V

  Measured: -14.000035 low limit: -14.003417 high limit: -13.996582
- %PASS CALCUB test of voltage measure at -14V Measured: -14.000073 low limit: -14.003087 high limit: -13.996983
- %PASS CALCUB test of source voltage at -13V Measured: -12.999790 low limit: -13.003217 high limit: -12.996782

- %PASS CALCUB test of voltage measure at -13V Measured: -12.999788 low limit: -13.002842 high limit: -12.996739
- %PASS CALCUB test of source voltage at -12V Measured: -11.999957 low limit: -12.003017 high limit: -11.996982
- %PASS CALCUB test of voltage measure at -12V Measured: -11.999942 low limit: -12.003009 high limit: -11.996905
- %PASS CALCUB test of source voltage at -11V Measured: -11.000173 low limit: -11.002817 high limit: -10.997182
- %PASS CALCUB test of voltage measure at -11V

  Measured: -11.000214 low limit: -11.003225 high limit: -10.997121
- %PASS CALCUB test of source voltage at -10V Measured: -9.999893 low limit: -10.002617 high limit: -9.997382
- %PASS CALCUB test of voltage measure at -10V

  Measured: -9.999974 low limit: -10.002945 high limit: -9.996841
- %PASS CALCUB test of source voltage at -9V Measured: -8.999910 low limit: -9.002417 high limit: -8.997582
- %PASS CALCUB test of voltage measure at -9V Measured: -8.999955 low limit: -9.000673 high limit: -8.999147
- %PASS CALCUB test of source voltage at -8V Measured: -7.999757 low limit: -8.002217 high limit: -7.997782
- %PASS CALCUB test of voltage measure at -8V Measured: -7.999749 low limit: -8.000520 high limit: -7.998994
- %PASS CALCUB test of source voltage at -7V Measured: -7.000367 low limit: -7.002017 high limit: -6.997982
- %PASS CALCUB test of voltage measure at -7V Measured: -7.000397 low limit: -7.001130 high limit: -6.999604

- %PASS CALCUB test of source voltage at -6V
  Measured: -6.000239 low limit: -6.001817 high limit: -5.998182
- %PASS CALCUB test of voltage measure at -6V
  Measured: -6.000229 low limit: -6.001002 high limit: -5.999476
- %PASS CALCUB test of source voltage at -5V

  Measured: -5.000055 low limit: -5.001617 high limit: -4.998382
- %PASS CALCUB test of voltage measure at -5V Measured: -5.000062 low limit: -5.000818 high limit: -4.999292
- %PASS CALCUB test of source voltage at -4V
  Measured: -3.999865 low limit: -4.001417 high limit: -3.998582
- %PASS CALCUB test of voltage measure at -4V
  Measured: -3.999890 low limit: -4.000628 high limit: -3.999102
- %PASS CALCUB test of source voltage at -3V Measured: -2.999748 low limit: -3.001217 high limit: -2.998782
- %PASS CALCUB test of voltage measure at -3V Measured: -2.999756 low limit: -3.000511 high limit: -2.998985
- %PASS CALCUB test of source voltage at -2V Measured: -1.999951 low limit: -2.001017 high limit: -1.998982
- %PASS CALCUB test of voltage measure at -2V Measured: -1.999961 low limit: -2.000714 high limit: -1.999188
- %PASS CALCUB test of source voltage at -2V at 200mA Measured: -1.999554 low limit: -2.001235 high limit: -1.998764
- %PASS CALCUB test of source voltage at -1V Measured: -1.000213 low limit: -1.000817 high limit: -0.9991820
- %PASS CALCUB test of voltage measure at -1V

- Measured: -1.000186 low limit: -1.000976 high limit: -0.9994504
- %PASS CALCUB test of source voltage at 0V Measured: -0.00007462692 low limit: -6.179903E-04 high limit: 6.179903E-04
- %PASS CALCUB test of source voltage at 1V
  Measured: 1.000213 low limit: 0.9991820 high limit: 1.000817
- %PASS CALCUB test of voltage measure at 1V

  Measured: 1.000205 low limit: 0.9994500 high limit: 1.000976
- %PASS CALCUB test of source voltage at 2V Measured: 1.999991 low limit: 1.998982 high limit: 2.001017
- %PASS CALCUB test of voltage measure at 2V Measured: 2.000025 low limit: 1.999228 high limit: 2.000754
- %PASS CALCUB test of source voltage at 2V at 200mA Measured: 1.999617 low limit: 1.998764 high limit: 2.001235
- %PASS CALCUB test of source voltage at 3V Measured: 2.999824 low limit: 2.998782 high limit: 3.001217
- %PASS CALCUB test of voltage measure at 3V Measured: 2.999843 low limit: 2.999061 high limit: 3.000587
- %PASS CALCUB test of source voltage at 3V with DGS perturbed high Measured: 3.174671 low limit: 3.1 high limit: 3.3
- %PASS CALCUB test of voltage measure at 3V with DGS perturbed high Measured: 2.999821 low limit: 2.999061 high limit: 3.000587
- %PASS CALCUB test of source voltage at 3V with DGS perturbed low Measured: 2.782949 low limit: 2.7 high limit: 2.9

- %PASS CALCUB test of voltage measure at 3V with DGS perturbed low Measured: 2.999824 low limit: 2.999061 high limit: 3.000587
- %PASS CALCUB test of source voltage at 4V Measured: 3.999964 low limit: 3.998582 high limit: 4.001417
- %PASS CALCUB test of voltage measure at 4V Measured: 3.999996 low limit: 3.999201 high limit: 4.000727
- %PASS CALCUB test of source voltage at 5V Measured: 5.000190 low limit: 4.998382 high limit: 5.001617
- %PASS CALCUB test of voltage measure at 5V Measured: 5.000173 low limit: 4.999427 high limit: 5.000953
- %PASS CALCUB test of source voltage at 6V Measured: 5.999998 low limit: 5.998182 high limit: 6.001817
- %PASS CALCUB test of voltage measure at 6V Measured: 6.000018 low limit: 5.999235 high limit: 6.000761
- %PASS CALCUB test of source voltage at 7V Measured: 6.999752 low limit: 6.997982 high limit: 7.002017
- %PASS CALCUB test of voltage measure at 7V Measured: 6.999790 low limit: 6.998989 high limit: 7.000515
- %PASS CALCUB test of source voltage at 8V Measured: 7.999991 low limit: 7.997782 high limit: 8.002217
- %PASS CALCUB test of voltage measure at 8V Measured: 8.000006 low limit: 7.999228 high limit: 8.000754
- %PASS CALCUB test of source voltage at 9V Measured: 9.000183 low limit: 8.997582 high limit: 9.002417
- %PASS CALCUB test of voltage measure at 9V Measured: 9.000182 low limit: 8.999420 high limit: 9.000946

- %PASS CALCUB test of source voltage at 10V Measured: 10.000161 low limit: 9.997382 high limit: 10.002617
- %PASS CALCUB test of voltage measure at 10V

  Measured: 10.000142 low limit: 9.997109 high limit: 10.003213
- %PASS CALCUB test of source voltage at 11V Measured: 10.999619 low limit: 10.997182 high limit: 11.002817
- %PASS CALCUB test of voltage measure at 11V Measured: 10.999484 low limit: 10.996567 high limit: 11.002671
- %PASS CALCUB test of source voltage at 12V Measured: 11.999854 low limit: 11.996982 high limit: 12.003017
- %PASS CALCUB test of voltage measure at 12V Measured: 11.999840 low limit: 11.996802 high limit: 12.002906
- %PASS CALCUB test of source voltage at 13V Measured: 13.000050 low limit: 12.996782 high limit: 13.003217
- %PASS CALCUB test of voltage measure at 13V Measured: 12.999955 low limit: 12.996998 high limit: 13.003102
- %PASS CALCUB test of source voltage at 14V Measured: 13.999912 low limit: 13.996582 high limit: 14.003417
- %PASS CALCUB test of voltage measure at 14V Measured: 13.999932 low limit: 13.996861 high limit: 14.002964
- %PASS CALCUB test of source voltage at 15V Measured: 15.000215 low limit: 14.996382 high limit: 15.003617
- %PASS CALCUB test of voltage measure at 15V Measured: 15.000154 low limit: 14.997163 high limit: 15.003267
- $\mbox{\%PASS}$  CALCUB test of source voltage at  $16\mbox{V}$

- Measured: 15.999584 low limit: 15.996182 high limit: 16.003817
- %PASS CALCUB test of voltage measure at 16V Measured: 15.999857 low limit: 15.996532 high limit: 16.002636
- %PASS CALCUB test of source voltage at 17V Measured: 16.999977 low limit: 16.995982 high limit: 17.004017
- %PASS CALCUB test of voltage measure at 17V Measured: 16.999892 low limit: 16.996925 high limit: 17.003029
- %PASS CALCUB test of source voltage at 18V Measured: 17.999876 low limit: 17.995782 high limit: 18.004217
- %PASS CALCUB test of voltage measure at 18V Measured: 18.000025 low limit: 17.996824 high limit: 18.002928
- %PASS CALCUB test of source voltage at 19V Measured: 19.000067 low limit: 18.995582 high limit: 19.004417
- %PASS CALCUB test of voltage measure at 19V Measured: 19.000143 low limit: 18.997015 high limit: 19.003119
- %PASS CALCUB test of source voltage at 20V Measured: 19.999973 low limit: 19.995382 high limit: 20.004617
- %PASS CALCUB test of voltage measure at 20V Measured: 20.000103 low limit: 19.996921 high limit: 20.003025
- %PASS CALCUB test of source voltage at 21V Measured: 20.999910 low limit: 20.995182 high limit: 21.004817
- %PASS CALCUB test of voltage measure at 21V Measured: 21.000003 low limit: 20.996858 high limit: 21.002962
- %PASS CALCUB test of source voltage at 22V Measured: 22.000119 low limit: 21.994982 high limit: 22.005017

%PASS - CALCUB test of voltage measure at 22V Measured: 22.000207 low limit: 21.997067 high limit: 22.003170

%PASS - CALCUB test of source voltage at 23V Measured: 23.000021 low limit: 22.994782 high limit: 23.005217

%PASS - CALCUB test of voltage measure at 23V Measured: 22.999989 low limit: 22.996969 high limit: 23.003073

%PASS - CALCUB test of source voltage at 24V
Measured: 24.000244 low limit: 23.994582 high limit: 24.005417

%PASS - CALCUB test of voltage measure at 24V
Measured: 24.000310 low limit: 23.997192 high limit: 24.003296

- Performing current measure verification...

 $\ensuremath{\mathrm{WPASS}}$  - CALCUB test of current measure at -0.00002472105738mA with 2Mohm source impedance

Measured: -2.5073E-05 low limit: -2.5941E-05 high limit: -2.3500E-05

 $\ensuremath{\mathrm{WPASS}}$  - CALCUB test of current measure at  $0.00002508981454\mathrm{mA}$  with 2Mohm source impedance

Measured: 2.4489E-05 low limit: 2.3869E-05 high limit: 2.6310E-05

 $\ensuremath{\mathrm{\%PASS}}$  - CALCUB test of current measure at -0.00004964199351mA with 2Mohm source impedance

Measured: -4.9839E-05 low limit: -5.0862E-05 high limit: -4.8421E-05

 $\ensuremath{\mathrm{WPASS}}$  - CALCUB test of current measure at  $0.0000496747848\mathrm{mA}$  with 2Mohm source impedance

Measured: 4.9215E-05 low limit: 4.8454E-05 high limit: 5.0895E-05

 $\ensuremath{\mathrm{\%PASS}}$  - CALCUB test of current measure at -0.00009838308312mA with 2Mohm source impedance

Measured: -9.9355E-05 low limit: -9.9603E-05 high limit: -9.7162E-05

 $\ensuremath{\mathrm{\%PASS}}$  - CALCUB test of current measure at  $0.00009923208785 \mathrm{mA}$  with 2Mohm source impedance

Measured: 9.9022E-05 low limit: 9.8011E-05 high limit: 1.0045E-04

 $\ensuremath{\mathrm{\%PASS}}$  - CALCUB test of current measure at -0.0001988542955mA with 2Mohm source impedance

Measured: -1.9891E-04 low limit: -2.0007E-04 high limit: -1.9763E-04

 $\ensuremath{\mathrm{WPASS}}$  - CALCUB test of current measure at  $0.0001987579832\mathrm{mA}$  with 2Mohm source impedance

Measured: 1.9829E-04 low limit: 1.9753E-04 high limit: 1.9997E-04

 $\ensuremath{\mathrm{\%PASS}}$  - CALCUB test of current measure at -0.0004970692285mA with 2Mohm source impedance

Measured: -4.9729E-04 low limit: -4.9828E-04 high limit: -4.9584E-04

 $\ensuremath{\mathrm{\%PASS}}$  - CALCUB test of current measure at  $0.0004971488425 \mathrm{mA}$  with 2Mohm source impedance

Measured: 4.9676E-04 low limit: 4.9592E-04 high limit: 4.9836E-04

 $\ensuremath{\mathrm{\%PASS}}$  - CALCUB test of current measure at -0.001013985266mA with 2Mohm source impedance

Measured: -1.0140E-03 low limit: -1.0152E-03 high limit: -1.0127E-03

 $\ensuremath{\mathrm{\%PASS}}$  - CALCUB test of current measure at 0.001014160869mA with 2Mohm source impedance

Measured: 1.0135E-03 low limit: 1.0129E-03 high limit: 1.0153E-03

- %PASS CALCUB test of current measure at -0.00202815034mA with 2Mohm source impedance Measured: -2.0279E-03 low limit: -2.0293E-03 high limit: -2.0269E-03
- $\ensuremath{\mathrm{\%PASS}}$  CALCUB test of current measure at 0.002028243066mA with 2Mohm source impedance

Measured: 2.0273E-03 low limit: 2.0270E-03 high limit: 2.0294E-03

 $\ensuremath{\mathrm{WPASS}}$  - CALCUB test of current measure at -0.0002066194426mA with 200Kohm source impedance

Measured: -2.0643E-04 low limit: -2.1882E-04 high limit: -1.9441E-04

%PASS - CALCUB test of current measure at 0.0002079965483mA with 200Kohm source

impedance

Measured: 2.0624E-04 low limit: 1.9578E-04 high limit: 2.2020E-04

 $\ensuremath{\mathrm{\%PASS}}$  - CALCUB test of current measure at -0.0004145605227mA with 200Kohm source impedance

Measured: -4.1195E-04 low limit: -4.2676E-04 high limit: -4.0235E-04

 $\ensuremath{\mathrm{\%PASS}}$  - CALCUB test of current measure at  $0.0004145860213\mathrm{mA}$  with 200Kohm source impedance

Measured: 4.1199E-04 low limit: 4.0237E-04 high limit: 4.2679E-04

 $\ensuremath{\mathrm{\%PASS}}$  - CALCUB test of current measure at -0.0009887101775mA with 200Kohm source impedance

Measured: -9.8640E-04 low limit: -1.0009E-03 high limit: -9.7650E-04

 $\ensuremath{\mathrm{\%PASS}}$  - CALCUB test of current measure at  $0.0009906415881 \mathrm{mA}$  with 200Kohm source impedance

Measured: 9.8909E-04 low limit: 9.7843E-04 high limit: 1.0028E-03

 $\ensuremath{\mathrm{\%PASS}}$  - CALCUB test of current measure at -0.001979341807mA with 200Kohm source impedance

Measured: -1.9773E-03 low limit: -1.9915E-03 high limit: -1.9671E-03

 $\ensuremath{\mathrm{\%PASS}}$  - CALCUB test of current measure at 0.001979422693mA with 200Kohm source impedance

Measured: 1.9775E-03 low limit: 1.9672E-03 high limit: 1.9916E-03

 $\ensuremath{\mathrm{\%PASS}}$  - CALCUB test of current measure at -0.004949898595mA with 200Kohm source impedance

Measured: -4.9485E-03 low limit: -4.9621E-03 high limit: -4.9376E-03

 $\ensuremath{\mathrm{\%PASS}}$  - CALCUB test of current measure at 0.00494970916mA with 200Kohm source impedance

Measured: 4.9492E-03 low limit: 4.9375E-03 high limit: 4.9619E-03

 $\ensuremath{\mathrm{\%PASS}}$  - CALCUB test of current measure at -0.0101202918mA with 200Kohm source impedance

Measured: -1.0119E-02 low limit: -1.0132E-02 high limit: -1.0108E-02

 $\ensuremath{\mathrm{\%PASS}}$  - CALCUB test of current measure at 0.01012018834mA with 200Kohm source impedance

Measured: 1.0120E-02 low limit: 1.0107E-02 high limit: 1.0132E-02

 $\ensuremath{\mathrm{WPASS}}$  - CALCUB test of current measure at -0.02024054333mA with 200Kohm source impedance

Measured: -2.0239E-02 low limit: -2.0252E-02 high limit: -2.0228E-02

 $\ensuremath{\mathrm{\%PASS}}$  - CALCUB test of current measure at  $0.02024093983 \mathrm{mA}$  with 200Kohm source impedance

Measured: 0.020239 low limit: 2.0228E-02 high limit: 2.0253E-02

 $\ensuremath{\mathrm{\%PASS}}$  - CALCUB test of current measure at -0.001992747822mA with 20Kohm source impedance

Measured: -1.9945E-03 low limit: -2.1148E-03 high limit: -1.8706E-03

 $\ensuremath{\mathrm{WPASS}}$  - CALCUB test of current measure at  $0.002007111187 \mathrm{mA}$  with 20Kohm source impedance

Measured: 1.9885E-03 low limit: 1.8850E-03 high limit: 2.1291E-03

 $\ensuremath{\mathrm{\%PASS}}$  - CALCUB test of current measure at -0.004001609616mA with 20Kohm source impedance

Measured: -3.9962E-03 low limit: -4.1236E-03 high limit: -3.8795E-03

% PASS - CALCUB test of current measure at 0.004000026424 mA with 20 Kohm source impedance

Measured: 3.9769E-03 low limit: 3.8779E-03 high limit: 4.1221E-03

 $\ensuremath{\mathrm{\%PASS}}$  - CALCUB test of current measure at -0.009712863644mA with 20Kohm source impedance

Measured: -9.6871E-03 low limit: -9.8349E-03 high limit: -9.5907E-03

 $\ensuremath{\mathrm{WPASS}}$  - CALCUB test of current measure at  $0.009730763799\mathrm{mA}$  with 20Kohm source impedance

Measured: 9.7075E-03 low limit: 9.6086E-03 high limit: 9.8528E-03

%PASS - CALCUB test of current measure at -0.01944474033mA with 20Kohm source

impedance

Measured: -1.9425E-02 low limit: -0.019566 high limit: -0.019322

 $\ensuremath{\mathrm{\%PASS}}$  - CALCUB test of current measure at  $0.01944320725 \mathrm{mA}$  with 20Kohm source impedance

Measured: 1.9423E-02 low limit: 0.019321 high limit: 0.019565

 $\ensuremath{\mathrm{\%PASS}}$  - CALCUB test of current measure at -0.04862348596mA with 20Kohm source impedance

Measured: -4.8612E-02 low limit: -0.048745 high limit: -0.048501

% PASS - CALCUB test of current measure at 0.04862203423 mA with 20 Kohm source impedance

Measured: 4.8615E-02 low limit: 0.048499 high limit: 0.048744

- %PASS CALCUB test of current measure at -0.1011412257mA with 20Kohm source impedance Measured: -0.10113 low limit: -0.10126 high limit: -0.10101
- %PASS CALCUB test of current measure at 0.1011447515mA with 20Kohm source impedance Measured: 0.10113 low limit: 0.10102 high limit: 0.10126
- %PASS CALCUB test of current measure at -0.2022855226mA with 20Kohm source impedance Measured: -0.20228 low limit: -0.20240 high limit: -0.20216
- %PASS CALCUB test of current measure at 0.2022928191mA with 20Kohm source impedance Measured: 0.20228 low limit: 0.20217 high limit: 0.20241
- %PASS CALCUB test of current measure at -0.01740556647mA with 2Kohm source impedance Measured: -1.7379E-02 low limit: -1.8626E-02 high limit: -1.6184E-02
- %PASS CALCUB test of current measure at 0.01752870182mA with 2Kohm source impedance Measured: 0.017330 low limit: 1.6307E-02 high limit: 1.8749E-02
- %PASS CALCUB test of current measure at -0.0349508889mA with 2Kohm source impedance Measured: -3.4952E-02 low limit: -3.6171E-02 high limit: -3.3730E-02
- %PASS CALCUB test of current measure at 0.03493210571mA with 2Kohm source impedance Measured: 3.4755E-02 low limit: 3.3711E-02 high limit: 3.6152E-02

- %PASS CALCUB test of current measure at -0.09662880271mA with 2Kohm source impedance Measured: -9.6377E-02 low limit: -9.7849E-02 high limit: -9.5408E-02
- %PASS CALCUB test of current measure at 0.09681005422mA with 2Kohm source impedance Measured: 9.6619E-02 low limit: 9.5589E-02 high limit: 9.8030E-02
- %PASS CALCUB test of current measure at -0.1934530244mA with 2Kohm source impedance Measured: -0.19326 low limit: -0.19467 high limit: -0.19223
- %PASS CALCUB test of current measure at 0.1934364151mA with 2Kohm source impedance Measured: 0.19330 low limit: 0.19221 high limit: 0.19465
- %PASS CALCUB test of current measure at -0.4837517402mA with 2Kohm source impedance Measured: -0.48358 low limit: -0.48497 high limit: -0.48253
- %PASS CALCUB test of current measure at 0.4837560761mA with 2Kohm source impedance Measured: 0.48368 low limit: 0.48253 high limit: 0.48497
- %PASS CALCUB test of current measure at -1.010612408mA with 2Kohm source impedance Measured: -1.0105 low limit: -1.0118 high limit: -1.0093
- %PASS CALCUB test of current measure at 1.010613734mA with 2Kohm source impedance Measured: 1.0105 low limit: 1.0093 high limit: 1.0118
- %PASS CALCUB test of current measure at -2.021189008mA with 2Kohm source impedance Measured: -2.0211 low limit: -2.0224 high limit: -2.0199
- %PASS CALCUB test of current measure at 2.021300986mA with 2Kohm source impedance Measured: 2.0212 low limit: 2.0200 high limit: 2.0225
- %PASS CALCUB test of current measure at -0.167644747mA with 200ohm source impedance Measured: -0.16702 low limit: -0.17985 high limit: -0.15543
- %PASS CALCUB test of current measure at 0.1688699911mA with 200ohm source impedance Measured: 0.16656 low limit: 0.15666 high limit: 0.18107
- %PASS CALCUB test of current measure at -0.3366464063mA with 200ohm source impedance

- Measured: -0.33611 low limit: -0.34885 high limit: -0.32443
- %PASS CALCUB test of current measure at 0.3365386846mA with 200ohm source impedance Measured: 0.33431 low limit: 0.32433 high limit: 0.34874
- %PASS CALCUB test of current measure at -0.9572175542mA with 200ohm source impedance Measured: -0.95500 low limit: -0.96942 high limit: -0.94501
- %PASS CALCUB test of current measure at 0.9589057536mA with 200ohm source impedance Measured: 0.95720 low limit: 0.94669 high limit: 0.97111
- %PASS CALCUB test of current measure at -1.916341093mA with 200ohm source impedance Measured: -1.9143 low limit: -1.9285 high limit: -1.9041
- %PASS CALCUB test of current measure at 1.916292082mA with 200ohm source impedance Measured: 1.9148 low limit: 1.9040 high limit: 1.9284
- %PASS CALCUB test of current measure at -4.791905826mA with 200ohm source impedance Measured: -4.7912 low limit: -4.8041 high limit: -4.7796
- %PASS CALCUB test of current measure at 4.792596951mA with 200ohm source impedance Measured: 4.7923 low limit: 4.7803 high limit: 4.8048
- %PASS CALCUB test of current measure at -10.00467395mA with 200ohm source impedance Measured: -10.0043 low limit: -10.0168 high limit: -9.9924
- %PASS CALCUB test of current measure at 10.00701174mA with 200ohm source impedance Measured: 10.0062 low limit: 9.9948 high limit: 10.0192
- %PASS CALCUB test of current measure at -20.00978357mA with 200ohm source impedance Measured: -20.0096 low limit: -20.0219 high limit: -19.9975
- %PASS CALCUB test of current measure at 20.01448041mA with 200ohm source impedance Measured: 20.0144 low limit: 20.0022 high limit: 20.0266
- %PASS CALCUB test of current measure at -1.577211382mA with 20ohm source impedance Measured: -1.5722 low limit: -1.6992 high limit: -1.4551

- %PASS CALCUB test of current measure at 1.589074041mA with 20ohm source impedance Measured: 1.5715 low limit: 1.4670 high limit: 1.7111
- %PASS CALCUB test of current measure at -3.167290166mA with 20ohm source impedance Measured: -3.1632 low limit: -3.2893 high limit: -3.0452
- %PASS CALCUB test of current measure at 3.168454919mA with 20ohm source impedance Measured: 3.1516 low limit: 3.0463 high limit: 3.2905
- %PASS CALCUB test of current measure at -8.772114625mA with 20ohm source impedance Measured: -8.7418 low limit: -8.8941 high limit: -8.6500
- %PASS CALCUB test of current measure at 8.803616398mA with 20ohm source impedance Measured: 8.7926 low limit: 8.6815 high limit: 8.9256
- %PASS CALCUB test of current measure at -17.5598659mA with 20ohm source impedance Measured: -17.5372 low limit: -17.6819 high limit: -17.4377
- %PASS CALCUB test of current measure at 17.592425mA with 20ohm source impedance Measured: 17.5826 low limit: 17.4703 high limit: 17.7144
- %PASS CALCUB test of current measure at -43.92160548mA with 20ohm source impedance Measured: -43.9149 low limit: -44.0436 high limit: -43.7995
- %PASS CALCUB test of current measure at 43.99498033mA with 20ohm source impedance Measured: 43.9689 low limit: 43.8729 high limit: 44.1170
- %PASS CALCUB test of current measure at -91.92457881mA with 20ohm source impedance Measured: -91.9196 low limit: -92.0466 high limit: -91.8025
- %PASS CALCUB test of current measure at 92.07339962mA with 20ohm source impedance Measured: 92.0644 low limit: 91.9513 high limit: 92.1954
- %PASS CALCUB test of current measure at -10.06659892mA with 20hm source impedance Measured: -10.0651 low limit: -11.2873 high limit: -8.8458
- %PASS CALCUB test of current measure at 10.2219772mA with 20hm source impedance Measured: 10.0670 low limit: 9.0012 high limit: 11.4427

- %PASS CALCUB test of current measure at -20.22160364mA with 20hm source impedance Measured: -20.2209 low limit: -21.4423 high limit: -19.0008
- %PASS CALCUB test of current measure at 20.36368837mA with 20hm source impedance Measured: 20.2307 low limit: 19.1429 high limit: 21.5844
- %PASS CALCUB test of current measure at -50.45452424mA with 20hm source impedance Measured: -50.2591 low limit: -51.6752 high limit: -49.2337
- %PASS CALCUB test of current measure at 51.06610918mA with 20hm source impedance Measured: 50.8633 low limit: 49.8453 high limit: 52.2868
- %PASS CALCUB test of current measure at -101.228526mA with 20hm source impedance Measured: -101.0869 low limit: -102.4492 high limit: -100.0077
- %PASS CALCUB test of current measure at 102.2624516mA with 20hm source impedance Measured: 102.1968 low limit: 101.0417 high limit: 103.4831
- %PASS CALCUB test of current measure at -252.0886104mA with 20hm source impedance Measured: -252.1012 low limit: -253.3093 high limit: -250.8678
- %PASS CALCUB test of current measure at 240.4739705mA with 20hm source impedance Measured: 240.7207 low limit: 239.2532 high limit: 241.6947
- Performing 100Mhz clock accuracy verification...
- %PASS CALCUB Clock 100 External Performance Verification test.

  Measured: 100000399.3 low limit: 99996000 high limit: 100004000
- %JOB END \*\*\*\*PASSED\*\*\*\* CUB External Verification of slot 18 (C017D6D) at 5:10:04 PM