

Trust Technology Corporation

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TTC ID

Maker TERADYNE

Model J750

Description Tester

Vintage

QTY 1

[NOTES]

There are more tools listed on the following URL.

We hope you will also view it.

(Used Tools Information)

http://www.trust-t.com/ttcen/equipment/

CurrentConfig.

#slot[.subslot]	Type	idprom (type serial rev company)
-1	sli	239-624-00 8014c43 9846-A 5445
0	channel	239-026-03 8035120 0951-B 5445
1	channel	239-026-03 8031672 1742-B 5445
2	channel	$239 \hbox{-} 026 \hbox{-} 31 \ c00 \hbox{e} 109 \ 0737 \hbox{-} 5 \ 5445$
3	channel	$239 \hbox{-} 026 \hbox{-} 31 \ c0157 \hbox{f4} \ 0746 \hbox{-} 5 \ 5445$
4	channel	$239 \hbox{-} 026 \hbox{-} 31 \ c014821 \ 0737 \hbox{-} 5 \ 5445$
5	channel	239-026-03 5007f60 0951-B 5445
6	channel	239-026-31 c0157f1 0746-5 5445
7	channel	$239 \hbox{-} 026 \hbox{-} 31 \ c015 f3c \ 0746 \hbox{-} 5 \ 5445$
17	cto	$239 \text{-} 029 \text{-} 00 \ 500933 \text{b} \ 0710 \text{-} \text{B} \ 5445$
18	cub	239-020-06 26da50 0621-D 5445
21	dps	239-016-06 c 01524a 0702-F 5445
22	dps	239-016-06 c 01521b 0702-F 5445
23	dps	239-016-06 c01523f 0702-F 5445
24	dps	239-016-05 c00577d 0645-E 5445

Calibration_Performance_PASS

%JOB_START - Beginning CUB Calibration test on slot 18 at 3:11:58 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A

- Starting Channel Drive Levels: Vih Vil
- Starting Channel Compare Levels: Voh Vol

%JOB END - ****PASSED**** CUB Calibration of slot 18 (26DA50) at 3:11:58 PM

%JOB_START - Beginning Channel_Board_DIB Calibration test on slot 0 at $3\hbox{:}12\hbox{:}04$ PM on 2/29/2020

- Systemwide functionality and continuity to slot 0
- Starting dib_test
- Temperature at PE Ch00 is 56 deg C
- Temperature at PE Ch60 is 40 deg C
- Temperature at Incoming Air is 26 deg C
- Temperature at TG Ch00 is 46 deg C
- Starting BPMU Force Voltage
- Starting Bpmu Clamp Voltage
- Starting Bpmu Measure Voltage
- Starting Bpmu Measure Current
- Starting Bpmu Force current
- Starting Bpmu Limit current
- Starting Channel Drive Levels: Vih Vil
- Starting Channel Compare Levels: Voh Vol
- Starting Channel Clamps: Vch Vcl
- Starting Channel Loads: Ioh Iol
- Starting Channel Load Threshold: Vt
- Starting HV Channel Drive Levels
- Starting HV Channel Drive Current
- Ppmu Mi Warmup 3:14:25 PM
- Ppmu Mi Warmup 3:14:25 PM

- Starting Ppmu Force Voltage
- Starting Ppmu Measure Voltage
- Starting Ppmu Force Current Ppmu Force 200ua
- Starting Ppmu Force Current Ppmu Force 2ma
- Starting Ppmu Measure 2ma
- Continuing Ppmu Measure Current chan 15
- Continuing Ppmu Measure Current chan 31
- Continuing Ppmu Measure Current chan 47
- Continuing Ppmu Measure Current chan 63
- Starting Ppmu Measure 200ua
- Continuing Ppmu Measure Current chan 15
- Continuing Ppmu Measure Current chan 31
- Continuing Ppmu Measure Current chan 47
- Continuing Ppmu Measure Current chan 63
- Starting Ppmu Measure Int 20ua
- Continuing Ppmu Measure Current chan 15
- Continuing Ppmu Measure Current chan 31
- Continuing Ppmu Measure Current chan 47
- Continuing Ppmu Measure Current chan 63
- Starting Ppmu Measure Int 2ua
- Continuing Ppmu Measure Current chan 15
- Continuing Ppmu Measure Current chan 31
- Continuing Ppmu Measure Current chan 47
- Continuing Ppmu Measure Current chan 63
- Starting Ppmu Measure Int 200na
- Continuing Ppmu Measure Current chan 15
- Continuing Ppmu Measure Current chan 31
- Continuing Ppmu Measure Current chan 47
- Continuing Ppmu Measure Current chan 63
- Finished Channel Calibration

%JOB_END - ****PASSED**** Channel_Board_DIB Calibration of slot 0 (8035120) at 3:17:32 PM

%JOB_START - Beginning Channel_Board_DIB Calibration test on slot 1 at $3\.:17\.:37$ PM on 2/29/2020

- Systemwide functionality and continuity to slot 1
- Starting dib_test
- Temperature at PE Ch00 is $57 \deg C$
- Temperature at PE Ch60 is 47 deg C
- Temperature at Incoming Air is 25 deg C
- Temperature at TG Ch00 is 44 deg C
- Starting BPMU Force Voltage
- Starting Bpmu Clamp Voltage
- Starting Bpmu Measure Voltage
- Starting Bpmu Measure Current
- Starting Bpmu Force current
- Starting Bpmu Limit current
- Starting Channel Drive Levels: Vih Vil
- Starting Channel Compare Levels: Voh Vol
- Starting Channel Clamps: Vch Vcl
- Starting Channel Loads: Ioh Iol
- Starting Channel Load Threshold: Vt
- Starting HV Channel Drive Levels
- Starting HV Channel Drive Current
- Ppmu Mi Warmup 3:20:01 PM
- Ppmu Mi Warmup 3:20:01 PM
- Starting Ppmu Force Voltage
- Starting Ppmu Measure Voltage
- Starting Ppmu Force Current Ppmu Force 200ua
- Starting Ppmu Force Current Ppmu Force 2ma
- Starting Ppmu Measure 2ma
- Continuing Ppmu Measure Current chan 79
- Continuing Ppmu Measure Current chan 95
- Continuing Ppmu Measure Current chan 111
- Continuing Ppmu Measure Current chan 127
- Starting Ppmu Measure 200ua
- Continuing Ppmu Measure Current chan 79
- Continuing Ppmu Measure Current chan 95
- Continuing Ppmu Measure Current chan 111
- Continuing Ppmu Measure Current chan 127
- Starting Ppmu Measure Int 20ua
- Continuing Ppmu Measure Current chan 79

- Continuing Ppmu Measure Current chan 95
- Continuing Ppmu Measure Current chan 111
- Continuing Ppmu Measure Current chan 127
- Starting Ppmu Measure Int 2ua
- Continuing Ppmu Measure Current chan 79
- Continuing Ppmu Measure Current chan 95
- Continuing Ppmu Measure Current chan 111
- Continuing Ppmu Measure Current chan 127
- Starting Ppmu Measure Int 200na
- Continuing Ppmu Measure Current chan 79
- Continuing Ppmu Measure Current chan 95
- Continuing Ppmu Measure Current chan 111
- Continuing Ppmu Measure Current chan 127
- Finished Channel Calibration

%JOB_END - ****PASSED**** Channel_Board_DIB Calibration of slot 1 (8031672) at 3:23:07 PM

%JOB_START - Beginning Channel_Board_DIB Calibration test on slot 2 at 3:23:13 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A

- Systemwide functionality and continuity to slot 2
- Starting dib_test
- Temperature at PE Ch00 is 64 deg C
- Temperature at PE Ch60 is 43 deg C
- Temperature at Incoming Air is 27 deg C
- Temperature at TG Ch00 is 48 deg C
- Starting BPMU Force Voltage
- Starting Bpmu Clamp Voltage
- Starting Bpmu Measure Voltage
- Starting Bpmu Measure Current
- Starting Bpmu Force current
- Starting Bpmu Limit current
- Starting Channel Drive Levels: Vih Vil
- Starting Channel Compare Levels: Voh Vol

- Starting Channel Clamps: Vch Vcl
- Starting Channel Loads: Ioh Iol
- Starting Channel Load Threshold: Vt
- Starting HV Channel Drive Levels
- Starting HV Channel Drive Current
- Ppmu Mi Warmup 3:25:35 PM
- Ppmu Mi Warmup 3:25:35 PM
- Starting Ppmu Force Voltage
- Starting Ppmu Measure Voltage
- Starting Ppmu Force Current Ppmu Force 200ua
- Starting Ppmu Force Current Ppmu Force 2ma
- Starting Ppmu Measure 2ma
- Continuing Ppmu Measure Current chan 143
- Continuing Ppmu Measure Current chan 159
- Continuing Ppmu Measure Current chan 175
- Continuing Ppmu Measure Current chan 191
- Starting Ppmu Measure 200ua
- Continuing Ppmu Measure Current chan 143
- Continuing Ppmu Measure Current chan 159
- Continuing Ppmu Measure Current chan 175
- Continuing Ppmu Measure Current chan 191
- Starting Ppmu Measure Int 20ua
- Continuing Ppmu Measure Current chan 143
- Continuing Ppmu Measure Current chan 159
- Continuing Ppmu Measure Current chan 175
- Continuing Ppmu Measure Current chan 191
- Starting Ppmu Measure Int 2ua
- Continuing Ppmu Measure Current chan 143
- Continuing Ppmu Measure Current chan 159
- Continuing Ppmu Measure Current chan 175
- Continuing Ppmu Measure Current chan 191
- Starting Ppmu Measure Int 200na
- Continuing Ppmu Measure Current chan 143
- Continuing Ppmu Measure Current chan 159
- Continuing Ppmu Measure Current chan 175
- Continuing Ppmu Measure Current chan 191
- Finished Channel Calibration

%JOB_END - ****PASSED**** Channel_Board_DIB Calibration of slot 2 (C00E109) at 3:28:41 PM

%JOB_START - Beginning Channel_Board_DIB Calibration test on slot 3 at 3:28:47 PM on 2/29/2020 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A

- Systemwide functionality and continuity to slot 3
- Starting dib_test
- Temperature at PE Ch00 is 63 deg C
- Temperature at PE Ch60 is 42 deg C
- Temperature at Incoming Air is 26 deg C
- Temperature at TG Ch00 is $47 \deg C$
- Starting BPMU Force Voltage
- Starting Bpmu Clamp Voltage
- Starting Bpmu Measure Voltage
- Starting Bpmu Measure Current
- Starting Bpmu Force current
- Starting Bpmu Limit current
- Starting Channel Drive Levels: Vih Vil
- Starting Channel Compare Levels: Voh Vol
- Starting Channel Clamps: Vch Vcl
- Starting Channel Loads: Ioh Iol
- Starting Channel Load Threshold: Vt
- Starting HV Channel Drive Levels
- Starting HV Channel Drive Current
- Ppmu Mi Warmup 3:31:08 PM
- Ppmu Mi Warmup 3:31:08 PM
- Starting Ppmu Force Voltage
- Starting Ppmu Measure Voltage
- Starting Ppmu Force Current Ppmu Force 200ua
- Starting Ppmu Force Current Ppmu Force 2ma
- Starting Ppmu Measure 2ma
- Continuing Ppmu Measure Current chan 207
- Continuing Ppmu Measure Current chan 223
- Continuing Ppmu Measure Current chan 239
- Continuing Ppmu Measure Current chan 255

- Starting Ppmu Measure 200ua
- Continuing Ppmu Measure Current chan 207
- Continuing Ppmu Measure Current chan 223
- Continuing Ppmu Measure Current chan 239
- Continuing Ppmu Measure Current chan 255
- Starting Ppmu Measure Int 20ua
- Continuing Ppmu Measure Current chan 207
- Continuing Ppmu Measure Current chan 223
- Continuing Ppmu Measure Current chan 239
- Continuing Ppmu Measure Current chan 255
- Starting Ppmu Measure Int 2ua
- Continuing Ppmu Measure Current chan 207
- Continuing Ppmu Measure Current chan 223
- Continuing Ppmu Measure Current chan 239
- Continuing Ppmu Measure Current chan 255
- Starting Ppmu Measure Int 200na
- Continuing Ppmu Measure Current chan 207
- Continuing Ppmu Measure Current chan 223
- Continuing Ppmu Measure Current chan 239
- Continuing Ppmu Measure Current chan 255
- Finished Channel Calibration

 $\% JOB_END$ - ****PASSED**** Channel_Board_DIB Calibration of slot 3 (C0157F4) at 3:34:15 PM

%JOB_START - Beginning Channel_Board_DIB Calibration test on slot 4 at 3:34:20 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A

- Systemwide functionality and continuity to slot 4
- Starting dib_test
- Temperature at PE Ch00 is 65 deg C
- Temperature at PE Ch60 is 43 deg C
- Temperature at Incoming Air is 27 deg C
- Temperature at TG Ch00 is 50 deg C
- Starting BPMU Force Voltage
- Starting Bpmu Clamp Voltage

- Starting Bpmu Measure Voltage
- Starting Bpmu Measure Current
- Starting Bpmu Force current
- Starting Bpmu Limit current
- Starting Channel Drive Levels: Vih Vil
- Starting Channel Compare Levels: Voh Vol
- Starting Channel Clamps: Vch Vcl
- Starting Channel Loads: Ioh Iol
- Starting Channel Load Threshold: Vt
- Starting HV Channel Drive Levels
- Starting HV Channel Drive Current
- Ppmu Mi Warmup 3:36:44 PM
- Ppmu Mi Warmup 3:36:44 PM
- Starting Ppmu Force Voltage
- Starting Ppmu Measure Voltage
- Starting Ppmu Force Current Ppmu Force 200ua
- Starting Ppmu Force Current Ppmu Force 2ma
- Starting Ppmu Measure 2ma
- Continuing Ppmu Measure Current chan 271
- Continuing Ppmu Measure Current chan 287
- Continuing Ppmu Measure Current chan 303
- Continuing Ppmu Measure Current chan 319
- Starting Ppmu Measure 200ua
- Continuing Ppmu Measure Current chan 271
- Continuing Ppmu Measure Current chan 287
- Continuing Ppmu Measure Current chan 303
- Continuing Ppmu Measure Current chan 319
- Starting Ppmu Measure Int 20ua
- Continuing Ppmu Measure Current chan 271
- Continuing Ppmu Measure Current chan 287
- Continuing Ppmu Measure Current chan 303
- Continuing Ppmu Measure Current chan 319
- Starting Ppmu Measure Int 2ua
- Continuing Ppmu Measure Current chan 271
- Continuing Ppmu Measure Current chan 287
- Continuing Ppmu Measure Current chan 303
- Continuing Ppmu Measure Current chan 319
- Starting Ppmu Measure Int 200na

- Continuing Ppmu Measure Current chan 271
- Continuing Ppmu Measure Current chan 287
- Continuing Ppmu Measure Current chan 303
- Continuing Ppmu Measure Current chan 319
- Finished Channel Calibration

%JOB_END - ****PASSED**** Channel_Board_DIB Calibration of slot 4 (C014821) at 3:39:50 PM

%JOB_START - Beginning Channel_Board_DIB Calibration test on slot 5 at 3:39:56 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A

- Systemwide functionality and continuity to slot 5
- Starting dib_test
- Temperature at PE Ch00 is 63 deg C
- Temperature at PE Ch60 is 44 deg C
- Temperature at Incoming Air is 26 deg C
- Temperature at TG Ch00 is 47 deg C
- Starting BPMU Force Voltage
- Starting Bpmu Clamp Voltage
- Starting Bpmu Measure Voltage
- Starting Bpmu Measure Current
- Starting Bpmu Force current
- Starting Bpmu Limit current
- Starting Channel Drive Levels: Vih Vil
- Starting Channel Compare Levels: Voh Vol
- Starting Channel Clamps: Vch Vcl
- Starting Channel Loads: Ioh Iol
- Starting Channel Load Threshold: Vt
- Starting HV Channel Drive Levels
- Starting HV Channel Drive Current
- Ppmu Mi Warmup 3:42:16 PM
- Ppmu Mi Warmup 3:42:16 PM
- Starting Ppmu Force Voltage
- Starting Ppmu Measure Voltage
- Starting Ppmu Force Current Ppmu Force 200ua

- Starting Ppmu Force Current Ppmu Force 2ma
- Starting Ppmu Measure 2ma
- Continuing Ppmu Measure Current chan 335
- Continuing Ppmu Measure Current chan 351
- Continuing Ppmu Measure Current chan 367
- Continuing Ppmu Measure Current chan 383
- Starting Ppmu Measure 200ua
- Continuing Ppmu Measure Current chan 335
- Continuing Ppmu Measure Current chan 351
- Continuing Ppmu Measure Current chan 367
- Continuing Ppmu Measure Current chan 383
- Starting Ppmu Measure Int 20ua
- Continuing Ppmu Measure Current chan 335
- Continuing Ppmu Measure Current chan 351
- Continuing Ppmu Measure Current chan 367
- Continuing Ppmu Measure Current chan 383
- Starting Ppmu Measure Int 2ua
- Continuing Ppmu Measure Current chan 335
- Continuing Ppmu Measure Current chan 351
- Continuing Ppmu Measure Current chan 367
- Continuing Ppmu Measure Current chan 383
- Starting Ppmu Measure Int 200na
- Continuing Ppmu Measure Current chan 335
- Continuing Ppmu Measure Current chan 351
- Continuing Ppmu Measure Current chan 367
- Continuing Ppmu Measure Current chan 383
- Finished Channel Calibration

%JOB_END - ****PASSED**** Channel_Board_DIB Calibration of slot 5 (5007F60) at 3:45:23 PM

%JOB_START - Beginning Channel_Board_DIB Calibration test on slot 6 at 3:45:28 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A

- Systemwide functionality and continuity to slot 6
- Starting dib_test

- Temperature at PE Ch00 is 61 deg C
- Temperature at PE Ch60 is 45 deg C
- Temperature at Incoming Air is $26 \deg C$
- Temperature at TG Ch00 is $46 \deg C$
- Starting BPMU Force Voltage
- Starting Bpmu Clamp Voltage
- Starting Bpmu Measure Voltage
- Starting Bpmu Measure Current
- Starting Bpmu Force current
- Starting Bpmu Limit current
- Starting Channel Drive Levels: Vih Vil
- Starting Channel Compare Levels: Voh Vol
- Starting Channel Clamps: Vch Vcl
- Starting Channel Loads: Ioh Iol
- Starting Channel Load Threshold: Vt
- Starting HV Channel Drive Levels
- Starting HV Channel Drive Current
- Ppmu Mi Warmup 3:47:48 PM
- Ppmu Mi Warmup 3:47:48 PM
- Starting Ppmu Force Voltage
- Starting Ppmu Measure Voltage
- Starting Ppmu Force Current Ppmu Force 200ua
- Starting Ppmu Force Current Ppmu Force 2ma
- Starting Ppmu Measure 2ma
- Continuing Ppmu Measure Current chan 399
- Continuing Ppmu Measure Current chan 415
- Continuing Ppmu Measure Current chan 431
- Continuing Ppmu Measure Current chan 447
- Starting Ppmu Measure 200ua
- Continuing Ppmu Measure Current chan 399
- Continuing Ppmu Measure Current chan 415
- Continuing Ppmu Measure Current chan 431
- Continuing Ppmu Measure Current chan 447
- Starting Ppmu Measure Int 20ua
- Continuing Ppmu Measure Current chan 399
- Continuing Ppmu Measure Current chan 415
- Continuing Ppmu Measure Current chan 431
- Continuing Ppmu Measure Current chan 447

- Starting Ppmu Measure Int 2ua
- Continuing Ppmu Measure Current chan 399
- Continuing Ppmu Measure Current chan 415
- Continuing Ppmu Measure Current chan 431
- Continuing Ppmu Measure Current chan 447
- Starting Ppmu Measure Int 200na
- Continuing Ppmu Measure Current chan 399
- Continuing Ppmu Measure Current chan 415
- Continuing Ppmu Measure Current chan 431
- Continuing Ppmu Measure Current chan 447
- Finished Channel Calibration

%JOB_END - ****PASSED**** Channel_Board_DIB Calibration of slot 6 (C0157F1) at 3:50:55 PM

 $\mbox{\%JOB_START}$ - Beginning Channel_Board_DIB Calibration test on slot 7 at 3.51.01 PM on 2/29/2020

- Systemwide functionality and continuity to slot 7
- Starting dib_test
- Temperature at PE Ch00 is 60 deg C
- Temperature at PE Ch60 is 47 deg C
- Temperature at Incoming Air is 27 deg C
- Temperature at TG Ch00 is 56 deg C
- Starting BPMU Force Voltage
- Starting Bpmu Clamp Voltage
- Starting Bpmu Measure Voltage
- Starting Bpmu Measure Current
- Starting Bpmu Force current
- Starting Bpmu Limit current
- Starting Channel Drive Levels: Vih Vil
- Starting Channel Compare Levels: Voh Vol
- Starting Channel Clamps: Vch Vcl
- Starting Channel Loads: Ioh Iol
- Starting Channel Load Threshold: Vt

- Starting HV Channel Drive Levels
- Starting HV Channel Drive Current
- Ppmu Mi Warmup 3:53:20 PM
- Ppmu Mi Warmup 3:53:20 PM
- Starting Ppmu Force Voltage
- Starting Ppmu Measure Voltage
- Starting Ppmu Force Current Ppmu Force 200ua
- Starting Ppmu Force Current Ppmu Force 2ma
- Starting Ppmu Measure 2ma
- Continuing Ppmu Measure Current chan 463
- Continuing Ppmu Measure Current chan 479
- Continuing Ppmu Measure Current chan 495
- Continuing Ppmu Measure Current chan 511
- Starting Ppmu Measure 200ua
- Continuing Ppmu Measure Current chan 463
- Continuing Ppmu Measure Current chan 479
- Continuing Ppmu Measure Current chan 495
- Continuing Ppmu Measure Current chan 511
- Starting Ppmu Measure Int 20ua
- Continuing Ppmu Measure Current chan 463
- Continuing Ppmu Measure Current chan 479
- Continuing Ppmu Measure Current chan 495
- Continuing Ppmu Measure Current chan 511
- Starting Ppmu Measure Int 2ua
- Continuing Ppmu Measure Current chan 463
- Continuing Ppmu Measure Current chan 479
- Continuing Ppmu Measure Current chan 495
- Continuing Ppmu Measure Current chan 511
- Starting Ppmu Measure Int 200na
- Continuing Ppmu Measure Current chan 463
- Continuing Ppmu Measure Current chan 479
- Continuing Ppmu Measure Current chan 495
- Continuing Ppmu Measure Current chan 511
- Finished Channel Calibration

%JOB_START - Beginning DPS_DIB Calibration test on slot 21 at 3:56:32 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A

- Starting DPS Calibration on slot 21
- Calibrating DPS Voltage on slot 21
- Calibrating DPS Current Limit on slot 21
- Calibrating DPS Current Measure (50uA Range) on slot 21
- Calibrating DPS Current Measure (500uA Range) on slot 21
- Calibrating DPS Current Measure (10mA Range) on slot 21
- Calibrating DPS Current Measure (100mA Range) on slot 21
- Calibrating DPS Current Measure (1A Range) on slot 21
- Finished DPS Calibration on slot 21
- %JOB_END ****PASSED**** DPS_DIB Calibration of slot 21 (C01524A) at $3\!:\!56\!:\!47$ PM
- % JOB_START - Beginning DPS_DIB Calibration test on slot 22 at 3:56:52 PM on 2/29/2020

- Starting DPS Calibration on slot 22
- Calibrating DPS Voltage on slot 22
- Calibrating DPS Current Limit on slot 22
- Calibrating DPS Current Measure (50uA Range) on slot 22
- Calibrating DPS Current Measure (500uA Range) on slot 22
- Calibrating DPS Current Measure (10mA Range) on slot 22
- Calibrating DPS Current Measure (100mA Range) on slot 22
- Calibrating DPS Current Measure (1A Range) on slot 22
- Finished DPS Calibration on slot 22

- %JOB_END ****PASSED**** DPS_DIB Calibration of slot 22 (C01521B) at 3:57:07 PM
- %JOB_START Beginning DPS_DIB Calibration test on slot 23 at 3:57:13 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A

- Starting DPS Calibration on slot 23
- Calibrating DPS Voltage on slot 23
- Calibrating DPS Current Limit on slot 23
- Calibrating DPS Current Measure (50uA Range) on slot 23
- Calibrating DPS Current Measure (500uA Range) on slot 23
- Calibrating DPS Current Measure (10mA Range) on slot 23
- Calibrating DPS Current Measure (100mA Range) on slot 23
- Calibrating DPS Current Measure (1A Range) on slot 23
- Finished DPS Calibration on slot 23
- %JOB_END ****PASSED**** DPS_DIB Calibration of slot 23 (C01523F) at $3\!:\!57\!:\!28$ PM
- % JOB_START - Beginning DPS_DIB Calibration test on slot 24 at 3:57:33 PM on 2/29/2020

- Starting DPS Calibration on slot 24
- Calibrating DPS Voltage on slot 24
- Calibrating DPS Current Limit on slot 24
- Calibrating DPS Current Measure (50uA Range) on slot 24
- Calibrating DPS Current Measure (500uA Range) on slot 24
- Calibrating DPS Current Measure (10mA Range) on slot 24
- Calibrating DPS Current Measure (100mA Range) on slot 24

- Calibrating DPS Current Measure (1A Range) on slot 24
- Finished DPS Calibration on slot 24
- %JOB_END ****PASSED**** DPS_DIB Calibration of slot 24 (C00577D) at $3:57:48~\mathrm{PM}$
- % JOB_START - Beginning CTO_DIB Calibration test on slot 17 at 3:57:53 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT $\,$ IG-XL Version: 3.40.13 $\,$ DIB $\#\,30562A2$ Rev 1137A

- Performing CTO Voltage Reference Calibration
- Performing CTO PPMU Force Voltage Calibration
- Performing CTO PPMU Measure Voltage Calibration
- Performing CTO PPMU Measure and Force Current Calibration on channel 0
- Performing CTO PPMU Measure and Force Current Calibration on channel 1
- Performing CTO PPMU Measure and Force Current Calibration on channel 2
- Performing CTO PPMU Measure and Force Current Calibration on channel 3
- Performing CTO PPMU Measure and Force Current Calibration on channel 4
- Performing CTO PPMU Measure and Force Current Calibration on channel 5
- Performing CTO PPMU Measure and Force Current Calibration on channel 6
- Performing CTO PPMU Measure and Force Current Calibration on channel $7\,$
- %JOB_END ****PASSED**** CTO_DIB Calibration of slot 17 (500933B) at 3:59:24 PM
- $\rm \%JOB_START$ Beginning AC Calibration at 3:59:30 PM on 2/29/2020 in High Accuracy Mode

- Beginning Digital Channel Timing Calibration
- Building List of Digital Channels
- Checking CalDIB AC Continuity for all Digital Channels
- Calibrating Super Linear Interpolator (SLI)
- Measuring Cub Round Trip Delay

- Measuring RF Matrix Delay
- Calibrating Drive Edges
- Calibrating Receive Edges
- Calibrating Receive Window
- Calibrating Mux Drive
- Calibrating Receive Mux
- Completed Digital Channel Timing Calibration

%JOB_END - ****PASSED**** AC Calibration at 4:34:56 PM

%JOB_START - Beginning Channel_Board Performance Verification test on slot 0 at 4:35:02 PM on 2/29/2020 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A

- Starting dib_test
- Temperature at PE Ch00 is 56 deg C
- Temperature at PE Ch60 is $40 \deg C$
- Temperature at Incoming Air is 26 deg C
- Temperature at TG Ch00 is 46 deg C
- Performing VIH/VIL level tests... at DGS=0mV
- Performing IOH/IOL level tests...
- Performing VT level tests...
- Starting Channel Comparator Performance Verification on slot 0
- Finished Channel Comparator Performance Verification on slot 0
- Performing Clamp level tests...
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests... DGS=0mV
- Starting BPMU Performance Verification on slot 0, DGS=-224.535 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy

- Starting BPMU Performance Verification on slot 0, DGS= 1.593 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 0, DGS= 181.085 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Finished BPMU Performance Verification on slot 0
- Starting High Voltage DC Performance Verification on slot 0
- Finished High Voltage DC Performance Verification on slot 0
- %JOB_END ****PASSED**** Channel_Board Performance Verification of slot 0 (8035120) at 4:43:25 PM
- %JOB_START Beginning Channel_Board Performance Verification test on slot 1 at 4:43:31 PM on 2/29/2020 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Starting dib_test
 - Temperature at PE Ch00 is 57 deg C
 - Temperature at PE Ch60 is 47 deg C
 - Temperature at Incoming Air is 25 deg C
 - Temperature at TG Ch00 is 44 deg C
 - Performing VIH/VIL level tests... at DGS=0mV
 - Performing IOH/IOL level tests...
 - Performing VT level tests...
 - Starting Channel Comparator Performance Verification on slot 1
 - Finished Channel Comparator Performance Verification on slot 1

- Performing Clamp level tests...
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests... DGS=0mV
- Starting BPMU Performance Verification on slot 1, DGS=-219.755 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 1, DGS= 6.354 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 1, DGS= 185.493 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Finished BPMU Performance Verification on slot 1
- Starting High Voltage DC Performance Verification on slot 1
- Finished High Voltage DC Performance Verification on slot 1
- %JOB_END ****PASSED**** Channel_Board Performance Verification of slot 1 (8031672) at 4:51:56 PM
- $\rm \%JOB_START$ Beginning Channel_Board Performance Verification test on slot 2 at 4:52:01 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB

30562A2 Rev 1137A

- Starting dib_test
- Temperature at PE Ch00 is 57 deg C
- Temperature at PE Ch60 is 40 deg C
- Temperature at Incoming Air is 27 deg C
- Temperature at TG Ch00 is 47 deg C
- Performing VIH/VIL level tests... at DGS=0mV
- Performing IOH/IOL level tests...
- Performing VT level tests...
- Starting Channel Comparator Performance Verification on slot 2
- Finished Channel Comparator Performance Verification on slot 2
- Performing Clamp level tests...
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests... DGS=0mV
- Starting BPMU Performance Verification on slot 2, DGS=-223.362 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 2, DGS= 3.138 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 2, DGS= 182.815 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy

- Finished BPMU Performance Verification on slot 2
- Starting High Voltage DC Performance Verification on slot 2
- Finished High Voltage DC Performance Verification on slot 2

%JOB_END - ****PASSED**** Channel_Board Performance Verification of slot 2 (C00E109) at 5:00:27 PM

%JOB_START - Beginning Channel_Board Performance Verification test on slot 3 at 5:00:32 PM on 2/29/2020 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A

- Starting dib_test
- Temperature at PE Ch00 is $56 \deg C$
- Temperature at PE Ch60 is 39 deg C
- Temperature at Incoming Air is 26 deg C
- Temperature at TG Ch00 is $46 \deg C$
- Performing VIH/VIL level tests... at DGS=0mV
- Performing IOH/IOL level tests...
- Performing VT level tests...
- Starting Channel Comparator Performance Verification on slot 3
- Finished Channel Comparator Performance Verification on slot 3
- Performing Clamp level tests...
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests... DGS=0mV
- Starting BPMU Performance Verification on slot 3, DGS=-230.528 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 3, DGS=- 4.457 mV
- Verifying BPMU Forced Voltage Accuracy

- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 3, DGS= 175.406 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Finished BPMU Performance Verification on slot 3
- Starting High Voltage DC Performance Verification on slot 3
- Finished High Voltage DC Performance Verification on slot 3
- %JOB_END ****PASSED**** Channel_Board Performance Verification of slot 3 (C0157F4) at 5:08:57 PM
- %JOB_START Beginning Channel_Board Performance Verification test on slot 4 at 5:09:02 PM on 2/29/2020 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Starting dib_test
 - Temperature at PE Ch00 is $57 \deg C$
 - Temperature at PE Ch60 is 40 deg C
 - Temperature at Incoming Air is 27 deg C
 - Temperature at TG Ch00 is 49 deg C
 - Performing VIH/VIL level tests... at DGS=0mV
 - Performing IOH/IOL level tests...
 - Performing VT level tests...
 - Starting Channel Comparator Performance Verification on slot 4
 - Finished Channel Comparator Performance Verification on slot 4
 - Performing Clamp level tests...
 - Performing PPMU force voltage tests...

- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests... DGS=0mV
- Starting BPMU Performance Verification on slot 4, DGS=-225.884 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 4, DGS= 371.456 uV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 4, DGS= 179.902 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Finished BPMU Performance Verification on slot 4
- Starting High Voltage DC Performance Verification on slot 4
- Finished High Voltage DC Performance Verification on slot 4
- %JOB_END ****PASSED**** Channel_Board Performance Verification of slot 4 (C014821) at 5:17:26 PM
- %JOB_START Beginning Channel_Board Performance Verification test on slot 5 at 5:17:32 PM on 2/29/2020 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A

- Starting dib_test
- Temperature at PE Ch00 is 56 deg C
- Temperature at PE Ch60 is 40 deg C
- Temperature at Incoming Air is 26 deg C
- Temperature at TG Ch00 is $46 \deg C$
- Performing VIH/VIL level tests... at DGS=0mV
- Performing IOH/IOL level tests...
- Performing VT level tests...
- Starting Channel Comparator Performance Verification on slot 5
- Finished Channel Comparator Performance Verification on slot 5
- Performing Clamp level tests...
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests... DGS=0mV
- Starting BPMU Performance Verification on slot 5, DGS=-220.498 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 5, DGS= 5.816 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 5, DGS= 184.975 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Finished BPMU Performance Verification on slot 5

- Starting High Voltage DC Performance Verification on slot 5
- Finished High Voltage DC Performance Verification on slot 5

%JOB_END - ****PASSED**** Channel_Board Performance Verification of slot 5 (5007F60) at 5:25:55 PM

%JOB_START - Beginning Channel_Board Performance Verification test on slot 6 at 5:26:00 PM on 2/29/2020 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A

- Starting dib_test
- Temperature at PE Ch00 is $54 \deg C$
- Temperature at PE Ch60 is 41 deg C
- Temperature at Incoming Air is 26 deg C
- Temperature at TG Ch00 is 45 deg C
- Performing VIH/VIL level tests... at DGS=0mV
- Performing IOH/IOL level tests...
- Performing VT level tests...
- Starting Channel Comparator Performance Verification on slot 6
- Finished Channel Comparator Performance Verification on slot 6
- Performing Clamp level tests...
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests... DGS=0mV
- Starting BPMU Performance Verification on slot 6, DGS=-224.066 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 6, DGS= 2.141 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy

- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 6, DGS= 181.671 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Finished BPMU Performance Verification on slot 6
- Starting High Voltage DC Performance Verification on slot 6
- Finished High Voltage DC Performance Verification on slot 6
- %JOB_END ****PASSED**** Channel_Board Performance Verification of slot 6 (C0157F1) at 5:34:24 PM
- %JOB_START Beginning Channel_Board Performance Verification test on slot 7 at 5:34:29 PM on 2/29/2020 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Starting dib_test
 - Temperature at PE Ch00 is $53 \deg C$
 - Temperature at PE Ch60 is 43 deg C
 - Temperature at Incoming Air is 27 deg C
 - Temperature at TG Ch00 is 55 deg C
 - Performing VIH/VIL level tests... at DGS=0mV
 - Performing IOH/IOL level tests...
 - Performing VT level tests...
 - Starting Channel Comparator Performance Verification on slot 7
 - Finished Channel Comparator Performance Verification on slot 7
 - Performing Clamp level tests...
 - Performing PPMU force voltage tests...
 - Performing PPMU measure voltage tests...
 - Performing PPMU force current tests...

- Performing PPMU measure current tests... DGS=0mV
- Starting BPMU Performance Verification on slot 7, DGS=-230.782 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 7, DGS=- 4.487 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 7, DGS= 175.181 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Finished BPMU Performance Verification on slot 7
- Starting High Voltage DC Performance Verification on slot 7
- Finished High Voltage DC Performance Verification on slot 7
- %JOB_END ****PASSED**** Channel_Board Performance Verification of slot 7 (C015F3C) at 5:42:55 PM
- %JOB_START Beginning DPS_DIB Performance Verification test on slot 21 at 5:43:00 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Starting DPS Performance Verification on slot 21

- Verifying DPS Voltage Accuracy
- Verifying DPS Current Limit Accuracy
- Verifying DPS Current Measure Accuracy
- Channel 0
- Channel 1
- Channel 2
- Channel 3
- Channel 4
- Channel 5
- Channel 6
- Channel 7
- Verifying DPS DIB MOUT Output Impedance Test
- Verifying DPS DIB Current Measure Output Accuracy
- Finished DPS Performance Verification on slot 21
- %JOB_END ****PASSED**** DPS_DIB Performance Verification of slot 21 (C01524A) at 5:43:53 PM
- %JOB_START Beginning DPS_DIB Performance Verification test on slot 22 at 5:43:59 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Starting DPS Performance Verification on slot 22
 - Verifying DPS Voltage Accuracy
 - Verifying DPS Current Limit Accuracy
 - Verifying DPS Current Measure Accuracy
 - Channel 0
 - Channel 1
 - Channel 2
 - Channel 3
 - Channel 4
 - Channel 5
 - Channel 6

- Channel 7
- Verifying DPS DIB MOUT Output Impedance Test
- Verifying DPS DIB Current Measure Output Accuracy
- Finished DPS Performance Verification on slot 22
- %JOB_END ****PASSED**** DPS_DIB Performance Verification of slot 22 (C01521B) at 5:44:52 PM
- %JOB_START Beginning DPS_DIB Performance Verification test on slot 23 at 5:44:58 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Starting DPS Performance Verification on slot 23
 - Verifying DPS Voltage Accuracy
 - Verifying DPS Current Limit Accuracy
 - Verifying DPS Current Measure Accuracy
 - Channel 0
 - Channel 1
 - Channel 2
 - Channel 3
 - Channel 4
 - Channel 5
 - Channel 6
 - Channel 7
 - Verifying DPS DIB MOUT Output Impedance Test
 - Verifying DPS DIB Current Measure Output Accuracy
 - Finished DPS Performance Verification on slot 23
- %JOB_END ****PASSED**** DPS_DIB Performance Verification of slot 23 (C01523F) at 5:45:51 PM

%JOB_START - Beginning DPS_DIB Performance Verification test on slot 24 at 5:45:56 PM on 2/29/2020

- Starting DPS Performance Verification on slot 24
- Verifying DPS Voltage Accuracy
- Verifying DPS Current Limit Accuracy
- Verifying DPS Current Measure Accuracy
- Channel 0
- Channel 1
- Channel 2
- Channel 3
- Channel 4
- Channel 5
- Channel 6
- Channel 7
- Verifying DPS DIB MOUT Output Impedance Test
- Verifying DPS DIB Current Measure Output Accuracy
- Finished DPS Performance Verification on slot 24
- %JOB_END ****PASSED**** DPS_DIB Performance Verification of slot 24 (C00577D) at 5:46:49 PM
- $\rm \%JOB_START$ Beginning CTO_DIB Performance Verification test on slot 17 at $\rm 5.46.55~PM$ on $\rm 2/29/2020$

- Performing source verification...
- Performing capture verification...
- Performing VREF verification...
- Performing PPMU Force V verification...

- Performing PPMU Measure V verification...
- Performing PPMU Force I verification...
- Performing PPMU Measure I verification...

%JOB_END - ****PASSED**** CTO_DIB Performance Verification of slot 17 (500933B) at 5:48:01 PM

%JOB_START - Beginning AC Performance Verification at 5:48:07 PM on 2/29/2020 in High Accuracy Mode

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A

- Beginning Digital Channel Timing Performance Verification
- Started at 2/29/2020 5:48:07 PM
- Building List of Digital Channels
- Checking CalDIB AC Continuity for all Digital Channels
- Calibrating Super Linear Interpolator (SLI)
- Calibrating user DIB RTD
- Loading drive deskew registers
- Loading receive deskew registers
- Beginning of Drive Skew Test
- Acquiring Data for Slot 0
- Acquiring Data for Slot 1
- Acquiring Data for Slot 2
- Acquiring Data for Slot 3
- Acquiring Data for Slot 4
- Acquiring Data for Slot 5
- Acquiring Data for Slot 6
- Acquiring Data for Slot 7
- End of Drive Skew Test
- Beginning of Receive Skew Test
- Acquiring Data for Slot 0

- Acquiring Data for Slot 1
- Acquiring Data for Slot 2
- Acquiring Data for Slot 3
- Acquiring Data for Slot 4
- Acquiring Data for Slot 5
- Acquiring Data for Slot 6
- Acquiring Data for Slot 7
- End of Receive Skew Test
- Beginning of Drive Linearity Test
- Acquiring Data for Slot 0
- Acquiring Data for Slot 1
- Acquiring Data for Slot 2
- Acquiring Data for Slot 3
- Acquiring Data for Slot 4
- Acquiring Data for Slot 5
- Acquiring Data for Slot 6
- Acquiring Data for Slot 7
- End of Drive Linearity Test
- Beginning of Receive Linearity Test
- Acquiring Data for Slot 0
- Acquiring Data for Slot 1
- Acquiring Data for Slot 2
- Acquiring Data for Slot 3
- Acquiring Data for Slot 4
- Acquiring Data for Slot 5
- Acquiring Data for Slot 6
- Acquiring Data for Slot 7
- End of Receive Linearity Test
- Adding up error budget
- Completed Digital Channel Timing Performance Verification
- Finished at 2/29/2020 6:30:38 PM

Quick_Module_Check_PASS

%JOB_START - Beginning PCIT Quick Check test on slot 0 at 6:31:15 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT $\,$ IG-XL Version: 3.40.13 $\,$ DIB $\#\,30562A2$ Rev 1137A

- PCIT CARD INFORMATION:

Part Number: 939-360-00

Serial Number: 0 Revision Date: A0422

%JOB_END - ****PASSED**** PCIT Quick Check of slot 0 at 6:31:16 PM

%JOB_START - Beginning CUB Quick Check test on slot 18 at 6:31:27 PM on 2/29/2020

- Starting DIB Power Tests
- Completed DIB Power Tests
- Starting System Fan Checks
- Completed System Fan Checks
- Starting PG_History_Ram
- Completed PG_History_Ram
- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG Scramble Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Skipping PG_LVM_BIST_Ram
- Beginning Qck_Register Test
- Completed Qck_Register Test
- Starting CalCub_TG_Register Tests
- LRS Off
- LRS On

- Completed CalCub_TG_Register Tests
- Started IdProm Test
- Completed IdProm Test
- Beginning Force Voltage Test
- Completed Force Voltage Test
- Beginning TestRefToDac
- Completed TestRefToDac
- Starting the CalCubSLITest
- Completed the CalCubSLITest

%JOB_END - ****PASSED**** CUB Quick Check of slot 18 (26DA50) at 6:31:32 PM

%JOB_START - Beginning Channel_Board Quick Check test on slot 0 at 6:31:37 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting PG_History_Ram
- Completed PG_History_Ram
- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Starting PG_LVM_BIST_Ram (up to 32 sec)
- Completed PG LVM BIST Ram in 21.6 sec

- LRS Off

- Starting TG Register Tests
- Completed TG Register Tests
- LRS On
- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG_Period_Ram

- Completed TG_Period_Ram
- Starting TG_Period_Map_Ram
- Completed TG_Period_Map_Ram
- Starting TG_LVM_BIST_Ram (up to 32 sec)
- Completed TG_LVM_BIST_Ram in 10.8 sec
- Starting TG_History_Ram
- Completed TG_History_Ram
- Starting TG_SVM_Ram
- Completed TG_SVM_Ram
- Starting TG_ADSS_Ram
- Completed TG_ADSS_Ram
- Starting TG_KeepAlive_Ram
- Completed TG_KeepAlive_Ram
- Starting TG_Tset_LkDwn_Ram
- Completed TG_Tset_LkDwn_Ram
- Starting TG_Edge_Ram
- Completed TG_Edge_Ram
- Starting TG_Format_Ram
- Completed TG_Format_Ram
- Starting TG_FormatLkDwn_Ram
- Completed TG_FormatLkDwn_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB_END - ****PASSED**** Channel_Board Quick Check of slot 0 (8035120) at 6:32:27 PM

%JOB_START - Beginning Channel_Board Quick Check test on slot 1 at 6:32:33

PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB

30562A2 Rev 1137A

- Started IdProm Test

- Completed IdProm Test
- Starting PG_History_Ram
- Completed PG_History_Ram
- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Starting PG_LVM_BIST_Ram (up to 32 sec)
- Completed PG_LVM_BIST_Ram in 21.6 sec
- LRS Off
- Starting TG Register Tests
- Completed TG Register Tests

- LRS On

- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG_Period_Ram
- Completed TG_Period_Ram
- Starting TG_Period_Map_Ram
- Completed TG_Period_Map_Ram
- Starting TG_LVM_BIST_Ram (up to 32 sec)
- Completed TG_LVM_BIST_Ram in 10.8 sec
- Starting TG_History_Ram
- Completed TG_History_Ram
- Starting TG_SVM_Ram
- Completed TG SVM Ram
- Starting TG_ADSS_Ram
- Completed TG_ADSS_Ram
- Starting TG_KeepAlive_Ram
- Completed TG_KeepAlive_Ram
- Starting TG_Tset_LkDwn_Ram
- Completed TG_Tset_LkDwn_Ram
- Starting TG_Edge_Ram
- Completed TG_Edge_Ram
- Starting TG_Format_Ram

- Completed TG_Format_Ram
- Starting TG_FormatLkDwn_Ram
- Completed TG_FormatLkDwn_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB_END - ****PASSED**** Channel_Board Quick Check of slot 1 (8031672) at 6:33:22 PM

%JOB_START - Beginning Channel_Board Quick Check test on slot 2 at 6:33:28

PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB

30562A2 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting PG_History_Ram
- Completed PG_History_Ram
- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG Scramble Ram
- Starting PG_LVM_BIST_Ram (up to 32 sec)
- Completed PG_LVM_BIST_Ram in 21.6 sec
- LRS Off
- Starting TG Register Tests
- Completed TG Register Tests
- LRS On
- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register

- Completed 32 bit Read Test Using ADB Register
- Starting TG_Period_Ram
- Completed TG_Period_Ram
- Starting TG_Period_Map_Ram
- Completed TG_Period_Map_Ram
- Starting TG_LVM_BIST_Ram (up to 32 sec)
- Completed TG_LVM_BIST_Ram in 10.8 sec
- Starting TG_History_Ram
- Completed TG_History_Ram
- Starting TG_SVM_Ram
- Completed TG_SVM_Ram
- Starting TG_ADSS_Ram
- Completed TG_ADSS_Ram
- Starting TG_KeepAlive_Ram
- Completed TG_KeepAlive_Ram
- Starting TG_Tset_LkDwn_Ram
- $\hbox{-} \quad Completed \ TG_Tset_LkDwn_Ram$
- Starting TG_Edge_Ram
- Completed TG_Edge_Ram
- Starting TG_Format_Ram
- Completed TG_Format_Ram
- Starting TG_FormatLkDwn_Ram
- Completed TG_FormatLkDwn_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB_END - ****PASSED**** Channel_Board Quick Check of slot 2 (C00E109) at 6:34:18 PM

 $\mbox{\sc MJOB_START}$ - Beginning Channel_Board Quick Check test on slot 3 at 6:34:23

PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT $\,$ IG-XL Version: 3.40.13 $\,$ DIB $\#\,30562A2$ Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting PG_History_Ram
- Completed PG_History_Ram
- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Starting PG_LVM_BIST_Ram (up to 32 sec)
- Completed PG_LVM_BIST_Ram in 21.6 sec

- LRS Off

- Starting TG Register Tests
- Completed TG Register Tests

- LRS On

- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG_Period_Ram
- Completed TG_Period_Ram
- Starting TG_Period_Map_Ram
- Completed TG_Period_Map_Ram
- Starting TG_LVM_BIST_Ram (up to 32 sec)
- Completed TG_LVM_BIST_Ram in 10.8 sec
- Starting TG_History_Ram
- Completed TG History Ram
- Starting TG_SVM_Ram
- Completed TG_SVM_Ram
- Starting TG_ADSS_Ram
- Completed TG_ADSS_Ram
- Starting TG_KeepAlive_Ram
- Completed TG_KeepAlive_Ram
- Starting TG_Tset_LkDwn_Ram
- Completed TG_Tset_LkDwn_Ram
- Starting TG_Edge_Ram

- Completed TG_Edge_Ram
- Starting TG_Format_Ram
- Completed TG_Format_Ram
- Starting TG_FormatLkDwn_Ram
- Completed TG_FormatLkDwn_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB_END - ****PASSED**** Channel_Board Quick Check of slot 3 (C0157F4) at 6:35:13 PM

%JOB_START - Beginning Channel_Board Quick Check test on slot 4 at 6:35:18

PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB

30562A2 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting PG_History_Ram
- Completed PG_History_Ram
- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG Scramble Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Starting PG_LVM_BIST_Ram (up to 32 sec)
- Completed PG_LVM_BIST_Ram in 21.6 sec
- LRS Off
- Starting TG Register Tests
- Completed TG Register Tests
- LRS On
- Starting TG Register Tests

- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG_Period_Ram
- Completed TG_Period_Ram
- Starting TG_Period_Map_Ram
- Completed TG_Period_Map_Ram
- Starting TG_LVM_BIST_Ram (up to 32 sec)
- Completed TG_LVM_BIST_Ram in 10.8 sec
- Starting TG_History_Ram
- Completed TG_History_Ram
- Starting TG_SVM_Ram
- Completed TG_SVM_Ram
- Starting TG_ADSS_Ram
- Completed TG_ADSS_Ram
- Starting TG_KeepAlive_Ram
- Completed TG_KeepAlive_Ram
- Starting TG_Tset_LkDwn_Ram
- Completed TG_Tset_LkDwn_Ram
- Starting TG_Edge_Ram
- Completed TG_Edge_Ram
- Starting TG_Format_Ram
- Completed TG_Format_Ram
- Starting TG_FormatLkDwn_Ram
- Completed TG_FormatLkDwn_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB_END - ****PASSED**** Channel_Board Quick Check of slot 4 (C014821) at 6:36:08 PM

%JOB_START - Beginning Channel_Board Quick Check test on slot 5 at 6:36:14 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT $\,$ IG-XL Version: 3.40.13 $\,$ DIB $\#\,30562A2$ Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting PG_History_Ram
- Completed PG_History_Ram
- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Starting PG_LVM_BIST_Ram (up to 32 sec)
- Completed PG_LVM_BIST_Ram in 21.6 sec

- LRS Off

- Starting TG Register Tests
- Completed TG Register Tests

- LRS On

- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG_Period_Ram
- Completed TG_Period_Ram
- Starting TG_Period_Map_Ram
- Completed TG_Period_Map_Ram
- Starting TG_LVM_BIST_Ram (up to 32 sec)
- Completed TG LVM BIST Ram in 10.8 sec
- Starting TG_History_Ram
- Completed TG_History_Ram
- Starting TG_SVM_Ram
- Completed TG_SVM_Ram
- Starting TG_ADSS_Ram
- Completed TG_ADSS_Ram
- Starting TG_KeepAlive_Ram
- Completed TG_KeepAlive_Ram
- Starting TG_Tset_LkDwn_Ram

- Completed TG_Tset_LkDwn_Ram
- Starting TG_Edge_Ram
- Completed TG_Edge_Ram
- Starting TG_Format_Ram
- Completed TG_Format_Ram
- Starting TG_FormatLkDwn_Ram
- Completed TG_FormatLkDwn_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB_END - ****PASSED**** Channel_Board Quick Check of slot 5 (5007F60) at 6:37:03 PM

%JOB_START - Beginning Channel_Board Quick Check test on slot 6 at 6:37:09
PM on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB
30562A2 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting PG_History_Ram
- Completed PG_History_Ram
- Starting PG_Svm_Ram
- Completed PG Svm Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Starting PG_LVM_BIST_Ram (up to 32 sec)
- Completed PG_LVM_BIST_Ram in 21.6 sec
- LRS Off
- Starting TG Register Tests
- Completed TG Register Tests

- LRS On

- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG_Period_Ram
- Completed TG_Period_Ram
- Starting TG_Period_Map_Ram
- Completed TG_Period_Map_Ram
- Starting TG_LVM_BIST_Ram (up to 32 sec)
- Completed TG_LVM_BIST_Ram in 10.8 sec
- Starting TG_History_Ram
- Completed TG_History_Ram
- Starting TG_SVM_Ram
- Completed TG_SVM_Ram
- Starting TG_ADSS_Ram
- Completed TG_ADSS_Ram
- Starting TG_KeepAlive_Ram
- Completed TG_KeepAlive_Ram
- Starting TG_Tset_LkDwn_Ram
- Completed TG_Tset_LkDwn_Ram
- Starting TG_Edge_Ram
- Completed TG_Edge_Ram
- Starting TG_Format_Ram
- Completed TG_Format_Ram
- Starting TG_FormatLkDwn_Ram
- Completed TG_FormatLkDwn_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB_END - ****PASSED**** Channel_Board Quick Check of slot 6 (C0157F1) at 6:37:58 PM

%JOB_START - Beginning Channel_Board Quick Check test on slot 7 at 6:38:04

PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT $\,$ IG-XL Version: 3.40.13 $\,$ DIB $\#\,30562A2$ Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting PG_History_Ram
- Completed PG_History_Ram
- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Starting PG_LVM_BIST_Ram (up to 32 sec)
- Completed PG_LVM_BIST_Ram in 21.6 sec

- LRS Off

- Starting TG Register Tests
- Completed TG Register Tests

- LRS On

- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG_Period_Ram
- Completed TG_Period_Ram
- Starting TG_Period_Map_Ram
- Completed TG_Period_Map_Ram
- Starting TG_LVM_BIST_Ram (up to 32 sec)
- Completed TG_LVM_BIST_Ram in 10.8 sec
- Starting TG_History_Ram
- Completed TG_History_Ram
- Starting TG SVM Ram
- Completed TG_SVM_Ram
- Starting TG_ADSS_Ram
- Completed TG_ADSS_Ram
- Starting TG_KeepAlive_Ram

- Completed TG_KeepAlive_Ram
- Starting TG_Tset_LkDwn_Ram
- Completed TG_Tset_LkDwn_Ram
- Starting TG_Edge_Ram
- Completed TG_Edge_Ram
- Starting TG_Format_Ram
- Completed TG_Format_Ram
- Starting TG_FormatLkDwn_Ram
- Completed TG_FormatLkDwn_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!
- %JOB_END ****PASSED**** Channel_Board Quick Check of slot 7 (C015F3C) at 6:38:54 PM
- %JOB_START Beginning Relay_Board_Lower Quick Check test on slot 0 at 6:38:59 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Running Relay Checker Ver 1.03 on AG012 in Slot 0 in Quick Mode (Cal Relay DIB Not Required)
- %JOB_END ****PASSED**** Relay_Board_Lower Quick Check of slot 0 at 6:39:00 PM
- %JOB_START Beginning Relay_Board_Lower Quick Check test on slot 1 at 6:39:05 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Running Relay Checker Ver 1.03 on AG012 in Slot 1 in Quick Mode (Cal Relay DIB Not Required)

- %JOB_END ****PASSED**** Relay_Board_Lower Quick Check of slot 1 at 6:39:06 PM
- %JOB_START Beginning Relay_Board_Lower Quick Check test on slot 2 at 6:39:11 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Running Relay Checker Ver 1.03 on AG012 in Slot 2 in Quick Mode (Cal Relay DIB Not Required)
- %JOB_END ****PASSED**** Relay_Board_Lower Quick Check of slot 2 at 6:39:12 PM
- %JOB_START Beginning Relay_Board_Lower Quick Check test on slot 3 at 6:39:17 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Running Relay Checker Ver 1.03 on AG012 in Slot 3 in Quick Mode (Cal Relay DIB Not Required)
- %JOB_END ****PASSED**** Relay_Board_Lower Quick Check of slot 3 at 6:39:18 PM
- %JOB_START Beginning Relay_Board_Lower Quick Check test on slot 4 at 6:39:24 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Running Relay Checker Ver 1.03 on AG012 in Slot 4 in Quick Mode (Cal Relay DIB Not Required)
- %JOB_END ****PASSED**** Relay_Board_Lower Quick Check of slot 4 at $6\rlap{:}39\rlap{:}24$ PM
- %JOB_START Beginning Relay_Board_Lower Quick Check test on slot 5 at

6:39:30 PM on 2/29/2020 Workbook Rev V7.30.12_0835_MOUT $$\rm IG\textsc{-}XL\ Version\ :\ 3.40.13\ DIB\ #\ 30562A2\ Rev\ 1137A$

- Running Relay Checker Ver 1.03 on AG012 in Slot 5 in Quick Mode (Cal Relay DIB Not Required)
- %JOB_END ****PASSED**** Relay_Board_Lower Quick Check of slot 5 at 6:39:30 PM
- %JOB_START Beginning Relay_Board_Lower Quick Check test on slot 6 at 6:39:36 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Running Relay Checker Ver 1.03 on AG012 in Slot 6 in Quick Mode (Cal Relay DIB Not Required)
- %JOB_END ****PASSED**** Relay_Board_Lower Quick Check of slot 6 at 6:39:36 PM
- %JOB_START Beginning Relay_Board_Lower Quick Check test on slot 7 at 6:39:42 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Running Relay Checker Ver 1.03 on AG012 in Slot 7 in Quick Mode (Cal Relay DIB Not Required)
- %JOB_END ****PASSED**** Relay_Board_Lower Quick Check of slot 7 at 6:39:42 PM
- %JOB_START Beginning Relay_Board_Upper Quick Check test on slot 0 at 6:39:48 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Running Relay Checker Ver 1.03 on AG009 in Slot 0 in Quick Mode (Cal Relay DIB

Not Required)

- %JOB_END ****PASSED**** Relay_Board_Upper Quick Check of slot 0 at 6:39:48 PM
- %JOB_START Beginning Relay_Board_Upper Quick Check test on slot 1 at 6:39:54 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Running Relay Checker Ver 1.03 on AG009 in Slot 1 in Quick Mode (Cal Relay DIB Not Required)
- %JOB_END ****PASSED**** Relay_Board_Upper Quick Check of slot 1 at 6:39:54 PM
- %JOB_START Beginning Relay_Board_Upper Quick Check test on slot 2 at 6:40:00 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Running Relay Checker Ver 1.03 on AG009 in Slot 2 in Quick Mode (Cal Relay DIB Not Required)
- %JOB_END ****PASSED**** Relay_Board_Upper Quick Check of slot 2 at 6:40:01 PM
- %JOB_START Beginning Relay_Board_Upper Quick Check test on slot 3 at 6:40:06 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Running Relay Checker Ver 1.03 on AG009 in Slot 3 in Quick Mode (Cal Relay DIB Not Required)
- %JOB_END ****PASSED**** Relay_Board_Upper Quick Check of slot 3 at 6:40:07 PM

- %JOB_START Beginning Relay_Board_Upper Quick Check test on slot 4 at 6:40:12 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Running Relay Checker Ver 1.03 on AG009 in Slot 4 in Quick Mode (Cal Relay DIB Not Required)
- %JOB_END ****PASSED**** Relay_Board_Upper Quick Check of slot 4 at 6:40:13 PM
- %JOB_START Beginning Relay_Board_Upper Quick Check test on slot 5 at 6:40:18 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Running Relay Checker Ver 1.03 on AG009 in Slot 5 in Quick Mode (Cal Relay DIB Not Required)
- %JOB_END ****PASSED**** Relay_Board_Upper Quick Check of slot 5 at 6:40:19 PM
- %JOB_START Beginning Relay_Board_Upper Quick Check test on slot 6 at 6:40:24 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Running Relay Checker Ver 1.03 on AG009 in Slot 6 in Quick Mode (Cal Relay DIB Not Required)
- %JOB_END ****PASSED**** Relay_Board_Upper Quick Check of slot 6 at $6\hbox{:}40\hbox{:}25~\mathrm{PM}$
- %JOB_START Beginning Relay_Board_Upper Quick Check test on slot 7 at 6:40:30 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A

 Running Relay Checker Ver 1.03 on AG009 in Slot 7 in Quick Mode (Cal Relay DIB Not Required)

%JOB_END - ****PASSED**** Relay_Board_Upper Quick Check of slot 7 at 6:40:31 PM

% JOB_START - Beginning CTO Quick Check test on slot 17 at 6:40:37 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A

- Performing register test...
- Completed register test.
- Performing idprom and temperature test...
- Completed idprom and temperature test.
- Performing PG test...
- Starting PG_History_Ram
- Completed PG_History_Ram
- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Starting PG_LVM_BIST_Ram (up to 32 sec)
- Completed PG_LVM_BIST_Ram in 21.6 sec
- Completed PG test.
- Performing internal loopback test...
- Completed internal loopback test.
- Performing local reference test...
- Completed local reference test.
- Performing internal Capture burst test...
- Completed internal Capture burst test.
- Performing internal loopback burst test...
- Completed internal loopback burst test.

%JOB_END - ****PASSED**** CTO Quick Check of slot 17 (500933B) at 6:41:01 PM

% JOB_START - Beginning CTO_DIB Quick Check test on slot 17 at 6:41:06 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A

- Performing relay test...
- %JOB_END ****PASSED**** CTO_DIB Quick Check of slot 17 (500933B) at 6:41:07 PM
- $\mbox{\sc MJOB_START}$ Beginning DPS Quick Check test on slot 21 at 6:41:12 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A

- Verifying DPS Current Leakage
- Channel 0
- Channel 1
- Channel 2
- Channel 3
- Channel 4
- Channel 5
- Channel 6
- Channel 7
- Finished Verifying DPS Current Leakage

%JOB_END - ****PASSED**** DPS Quick Check of slot 21 (C01524A) at 6:41:15 PM

 $\mbox{\sc MJOB_START}$ - Beginning DPS Quick Check test on slot 22 at 6:41:20 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT $\,$ IG-XL Version: 3.40.13 $\,$ DIB $\#\,30562A2$ Rev 1137A

- Verifying DPS Current Leakage
- Channel 0
- Channel 1
- Channel 2

- Channel 3
- Channel 4
- Channel 5
- Channel 6
- Channel 7
- Finished Verifying DPS Current Leakage

%JOB_END - ****PASSED**** DPS Quick Check of slot 22 (C01521B) at 6:41:23 PM

% JOB_START - Beginning DPS Quick Check test on slot 23 at 6:41:28 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A

- Verifying DPS Current Leakage
- Channel 0
- Channel 1
- Channel 2
- Channel 3
- Channel 4
- Channel 5
- Channel 6
- Channel 7
- Finished Verifying DPS Current Leakage

 $\mbox{\%JOB_END}$ - ****PASSED**** DPS Quick Check of slot 23 (C01523F) at 6:41:30 PM

%JOB_START - Beginning DPS Quick Check test on slot 24 at 6:41:36 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A

- Verifying DPS Current Leakage
- Channel 0
- Channel 1
- Channel 2

- Channel 3
- Channel 4
- Channel 5
- Channel 6
- Channel 7
- Finished Verifying DPS Current Leakage

%JOB_END - ****PASSED**** DPS Quick Check of slot 24 (C00577D) at 6:41:38 PM

%JOB_START - Beginning systemwide tests at 6:41:44 PM on 2/29/2020 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A

- Systemwide functionality and continuity to slot 0
- Systemwide functionality and continuity to slot 1
- Systemwide functionality and continuity to slot 2
- Systemwide functionality and continuity to slot 3
- Systemwide functionality and continuity to slot 4
- Systemwide functionality and continuity to slot 5
- Systemwide functionality and continuity to slot $\boldsymbol{6}$
- Systemwide functionality and continuity to slot 7
- Starting BackPlane Fail Bus test
- Completed BackPlane Fail Bus test
- Completed Extra

%JOB_END - ****PASSED**** Systemwide tests at 6:42:52 PM

%JOB_START - Beginning CUB Module Check test on slot 18 at 6:42:57 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT $\,$ IG-XL Version: 3.40.13 $\,$ DIB $\#\,30562A2$ Rev 1137A

- Testing CalCUB and CalDIB Leakage
- Completed Cal Dib Leakage Test
- Testing CalCUB Voltage Sources on CalDIB
- Beginning Cal DIB RawV Test on Slot18
- Completed Cal DIB RawV Test on Slot 18

- Beginning Cal Dib to BPMU Test on Slot 0
- Completed Cal Dib to BPMU Test on Slot 0
- Beginning Cal Dib to EXTERN Test on Slot 0
- Completed Cal Dib to EXTERN Test on Slot 0
- Testing CalDIB Utility Bit Relays using Channel Board Utility Bits for Slot 0
- Completed Cal Dib to UTIL Test on Slot 0
- Beginning Cal Dib RF Tree & Pogo Test
- Beginning Cal Dib to BPMU Test on Slot 1
- Completed Cal Dib to BPMU Test on Slot 1
- Beginning Cal Dib to EXTERN Test on Slot 1
- Completed Cal Dib to EXTERN Test on Slot 1
- Testing CalDIB Utility Bit Relays using Channel Board Utility Bits for Slot 1
- Completed Cal Dib to UTIL Test on Slot 1
- Beginning Cal Dib RF Tree & Pogo Test
- Beginning Cal Dib to BPMU Test on Slot 2
- Completed Cal Dib to BPMU Test on Slot 2
- Beginning Cal Dib to EXTERN Test on Slot 2
- Completed Cal Dib to EXTERN Test on Slot 2
- Testing CalDIB Utility Bit Relays using Channel Board Utility Bits for Slot 2
- Completed Cal Dib to UTIL Test on Slot 2
- Beginning Cal Dib RF Tree & Pogo Test
- Beginning Cal Dib to BPMU Test on Slot 3
- Completed Cal Dib to BPMU Test on Slot 3
- Beginning Cal Dib to EXTERN Test on Slot 3
- Completed Cal Dib to EXTERN Test on Slot 3
- Testing CalDIB Utility Bit Relays using Channel Board Utility Bits for Slot 3
- Completed Cal Dib to UTIL Test on Slot 3
- Beginning Cal Dib RF Tree & Pogo Test
- Beginning Cal Dib to BPMU Test on Slot 4
- Completed Cal Dib to BPMU Test on Slot 4
- Beginning Cal Dib to EXTERN Test on Slot 4
- Completed Cal Dib to EXTERN Test on Slot 4
- Testing CalDIB Utility Bit Relays using Channel Board Utility Bits for Slot 4
- Completed Cal Dib to UTIL Test on Slot 4

- Beginning Cal Dib RF Tree & Pogo Test
- Beginning Cal Dib to BPMU Test on Slot 5
- Completed Cal Dib to BPMU Test on Slot 5
- Beginning Cal Dib to EXTERN Test on Slot 5
- Completed Cal Dib to EXTERN Test on Slot 5
- Testing CalDIB Utility Bit Relays using Channel Board Utility Bits for Slot 5
- Completed Cal Dib to UTIL Test on Slot 5
- Beginning Cal Dib RF Tree & Pogo Test
- Beginning Cal Dib to BPMU Test on Slot 6
- Completed Cal Dib to BPMU Test on Slot 6
- Beginning Cal Dib to EXTERN Test on Slot 6
- Completed Cal Dib to EXTERN Test on Slot 6
- Testing CalDIB Utility Bit Relays using Channel Board Utility Bits for Slot 6
- Completed Cal Dib to UTIL Test on Slot 6
- Beginning Cal Dib RF Tree & Pogo Test
- Beginning Cal Dib to BPMU Test on Slot 7
- Completed Cal Dib to BPMU Test on Slot 7
- Beginning Cal Dib to EXTERN Test on Slot 7
- Completed Cal Dib to EXTERN Test on Slot 7
- Testing CalDIB Utility Bit Relays using Channel Board Utility Bits for Slot 7
- Completed Cal Dib to UTIL Test on Slot 7
- Beginning Cal Dib RF Tree & Pogo Test
- Beginning Cal DIB to DPS_FSG Test on Slot 21
- Completed Cal DIB to DPS_FSG Test on Slot 21
- Beginning Cal DIB to DPS FSG Test on Slot 22
- Completed Cal DIB to DPS_FSG Test on Slot 22
- Beginning Cal DIB to DPS_FSG Test on Slot 23
- Completed Cal DIB to DPS_FSG Test on Slot 23
- Beginning Cal DIB to DPS_FSG Test on Slot 24
- Completed Cal DIB to DPS FSG Test on Slot 24
- Testing Device Ground Sense on CalDIB
- Completed DGS Test
- ****Completed CalDib Test****
- Start Compare Level Vol test

- Completed Compare Level Vol test
- Start Compare Level Voh test
- Completed Compare Level Voh test
- Start Drive_Level_test VIL
- Completed Drive_Level_test VIL
- Start Drive_Level_test VIH
- Completed Drive_Level_test VIH
- Beginning CalCub_Measure_Current using Bpmu in Slot 0
- Completed CalCub_Measure_Current

 $\mbox{\%JOB_END}$ - ****PASSED**** CUB Module Check of slot 18 (26DA50) at 6:44:32 PM

%JOB_START - Beginning Channel_Board_DIB Module Check test on slot 0 at 6:44:38 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test
- Starting Pin PMU Checker
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests...
- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
- Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
- Continuing Compare Level tests

- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Continuing Compare Level tests
- Completed Commpare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.

- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
- Statebus: Checking STB lines: Normal mode, 30MHz
- Statebus: Checking STB lines: Normal mode, 50MHz
- Statebus: Checking STB lines: Normal mode, 80MHz
- Statebus: Checking STB lines: Normal mode, 100MHz
- Statebus: Checking State number lines: Extended mode, 25MHz
- Statebus: Checking State number lines: Extended mode, 30MHz
- Statebus: Checking State number lines: Extended mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 25MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 30MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 35MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 35MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 80MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test

- Started Scan Load test
- Completed Scan Load test
- Started Scan ADB test
- Completed Scan ADB test
- Completed Scan test
- Starting MultiClock test
- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel_Board_DIB test on slot 0

%JOB_END - ****PASSED**** Channel_Board_DIB Module Check of slot 0 (8035120) at 6:49:03 PM

%JOB_START - Beginning Channel_Board_DIB Module Check test on slot 1 at 6:49:08 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test
- Starting Pin PMU Checker
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests...
- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
- Continuing Drive Level tests

- Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
- Continuing Compare Level tests
- Completed Commpare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.

- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
- Statebus: Checking STB lines: Normal mode, 30MHz
- Statebus : Checking STB lines : Normal mode, 50MHz
- Statebus: Checking STB lines: Normal mode, 80MHz
- Statebus: Checking STB lines: Normal mode, 100MHz
- Statebus: Checking State number lines: Extended mode, 25MHz
- Statebus: Checking State number lines: Extended mode, 30MHz
- Statebus: Checking State number lines: Extended mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 25MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 30MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 35MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 35MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 80MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test

- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
- Started Scan Load test
- Completed Scan Load test
- Started Scan ADB test
- Completed Scan ADB test
- Completed Scan test
- Starting MultiClock test
- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel_Board_DIB test on slot 1

%JOB_END - ****PASSED**** Channel_Board_DIB Module Check of slot 1 (8031672) at 6:53:33 PM

%JOB_START - Beginning Channel_Board_DIB Module Check test on slot 2 at 6:53:38 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test
- Starting Pin PMU Checker
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests...
- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
- Continuing Drive Level tests

- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
- Continuing Compare Level tests
- Completed Commpare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz
- Continuing Random Pattern test.

- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
- Statebus : Checking STB lines : Normal mode, 30MHz
- Statebus : Checking STB lines : Normal mode, 50MHz
- Statebus: Checking STB lines: Normal mode, 80MHz
- Statebus: Checking STB lines: Normal mode, 100MHz
- Statebus: Checking State number lines: Extended mode, 25MHz
- Statebus: Checking State number lines: Extended mode, 30MHz
- Statebus : Checking State number lines : Extended mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 25MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 30MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 35MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 50MHz

- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 35MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 80MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
- Started Scan Load test
- Completed Scan Load test
- Started Scan ADB test
- Completed Scan ADB test
- Completed Scan test
- Starting MultiClock test
- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel_Board_DIB test on slot 2

%JOB_END - ****PASSED**** Channel_Board_DIB Module Check of slot 2 (C00E109) at 6:58:03 PM

%JOB_START - Beginning Channel_Board_DIB Module Check test on slot 3 at 6:58:09 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test
- Starting Pin PMU Checker
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...

- Performing PPMU force current tests...
- Performing PPMU measure current tests...
- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
- Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
- Continuing Compare Level tests
- Completed Commpare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz

- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
- Statebus: Checking STB lines: Normal mode, 30MHz
- Statebus: Checking STB lines: Normal mode, 50MHz
- Statebus: Checking STB lines: Normal mode, 80MHz
- Statebus: Checking STB lines: Normal mode, 100MHz
- Statebus: Checking State number lines: Extended mode, 25MHz
- Statebus: Checking State number lines: Extended mode, 30MHz
- Statebus: Checking State number lines: Extended mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 25MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 30MHz

- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 35MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 80MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
- Started Scan Load test
- Completed Scan Load test
- Started Scan ADB test
- Completed Scan ADB test
- Completed Scan test
- Starting MultiClock test
- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel_Board_DIB test on slot 3

%JOB_END - ****PASSED**** Channel_Board_DIB Module Check of slot 3 (C0157F4) at 7:02:34 PM

%JOB_START - Beginning Channel_Board_DIB Module Check test on slot 4 at 7:02:39 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test

- Completed Board PMU test
- Starting Pin PMU Checker
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests...
- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
- Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
- Continuing Compare Level tests
- Completed Commpare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.

- Starting KeepAlive Test, Normal Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
- Statebus : Checking STB lines : Normal mode, 30MHz
- Statebus: Checking STB lines: Normal mode, 50MHz
- Statebus: Checking STB lines: Normal mode, 80MHz
- Statebus: Checking STB lines: Normal mode, 100MHz
- Statebus: Checking State number lines: Extended mode, 25MHz
- Statebus: Checking State number lines: Extended mode, 30MHz

- Statebus: Checking State number lines: Extended mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 25MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 30MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 35MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 80MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
- Started Scan Load test
- Completed Scan Load test
- Started Scan ADB test
- Completed Scan ADB test
- Completed Scan test
- Starting MultiClock test
- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel_Board_DIB test on slot 4

%JOB_END - ****PASSED**** Channel_Board_DIB Module Check of slot 4 (C014821) at 7:07:04 PM

 $\mbox{\%JOB_START}$ - Beginning Channel_Board_DIB Module Check test on slot 5 at 7:07:10 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB

30562A2 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test
- Starting Pin PMU Checker
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests...
- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
- Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
- Continuing Compare Level tests
- Completed Commpare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test

- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
- Statebus : Checking STB lines : Normal mode, 30MHz

- Statebus: Checking STB lines: Normal mode, 50MHz
- Statebus: Checking STB lines: Normal mode, 80MHz
- Statebus: Checking STB lines: Normal mode, 100MHz
- Statebus: Checking State number lines: Extended mode, 25MHz
- Statebus: Checking State number lines: Extended mode, 30MHz
- Statebus: Checking State number lines: Extended mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 25MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 30MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 35MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 35MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 80MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
- Started Scan Load test
- Completed Scan Load test
- Started Scan ADB test
- Completed Scan ADB test
- Completed Scan test
- Starting MultiClock test
- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel_Board_DIB test on slot 5

(5007F60) at 7:11:35 PM

$\mbox{\ensuremath{\mbox{WJOB_START}}}$ - Beginning Channel_Board_DIB Module Check test on slot 6 at

7:11:40 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test
- Starting Pin PMU Checker
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests...
- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
- Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
- Continuing Compare Level tests
- Completed Commpare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test

- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test

- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
- Statebus: Checking STB lines: Normal mode, 30MHz
- Statebus: Checking STB lines: Normal mode, 50MHz
- Statebus: Checking STB lines: Normal mode, 80MHz
- Statebus: Checking STB lines: Normal mode, 100MHz
- Statebus: Checking State number lines: Extended mode, 25MHz
- Statebus: Checking State number lines: Extended mode, 30MHz
- Statebus: Checking State number lines: Extended mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 25MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 30MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 35MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 35MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 80MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
- Started Scan Load test
- Completed Scan Load test
- Started Scan ADB test
- Completed Scan ADB test
- Completed Scan test
- Starting MultiClock test
- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.
- Starting Timing Edge Test

- Completed Timing Edge Test
- Completed Channel_Board_DIB test on slot 6

%JOB_END - ****PASSED**** Channel_Board_DIB Module Check of slot 6 (C0157F1) at 7:16:05 PM

%JOB_START - Beginning Channel_Board_DIB Module Check test on slot 7 at 7:16:10 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test
- Starting Pin PMU Checker
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests...
- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
- Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
- Continuing Compare Level tests
- Completed Commpare Level tests

- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.

- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
- Statebus: Checking STB lines: Normal mode, 30MHz
- Statebus: Checking STB lines: Normal mode, 50MHz
- Statebus: Checking STB lines: Normal mode, 80MHz
- Statebus: Checking STB lines: Normal mode, 100MHz
- Statebus: Checking State number lines: Extended mode, 25MHz
- Statebus: Checking State number lines: Extended mode, 30MHz
- Statebus: Checking State number lines: Extended mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 25MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 30MHz
 - ${\bf Statebus: Checking\ TSET, ADSS,\ MASK,\ CFAIL,\ FmtFAIL: Extended\ mode,}\\ {\bf 35MHz}$
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Extended mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 35MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 50MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 80MHz
- Statebus: Checking TSET, ADSS, MASK, CFAIL, FmtFAIL: Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
- Started Scan Load test
- Completed Scan Load test
- Started Scan ADB test
- Completed Scan ADB test
- Completed Scan test

- Starting MultiClock test
- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel_Board_DIB test on slot 7
- %JOB_END ****PASSED**** Channel_Board_DIB Module Check of slot 7 (C015F3C) at 7:20:35 PM
- %JOB_START Beginning Relay_Board_Lower Module Check test on slot 0 at 7:20:40 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Running Relay Checker Ver 1.03 on AG012 in Slot 0 in Full Mode
- %JOB_END ****PASSED**** Relay_Board_Lower Module Check of slot 0 at 7:20:41 PM
- %JOB_START Beginning Relay_Board_Lower Module Check test on slot 1 at 7:20:47 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Running Relay Checker Ver 1.03 on AG012 in Slot 1 in Full Mode
- %JOB_END ****PASSED**** Relay_Board_Lower Module Check of slot 1 at $7{:}20{:}47~\mathrm{PM}$
- %JOB_START Beginning Relay_Board_Lower Module Check test on slot 2 at 7:20:53 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Running Relay Checker Ver 1.03 on AG012 in Slot 2 in Full Mode

- %JOB_END ****PASSED**** Relay_Board_Lower Module Check of slot 2 at 7:20:53 PM
- %JOB_START Beginning Relay_Board_Lower Module Check test on slot 3 at 7:20:59 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Running Relay Checker Ver 1.03 on AG012 in Slot 3 in Full Mode
- %JOB_END ****PASSED**** Relay_Board_Lower Module Check of slot 3 at 7:20:59 PM
- %JOB_START Beginning Relay_Board_Lower Module Check test on slot 4 at 7:21:05 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Running Relay Checker Ver 1.03 on AG012 in Slot 4 in Full Mode
- %JOB_END ****PASSED**** Relay_Board_Lower Module Check of slot 4 at 7:21:05 PM
- %JOB_START Beginning Relay_Board_Lower Module Check test on slot 5 at 7:21:11 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Running Relay Checker Ver 1.03 on AG012 in Slot 5 in Full Mode
- $\% JOB_END$ ****PASSED**** Relay_Board_Lower Module Check of slot 5 at 7:21:11 PM
- %JOB_START Beginning Relay_Board_Lower Module Check test on slot 6 at 7:21:17 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 6 in Full Mode
- %JOB_END ****PASSED**** Relay_Board_Lower Module Check of slot 6 at 7:21:17 PM
- %JOB_START Beginning Relay_Board_Lower Module Check test on slot 7 at 7:21:23 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Running Relay Checker Ver 1.03 on AG012 in Slot 7 in Full Mode
- %JOB_END ****PASSED**** Relay_Board_Lower Module Check of slot 7 at 7:21:24 PM
- %JOB_START Beginning Relay_Board_Upper Module Check test on slot 0 at 7:21:29 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Running Relay Checker Ver 1.03 on AG009 in Slot 0 in Full Mode
- %JOB_END ****PASSED**** Relay_Board_Upper Module Check of slot 0 at 7:21:30 PM
- %JOB_START Beginning Relay_Board_Upper Module Check test on slot 1 at 7:21:35 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Running Relay Checker Ver 1.03 on AG009 in Slot 1 in Full Mode
- %JOB_END ****PASSED**** Relay_Board_Upper Module Check of slot 1 at 7:21:36 PM

30562A2 Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 2 in Full Mode
- %JOB_END ****PASSED**** Relay_Board_Upper Module Check of slot 2 at 7:21:42 PM
- %JOB_START Beginning Relay_Board_Upper Module Check test on slot 3 at 7:21:47 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Running Relay Checker Ver 1.03 on AG009 in Slot 3 in Full Mode
- %JOB_END ****PASSED**** Relay_Board_Upper Module Check of slot 3 at 7:21:48 PM
- %JOB_START Beginning Relay_Board_Upper Module Check test on slot 4 at 7:21:53 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Running Relay Checker Ver 1.03 on AG009 in Slot 4 in Full Mode
- %JOB_END ****PASSED**** Relay_Board_Upper Module Check of slot 4 at 7:21:54 PM
- %JOB_START Beginning Relay_Board_Upper Module Check test on slot 5 at 7:21:59 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Running Relay Checker Ver 1.03 on AG009 in Slot 5 in Full Mode
- $\mbox{\sc Mobseller}$ ****PASSED**** Relay_Board_Upper Module Check of slot 5 at 7:22:00 PM
- %JOB_START Beginning Relay_Board_Upper Module Check test on slot 6 at

- Running Relay Checker Ver 1.03 on AG009 in Slot 6 in Full Mode
- %JOB_END ****PASSED**** Relay_Board_Upper Module Check of slot 6 at 7:22:06 PM
- %JOB_START Beginning Relay_Board_Upper Module Check test on slot 7 at 7:22:12 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Running Relay Checker Ver 1.03 on AG009 in Slot 7 in Full Mode
- %JOB_END ****PASSED**** Relay_Board_Upper Module Check of slot 7 at 7:22:12 PM
- %JOB_START Beginning DPS_DIB Module Check test on slot 21 at 7:22:18 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Verifying DPS DIB MOUT Output Impedance Test
 - Verifying DPS DIB Current Measure Output Test
- %JOB_END ****PASSED**** DPS_DIB Module Check of slot 21 (C01524A) at 7:22:37 PM
- %JOB_START Beginning DPS_DIB Module Check test on slot 22 at 7:22:42 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Verifying DPS DIB MOUT Output Impedance Test

- Verifying DPS DIB Current Measure Output Test
- $\% JOB_END$ ****PASSED**** DPS_DIB Module Check of slot 22 (C01521B) at 7:23:01 PM
- %JOB_START Beginning DPS_DIB Module Check test on slot 23 at 7:23:07 PM on 2/29/2020 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB

 - Verifying DPS DIB MOUT Output Impedance Test
 - Verifying DPS DIB Current Measure Output Test
- %JOB_END ****PASSED**** DPS_DIB Module Check of slot 23 (C01523F) at 7:23:26 PM
- %JOB_START Beginning DPS_DIB Module Check test on slot 24 at 7:23:31 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Verifying DPS DIB MOUT Output Impedance Test
 - Verifying DPS DIB Current Measure Output Test
- %JOB_END ****PASSED**** DPS_DIB Module Check of slot 24 (C00577D) at 7:23:51 PM
- %JOB_START Beginning CTO Module Check test on slot 17 at 7:23:56 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Performing CTO calibration test...
 - Performing RAM test...
- %JOB END ****PASSED**** CTO Module Check of slot 17 (500933B) at 7:23:59

%JOB_START - Beginning CTO_DIB Module Check test on slot 17 at 7:24:05 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A

- Performing internal reference test...
- Performing VREF voltage test...
- Performing PPMU force V test...
- Performing PPMU force I test...
- Performing PPMU measure V test...
- Performing PPMU measure I test...
- Performing Source voltage test...
- Performing Capture voltage test...
- Performing Source/Capture loopback test...
- Performing Source burst test...
- Performing Capture burst test...
- Performing Source/Capture loopback burst test...

%JOB_END - **** PASSED**** CTO_DIB Module Check of slot 17 (500933B) at 7:26:06 PM

Slot17_CTO_ExternalCal

- %JOB_START Beginning CTO_DIB External Calibration test on slot 17 at 2:40:46 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - %PASS Slot 17 channel 0 3V Source offset in mV Measured: 0.4737 low limit: -50 high limit: 50
 - %PASS Slot 17 channel 0 3V Source gain in mV Measured: 0.9994 low limit: 0.98 high limit: 1.02
 - %PASS Slot 17 channel 1 3V Source offset in mV Measured: -0.3303 low limit: -50 high limit: 50
 - %PASS Slot 17 channel 1 3V Source gain in mV Measured: 0.9999 low limit: 0.98 high limit: 1.02
 - %PASS Slot 17 channel 2 3V Source offset in mV Measured: 0.8995 low limit: -50 high limit: 50
 - %PASS Slot 17 channel 2 3V Source gain in mV Measured: 0.9994 low limit: 0.98 high limit: 1.02
 - %PASS Slot 17 channel 3 3V Source offset in mV ${\it Measured: 9.536E-02\ low\ limit: -50\ high\ limit: 50}$
 - %PASS Slot 17 channel 3 3V Source gain in mV Measured: 1.000 low limit: 0.98 high limit: 1.02
 - %PASS Slot 17 channel 4 3V Source offset in mV Measured: 1.136 low limit: -50 high limit: 50
 - %PASS Slot 17 channel 4 3V Source gain in mV Measured: 0.9997 low limit: 0.98 high limit: 1.02
 - %PASS Slot 17 channel 5 3V Source offset in mV

- Measured: 0.6157 low limit: -50 high limit: 50
- %PASS Slot 17 channel 5 3V Source gain in mV Measured: 0.9996 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 6 3V Source offset in mV Measured: 0.1426 low limit: -50 high limit: 50
- %PASS Slot 17 channel 6 3V Source gain in mV Measured: 0.9996 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 7 3V Source offset in mV Measured: 0.6630 low limit: -50 high limit: 50
- %PASS Slot 17 channel 7 3V Source gain in mV Measured: 0.9994 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 0 6V Source offset in mV Measured: 0.9475 low limit: -100 high limit: 100
- %PASS Slot 17 channel 0 6V Source gain in mV Measured: 0.9993 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 1 6V Source offset in mV Measured: 9.613E-02 low limit: -100 high limit: 100
- %PASS Slot 17 channel 1 6V Source gain in mV Measured: 0.9997 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 2 6V Source offset in mV Measured: 1.799 low limit: -100 high limit: 100
- %PASS Slot 17 channel 2 6V Source gain in mV Measured: 0.9991 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 3 6V Source offset in mV Measured: 0.7583 low limit: -100 high limit: 100

- %PASS Slot 17 channel 3 6V Source gain in mV Measured: 0.9996 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 4 6V Source offset in mV Measured: 2.272 low limit: -100 high limit: 100
- %PASS Slot 17 channel 4 6V Source gain in mV Measured: 0.9998 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 5 6V Source offset in mV Measured: 1.420 low limit: -100 high limit: 100
- %PASS Slot 17 channel 5 6V Source gain in mV Measured: 0.9996 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 6 6V Source offset in mV Measured: 0.2853 low limit: -100 high limit: 100
- %PASS Slot 17 channel 6 6V Source gain in mV Measured: 0.9996 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 7 6V Source offset in mV Measured: 1.420 low limit: -100 high limit: 100
- %PASS Slot 17 channel 7 6V Source gain in mV Measured: 0.9993 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 0 at 0V on 3V range $\label{eq:pass} Measured: -0.0001054 \ low \ limit: -0.05 \ high \ limit: 0.05$
- %PASS Slot 17 channel 0 at 0V on 3V range Measured: -4.330E-05 low limit: -0.05 high limit: 0.05
- %PASS Slot 17 channel 0 at 3V on 3V range Measured: 3.000 low limit: 2.95 high limit: 3.05
- %PASS Slot 17 channel 0 at 3V on 3V range Measured: 3.000 low limit: 2.95 high limit: 3.05

- %PASS Slot 17 channel 0 at 0V on 6V range
 Measured: -0.0001284 low limit: -0.1 high limit: 0.1
- %PASS Slot 17 channel 0 at 0V on 6V range Measured: -3.404E-05 low limit: -0.1 high limit: 0.1
- %PASS Slot 17 channel 0 at 6V on 6V range Measured: 5.999 low limit: 5.9 high limit: 6.1
- %PASS Slot 17 channel 0 at 6V on 6V range Measured: 6.000 low limit: 5.9 high limit: 6.1
- %PASS Slot 17 channel 1 at 0V on 3V range

 Measured: -0.0001186 low limit: -0.05 high limit: 0.05
- %PASS Slot 17 channel 1 at 0V on 3V range Measured: -2.812E-05 low limit: -0.05 high limit: 0.05
- %PASS Slot 17 channel 1 at 3V on 3V range Measured: 3.000 low limit: 2.95 high limit: 3.05
- %PASS Slot 17 channel 1 at 3V on 3V range Measured: 3.000 low limit: 2.95 high limit: 3.05
- %PASS Slot 17 channel 1 at 0V on 6V range Measured: -0.0001462 low limit: -0.1 high limit: 0.1
- %PASS Slot 17 channel 1 at 0V on 6V range Measured: -5.773E-05 low limit: -0.1 high limit: 0.1
- %PASS Slot 17 channel 1 at 6V on 6V range Measured: 5.999 low limit: 5.9 high limit: 6.1
- %PASS Slot 17 channel 1 at 6V on 6V range Measured: 6.000 low limit: 5.9 high limit: 6.1
- $\mbox{\%PASS}$ Slot 17 channel 2 at 0V on 3V range

- Measured: -0.0001176 low limit: -0.05 high limit: 0.05
- %PASS Slot 17 channel 2 at 0V on 3V range Measured: -4.405E-05 low limit: -0.05 high limit: 0.05
- %PASS Slot 17 channel 2 at 3V on 3V range Measured: 3.000 low limit: 2.95 high limit: 3.05
- %PASS Slot 17 channel 2 at 3V on 3V range Measured: 3.000 low limit: 2.95 high limit: 3.05
- %PASS Slot 17 channel 2 at 0V on 6V range
 Measured: -0.0001225 low limit: -0.1 high limit: 0.1
- %PASS Slot 17 channel 2 at 0V on 6V range Measured: -3.553E-05 low limit: -0.1 high limit: 0.1
- %PASS Slot 17 channel 2 at 6V on 6V range Measured: 5.999 low limit: 5.9 high limit: 6.1
- %PASS Slot 17 channel 2 at 6V on 6V range Measured: 6.000 low limit: 5.9 high limit: 6.1
- %PASS Slot 17 channel 3 at 0V on 3V range
 Measured: -0.00009671 low limit: -0.05 high limit: 0.05
- %PASS Slot 17 channel 3 at 0V on 3V range Measured: -1.480E-05 low limit: -0.05 high limit: 0.05
- %PASS Slot 17 channel 3 at 3V on 3V range Measured: 3.000 low limit: 2.95 high limit: 3.05
- %PASS Slot 17 channel 3 at 3V on 3V range Measured: 3.000 low limit: 2.95 high limit: 3.05
- %PASS Slot 17 channel 3 at 0V on 6V range

 Measured: -0.0001605 low limit: -0.1 high limit: 0.1

- %PASS Slot 17 channel 3 at 0V on 6V range
 Measured: -9.472E-05 low limit: -0.1 high limit: 0.1
- %PASS Slot 17 channel 3 at 6V on 6V range Measured: 5.999 low limit: 5.9 high limit: 6.1
- %PASS Slot 17 channel 3 at 6V on 6V range Measured: 6.000 low limit: 5.9 high limit: 6.1
- %PASS Slot 17 channel 4 at 0V on 3V range
 Measured: -0.00006151 low limit: -0.05 high limit: 0.05
- %PASS Slot 17 channel 4 at 0V on 3V range Measured: 4.220E-05 low limit: -0.05 high limit: 0.05
- %PASS Slot 17 channel 4 at 3V on 3V range Measured: 3.000 low limit: 2.95 high limit: 3.05
- %PASS Slot 17 channel 4 at 3V on 3V range Measured: 3.000 low limit: 2.95 high limit: 3.05
- %PASS Slot 17 channel 4 at 0V on 6V range Measured: -0.00008595 low limit: -0.1 high limit: 0.1
- %PASS Slot 17 channel 4 at 0V on 6V range Measured: 5.182E-06 low limit: -0.1 high limit: 0.1
- %PASS Slot 17 channel 4 at 6V on 6V range Measured: 5.999 low limit: 5.9 high limit: 6.1
- %PASS Slot 17 channel 4 at 6V on 6V range Measured: 6.000 low limit: 5.9 high limit: 6.1
- %PASS Slot 17 channel 5 at 0V on 3V range Measured: -0.0001112 low limit: -0.05 high limit: 0.05
- %PASS Slot 17 channel 5 at 0V on 3V range Measured: 1.369E-05 low limit: -0.05 high limit: 0.05

- %PASS Slot 17 channel 5 at 3V on 3V range Measured: 3.000 low limit: 2.95 high limit: 3.05
- %PASS Slot 17 channel 5 at 3V on 3V range Measured: 3.000 low limit: 2.95 high limit: 3.05
- %PASS Slot 17 channel 5 at 0V on 6V range
 Measured: -0.0001238 low limit: -0.1 high limit: 0.1
- %PASS Slot 17 channel 5 at 0V on 6V range Measured: -8.218E-05 low limit: -0.1 high limit: 0.1
- %PASS Slot 17 channel 5 at 6V on 6V range Measured: 5.999 low limit: 5.9 high limit: 6.1
- %PASS Slot 17 channel 5 at 6V on 6V range Measured: 6.000 low limit: 5.9 high limit: 6.1
- %PASS Slot 17 channel 6 at 0V on 3V range Measured: -0.0001441 low limit: -0.05 high limit: 0.05
- %PASS Slot 17 channel 6 at 0V on 3V range Measured: -3.480E-05 low limit: -0.05 high limit: 0.05
- %PASS Slot 17 channel 6 at 3V on 3V range Measured: 3.000 low limit: 2.95 high limit: 3.05
- %PASS Slot 17 channel 6 at 3V on 3V range Measured: 3.000 low limit: 2.95 high limit: 3.05
- %PASS Slot 17 channel 6 at 0V on 6V range

 Measured: -0.0001640 low limit: -0.1 high limit: 0.1
- %PASS Slot 17 channel 6 at 0V on 6V range Measured: -4.442E-05 low limit: -0.1 high limit: 0.1
- $\mbox{\%PASS}$ Slot 17 channel 6 at 6V on 6V range

- Measured: 5.999 low limit: 5.9 high limit: 6.1
- %PASS Slot 17 channel 6 at 6V on 6V range Measured: 6.000 low limit: 5.9 high limit: 6.1
- %PASS Slot 17 channel 7 at 0V on 3V range
 Measured: -0.0001262 low limit: -0.05 high limit: 0.05
- %PASS Slot 17 channel 7 at 0V on 3V range
 Measured: -2.776E-05 low limit: -0.05 high limit: 0.05
- %PASS Slot 17 channel 7 at 3V on 3V range Measured: 3.000 low limit: 2.95 high limit: 3.05
- %PASS Slot 17 channel 7 at 3V on 3V range Measured: 3.000 low limit: 2.95 high limit: 3.05
- %PASS Slot 17 channel 7 at 0V on 6V range Measured: -0.00009514 low limit: -0.1 high limit: 0.1
- %PASS Slot 17 channel 7 at 0V on 6V range Measured: 2.073E-05 low limit: -0.1 high limit: 0.1
- %PASS Slot 17 channel 7 at 6V on 6V range Measured: 5.999 low limit: 5.9 high limit: 6.1
- %PASS Slot 17 channel 7 at 6V on 6V range Measured: 6.000 low limit: 5.9 high limit: 6.1
- %JOB_END ****PASSED**** CTO_DIB External Calibration of slot 17 (500933B) at 2:41:39 PM

Slot17_CTO_ExternalPV

- %JOB_START Beginning CTO_DIB External Verification test on slot 17 at 2:42:04 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB # 30562A2 Rev 1137A
 - Performing source and capture verification...
 - %PASS Slot 17 channel 0 3V Source offset in mV Measured: 0.3683 low limit: -50 high limit: 50
 - %PASS Slot 17 channel 0 3V Source gain in mV Measured: 0.9994 low limit: 0.98 high limit: 1.02
 - %PASS Slot 17 channel 1 3V Source offset in mV Measured: -0.4489 low limit: -50 high limit: 50
 - %PASS Slot 17 channel 1 3V Source gain in mV Measured: 0.9999 low limit: 0.98 high limit: 1.02
 - %PASS Slot 17 channel 2 3V Source offset in mV Measured: 0.7818 low limit: -50 high limit: 50
 - %PASS Slot 17 channel 2 3V Source gain in mV Measured: 0.9994 low limit: 0.98 high limit: 1.02
 - %PASS Slot 17 channel 3 3V Source offset in mV Measured: -1.349E-03 low limit: -50 high limit: 50
 - %PASS Slot 17 channel 3 3V Source gain in mV Measured: 1.000 low limit: 0.98 high limit: 1.02
 - %PASS Slot 17 channel 4 3V Source offset in mV Measured: 1.074 low limit: -50 high limit: 50
 - %PASS Slot 17 channel 4 3V Source gain in mV Measured: 0.9998 low limit: 0.98 high limit: 1.02

- %PASS Slot 17 channel 5 3V Source offset in mV Measured: 0.5044 low limit: -50 high limit: 50
- %PASS Slot 17 channel 5 3V Source gain in mV Measured: 0.9997 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 6 3V Source offset in mV Measured: -4.873E-02 low limit: -50 high limit: 50
- %PASS Slot 17 channel 6 3V Source gain in mV Measured: 0.9997 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 7 3V Source offset in mV Measured: 0.5367 low limit: -50 high limit: 50
- %PASS Slot 17 channel 7 3V Source gain in mV Measured: 0.9994 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 0 6V Source offset in mV Measured: 0.8191 low limit: -100 high limit: 100
- %PASS Slot 17 channel 0 6V Source gain in mV Measured: 0.9992 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 1 6V Source offset in mV Measured: -5.010E-02 low limit: -100 high limit: 100
- %PASS Slot 17 channel 1 6V Source gain in mV Measured: 0.9997 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 2 6V Source offset in mV Measured: 1.676 low limit: -100 high limit: 100
- %PASS Slot 17 channel 2 6V Source gain in mV Measured: 0.9990 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 3 6V Source offset in mV Measured: 0.5978 low limit: -100 high limit: 100

- %PASS Slot 17 channel 3 6V Source gain in mV Measured: 0.9996 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 4 6V Source offset in mV Measured: 2.186 low limit: -100 high limit: 100
- %PASS Slot 17 channel 4 6V Source gain in mV Measured: 0.9997 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 5 6V Source offset in mV Measured: 1.296 low limit: -100 high limit: 100
- %PASS Slot 17 channel 5 6V Source gain in mV Measured: 0.9996 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 6 6V Source offset in mV Measured: 0.1213 low limit: -100 high limit: 100
- %PASS Slot 17 channel 6 6V Source gain in mV Measured: 0.9996 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 7 6V Source offset in mV Measured: 1.325 low limit: -100 high limit: 100
- %PASS Slot 17 channel 7 6V Source gain in mV Measured: 0.9992 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 0 source accuracy at 0V on 3V range
 Measured: -0.000003556067V low limit: -0.00018V high limit: 0.00018V
- %PASS Slot 17 channel 0 acquire accuracy at -0.00000355606745V on 3V range Measured: 4.400038E-05V low limit: -0.0001835560V high limit: 0.0001764439V
- %PASS Slot 17 channel 0 source accuracy at 0.5V on 3V range Measured: 0.5000024V low limit: 0.49982V high limit: 0.50018V

- %PASS Slot 17 channel 0 acquire accuracy at 0.5000024077V on 3V range Measured: 0.5000661V low limit: 0.4998224V high limit: 0.5001824V
- %PASS Slot 17 channel 0 source accuracy at 1V on 3V range Measured: 1.000035V low limit: 0.99982V high limit: 1.00018V
- %PASS Slot 17 channel 0 acquire accuracy at 1.000035163V on 3V range Measured: 1.000094V low limit: 0.9998551V high limit: 1.000215V
- %PASS Slot 17 channel 0 source accuracy at 1.5V on 3V range Measured: 1.499989V low limit: 1.49982V high limit: 1.50018V
- %PASS Slot 17 channel 0 acquire accuracy at 1.499989564V on 3V range Measured: 1.500006V low limit: 1.499809V high limit: 1.500169V
- %PASS Slot 17 channel 0 source accuracy at 2V on 3V range Measured: 2.000015V low limit: 1.99982V high limit: 2.00018V
- %PASS Slot 17 channel 0 acquire accuracy at 2.000015859V on 3V range Measured: 1.999999V low limit: 1.999835V high limit: 2.000195V
- %PASS Slot 17 channel 0 source accuracy at 2.5V on 3V range Measured: 2.500017V low limit: 2.49982V high limit: 2.50018V
- %PASS Slot 17 channel 0 acquire accuracy at 2.50001759V on 3V range Measured: 2.499983V low limit: 2.499837V high limit: 2.500197V
- %PASS Slot 17 channel 0 acquire accuracy at 2.999980792V on 3V range Measured: 2.999996V low limit: 2.999800V high limit: 3.000160V
- %PASS Slot 17 channel 0 source accuracy at 0V on 6V range
 Measured: -0.000009615119V low limit: -0.00036V high limit: 0.00036V
- %PASS Slot 17 channel 0 acquire accuracy at -0.000009615119875V on 6V range Measured: 3.349067E-05V low limit: -0.0003696151V high limit:

0.0003503848V

- %PASS Slot 17 channel 0 source accuracy at 1V on 6V range Measured: 1.000007V low limit: 0.99964V high limit: 1.00036V
- %PASS Slot 17 channel 0 acquire accuracy at 1.000007754V on 6V range Measured: 1.000011V low limit: 0.9996477V high limit: 1.000367V
- %PASS Slot 17 channel 0 source accuracy at 2V on 6V range Measured: 2.000073V low limit: 1.99964V high limit: 2.00036V
- %PASS Slot 17 channel 0 acquire accuracy at 2.000073523V on 6V range Measured: 2.000108V low limit: 1.999713V high limit: 2.000433V
- %PASS Slot 17 channel 0 source accuracy at 3V on 6V range Measured: 3.000013V low limit: 2.99964V high limit: 3.00036V
- %PASS Slot 17 channel 0 acquire accuracy at 3.000013105V on 6V range Measured: 2.999967V low limit: 2.999653V high limit: 3.000373V
- %PASS Slot 17 channel 0 source accuracy at 4V on 6V range Measured: 4.000123V low limit: 3.99964V high limit: 4.00036V
- %PASS Slot 17 channel 0 acquire accuracy at 4.000123008V on 6V range Measured: 4.000040V low limit: 3.999763V high limit: 4.000483V
- %PASS Slot 17 channel 0 source accuracy at 5V on 6V range Measured: 5.000121V low limit: 4.99964V high limit: 5.00036V
- %PASS Slot 17 channel 0 acquire accuracy at 5.000121742V on 6V range Measured: 5.000042V low limit: 4.999761V high limit: 5.000481V
- %PASS Slot 17 channel 0 source accuracy at 6V on 6V range Measured: 6.000011V low limit: 5.99964V high limit: 6.00036V
- %PASS Slot 17 channel 0 acquire accuracy at 6.00001106V on 6V range Measured: 6.000020V low limit: 5.999651V high limit: 6.000371V

- %PASS Slot 17 channel 1 source accuracy at 0V on 3V range
 Measured: -0.00001716432V low limit: -0.00018V high limit: 0.00018V
- %PASS Slot 17 channel 1 acquire accuracy at -0.00001716432556V on 3V range Measured: -4.748223E-06V low limit: -0.0001971643V high limit: 0.0001628356V
- %PASS Slot 17 channel 1 source accuracy at 0.5V on 3V range Measured: 0.4999962V low limit: 0.49982V high limit: 0.50018V
- %PASS Slot 17 channel 1 acquire accuracy at 0.4999962575V on 3V range Measured: 0.5000622V low limit: 0.4998162V high limit: 0.5001762V
- %PASS Slot 17 channel 1 source accuracy at 1V on 3V range Measured: 0.9999861V low limit: 0.99982V high limit: 1.00018V
- %PASS Slot 17 channel 1 acquire accuracy at 0.9999861106V on 3V range Measured: 1.000037V low limit: 0.9998061V high limit: 1.000166V
- %PASS Slot 17 channel 1 source accuracy at 1.5V on 3V range Measured: 1.499975V low limit: 1.49982V high limit: 1.50018V
- %PASS Slot 17 channel 1 acquire accuracy at 1.499975115V on 3V range Measured: 1.499977V low limit: 1.499795V high limit: 1.500155V
- %PASS Slot 17 channel 1 source accuracy at 2V on 3V range Measured: 2.000065V low limit: 1.99982V high limit: 2.00018V
- %PASS Slot 17 channel 1 acquire accuracy at 2.000065072V on 3V range Measured: 2.000103V low limit: 1.999885V high limit: 2.000245V
- %PASS Slot 17 channel 1 source accuracy at 2.5V on 3V range Measured: 2.500030V low limit: 2.49982V high limit: 2.50018V
- %PASS Slot 17 channel 1 acquire accuracy at 2.500030113V on 3V range Measured: 2.499986V low limit: 2.499850V high limit: 2.500210V
- %PASS Slot 17 channel 1 source accuracy at 3V on 3V range

- Measured: 3.000003V low limit: 2.99982V high limit: 3.00018V
- %PASS Slot 17 channel 1 acquire accuracy at 3.000003866V on 3V range Measured: 3.000044V low limit: 2.999823V high limit: 3.000183V
- %PASS Slot 17 channel 1 source accuracy at 0V on 6V range Measured: 0.00006702233V low limit: -0.00036V high limit: 0.00036V
- %PASS Slot 17 channel 1 acquire accuracy at 0.00006702233375V on 6V range Measured: 6.235406E-05V low limit: -0.0002929776V high limit: 0.0004270223V
- %PASS Slot 17 channel 1 source accuracy at 1V on 6V range Measured: 1.000082V low limit: 0.99964V high limit: 1.00036V
- %PASS Slot 17 channel 1 acquire accuracy at 1.000082625V on 6V range Measured: 1.000208V low limit: 0.9997226V high limit: 1.000442V
- %PASS Slot 17 channel 1 source accuracy at 2V on 6V range Measured: 2.000095V low limit: 1.99964V high limit: 2.00036V
- %PASS Slot 17 channel 1 acquire accuracy at 2.000095853V on 6V range Measured: 2.000254V low limit: 1.999735V high limit: 2.000455V
- %PASS Slot 17 channel 1 source accuracy at 3V on 6V range Measured: 3.000068V low limit: 2.99964V high limit: 3.00036V
- %PASS Slot 17 channel 1 acquire accuracy at 3.000068973V on 6V range Measured: 3.000009V low limit: 2.999708V high limit: 3.000428V
- %PASS Slot 17 channel 1 source accuracy at 4V on 6V range Measured: 4.000254V low limit: 3.99964V high limit: 4.00036V
- %PASS Slot 17 channel 1 acquire accuracy at 4.000254492V on 6V range Measured: 4.000232V low limit: 3.999894V high limit: 4.000614V
- %PASS Slot 17 channel 1 source accuracy at 5V on 6V range Measured: 5.000183V low limit: 4.99964V high limit: 5.00036V

- %PASS Slot 17 channel 1 acquire accuracy at 5.000183434V on 6V range Measured: 5.000106V low limit: 4.999823V high limit: 5.000543V
- %PASS Slot 17 channel 1 source accuracy at 6V on 6V range Measured: 6.000115V low limit: 5.99964V high limit: 6.00036V
- %PASS Slot 17 channel 1 acquire accuracy at 6.000115223V on 6V range Measured: 6.000104V low limit: 5.999755V high limit: 6.000475V
- %PASS Slot 17 channel 2 source accuracy at 0V on 3V range
 Measured: -0.00001918517V low limit: -0.00018V high limit: 0.00018V
- %PASS Slot 17 channel 2 acquire accuracy at -0.00001918517639V on 3V range Measured: 2.362685E-05V low limit: -0.0001991851V high limit: 0.0001608148V
- %PASS Slot 17 channel 2 source accuracy at 0.5V on 3V range Measured: 0.4999945V low limit: 0.49982V high limit: 0.50018V
- %PASS Slot 17 channel 2 acquire accuracy at 0.4999945622V on 3V range Measured: 0.5000352V low limit: 0.4998145V high limit: 0.5001745V
- %PASS Slot 17 channel 2 source accuracy at 1V on 3V range Measured: 1.000032V low limit: 0.99982V high limit: 1.00018V
- %PASS Slot 17 channel 2 acquire accuracy at 1.000032799V on 3V range Measured: 1.000088V low limit: 0.9998527V high limit: 1.000212V
- %PASS Slot 17 channel 2 source accuracy at 1.5V on 3V range Measured: 1.499999V low limit: 1.49982V high limit: 1.50018V
- %PASS Slot 17 channel 2 acquire accuracy at 1.499999503V on 3V range Measured: 1.499999V low limit: 1.499819V high limit: 1.500179V
- %PASS Slot 17 channel 2 source accuracy at 2V on 3V range Measured: 2.000049V low limit: 1.99982V high limit: 2.00018V

- %PASS Slot 17 channel 2 acquire accuracy at 2.000049003V on 3V range Measured: 2.000052V low limit: 1.999869V high limit: 2.000229V
- %PASS Slot 17 channel 2 source accuracy at 2.5V on 3V range Measured: 2.500053V low limit: 2.49982V high limit: 2.50018V
- %PASS Slot 17 channel 2 acquire accuracy at 2.500053931V on 3V range Measured: 2.499995V low limit: 2.499873V high limit: 2.500233V
- %PASS Slot 17 channel 2 source accuracy at 3V on 3V range Measured: 3.000025V low limit: 2.99982V high limit: 3.00018V
- %PASS Slot 17 channel 2 acquire accuracy at 3.000025846V on 3V range Measured: 3.000022V low limit: 2.999845V high limit: 3.000205V
- %PASS Slot 17 channel 2 source accuracy at 0V on 6V range
 Measured: -0.00001936236V low limit: -0.00036V high limit: 0.00036V
- %PASS Slot 17 channel 2 acquire accuracy at -0.00001936236726V on 6V range Measured: -8.664795E-06V low limit: -0.0003793623V high limit: 0.0003406376V
- %PASS Slot 17 channel 2 source accuracy at 1V on 6V range Measured: 1.000103V low limit: 0.99964V high limit: 1.00036V
- %PASS Slot 17 channel 2 acquire accuracy at 1.000103992V on 6V range Measured: 1.000246V low limit: 0.9997439V high limit: 1.000463V
- %PASS Slot 17 channel 2 source accuracy at 2V on 6V range Measured: 2.000078V low limit: 1.99964V high limit: 2.00036V
- %PASS Slot 17 channel 2 acquire accuracy at 2.000078602V on 6V range Measured: 2.000139V low limit: 1.999718V high limit: 2.000438V
- %PASS Slot 17 channel 2 source accuracy at 3V on 6V range Measured: 3.000083V low limit: 2.99964V high limit: 3.00036V
- $\mbox{\%PASS}$ Slot 17 channel 2 acquire accuracy at $3.000083554\mbox{V}$ on 6V range

- Measured: 3.000074V low limit: 2.999723V high limit: 3.000443V
- %PASS Slot 17 channel 2 source accuracy at 4V on 6V range Measured: 4.000149V low limit: 3.99964V high limit: 4.00036V
- %PASS Slot 17 channel 2 acquire accuracy at 4.000149804V on 6V range Measured: 4.000121V low limit: 3.999789V high limit: 4.000509V
- %PASS Slot 17 channel 2 source accuracy at 5V on 6V range Measured: 5.000018V low limit: 4.99964V high limit: 5.00036V
- %PASS Slot 17 channel 2 acquire accuracy at 5.000018543V on 6V range Measured: 4.999926V low limit: 4.999658V high limit: 5.000378V
- %PASS Slot 17 channel 2 source accuracy at 6V on 6V range Measured: 5.999990V low limit: 5.99964V high limit: 6.00036V
- %PASS Slot 17 channel 2 acquire accuracy at 5.999990394V on 6V range Measured: 6.000015V low limit: 5.999630V high limit: 6.000350V
- %PASS Slot 17 channel 3 source accuracy at 0V on 3V range Measured: 0.000009415616V low limit: -0.00018V high limit: 0.00018V
- %PASS Slot 17 channel 3 acquire accuracy at 0.000009415616091V on 3V range Measured: 1.196595E-05V low limit: -0.0001705843V high limit: 0.0001894156V
- %PASS Slot 17 channel 3 source accuracy at 0.5V on 3V range Measured: 0.5000325V low limit: 0.49982V high limit: 0.50018V
- %PASS Slot 17 channel 3 acquire accuracy at 0.5000325457V on 3V range Measured: 0.5001055V low limit: 0.4998525V high limit: 0.5002125V
- %PASS Slot 17 channel 3 source accuracy at 1V on 3V range Measured: 1.000035V low limit: 0.99982V high limit: 1.00018V
- %PASS Slot 17 channel 3 acquire accuracy at 1.000035251V on 3V range Measured: 1.000128V low limit: 0.9998552V high limit: 1.000215V

- %PASS Slot 17 channel 3 source accuracy at 1.5V on 3V range Measured: 1.500015V low limit: 1.49982V high limit: 1.50018V
- %PASS Slot 17 channel 3 acquire accuracy at 1.500015791V on 3V range Measured: 1.500020V low limit: 1.499835V high limit: 1.500195V
- %PASS Slot 17 channel 3 source accuracy at 2V on 3V range Measured: 2.000054V low limit: 1.99982V high limit: 2.00018V
- %PASS Slot 17 channel 3 acquire accuracy at 2.000054958V on 3V range Measured: 2.000072V low limit: 1.999874V high limit: 2.000234V
- %PASS Slot 17 channel 3 source accuracy at 2.5V on 3V range Measured: 2.500022V low limit: 2.49982V high limit: 2.50018V
- %PASS Slot 17 channel 3 acquire accuracy at 2.500022494V on 3V range Measured: 2.500021V low limit: 2.499842V high limit: 2.500202V
- %PASS Slot 17 channel 3 source accuracy at 3V on 3V range Measured: 3.000010V low limit: 2.99982V high limit: 3.00018V
- %PASS Slot 17 channel 3 acquire accuracy at 3.000010522V on 3V range Measured: 3.000037V low limit: 2.999830V high limit: 3.000190V
- %PASS Slot 17 channel 3 source accuracy at 0V on 6V range Measured: 0.00003848804V low limit: -0.00036V high limit: 0.00036V
- %PASS Slot 17 channel 3 acquire accuracy at 0.00003848804252V on 6V range Measured: 1.198166E-04V low limit: -0.0003215119V high limit: 0.0003984880V
- %PASS Slot 17 channel 3 source accuracy at 1V on 6V range Measured: 1.000125V low limit: 0.99964V high limit: 1.00036V
- %PASS Slot 17 channel 3 acquire accuracy at 1.000125446V on 6V range Measured: 1.000301V low limit: 0.9997654V high limit: 1.000485V

- %PASS Slot 17 channel 3 source accuracy at 2V on 6V range Measured: 2.000085V low limit: 1.99964V high limit: 2.00036V
- %PASS Slot 17 channel 3 acquire accuracy at 2.000085432V on 6V range Measured: 2.000162V low limit: 1.999725V high limit: 2.000445V
- %PASS Slot 17 channel 3 source accuracy at 3V on 6V range Measured: 3.000078V low limit: 2.99964V high limit: 3.00036V
- %PASS Slot 17 channel 3 acquire accuracy at 3.000078781V on 6V range Measured: 3.000083V low limit: 2.999718V high limit: 3.000438V
- %PASS Slot 17 channel 3 source accuracy at 4V on 6V range Measured: 4.000169V low limit: 3.99964V high limit: 4.00036V
- %PASS Slot 17 channel 3 acquire accuracy at 4.000169944V on 6V range Measured: 4.000182V low limit: 3.999809V high limit: 4.000529V
- %PASS Slot 17 channel 3 source accuracy at 5V on 6V range Measured: 5.000022V low limit: 4.99964V high limit: 5.00036V
- %PASS Slot 17 channel 3 acquire accuracy at 5.000022571V on 6V range Measured: 4.999955V low limit: 4.999662V high limit: 5.000382V
- %PASS Slot 17 channel 3 source accuracy at 6V on 6V range Measured: 5.999966V low limit: 5.99964V high limit: 6.00036V
- %PASS Slot 17 channel 3 acquire accuracy at 5.999966663V on 6V range Measured: 5.999899V low limit: 5.999606V high limit: 6.000326V
- %PASS Slot 17 channel 4 source accuracy at 0V on 3V range Measured: -0.00001471952V low limit: -0.00018V high limit: 0.00018V
- %PASS Slot 17 channel 4 acquire accuracy at -0.00001471952919V on 3V range Measured: -3.202137E-06V low limit: -0.0001947195V high limit: 0.0001652804V
- %PASS Slot 17 channel 4 source accuracy at 0.5V on 3V range

- Measured: 0.4999890V low limit: 0.49982V high limit: 0.50018V
- %PASS Slot 17 channel 4 acquire accuracy at 0.4999890778V on 3V range Measured: 0.5000087V low limit: 0.4998090V high limit: 0.5001690V
- %PASS Slot 17 channel 4 source accuracy at 1V on 3V range Measured: 0.9999818V low limit: 0.99982V high limit: 1.00018V
- %PASS Slot 17 channel 4 acquire accuracy at 0.9999818265V on 3V range Measured: 1.000017V low limit: 0.9998018V high limit: 1.000161V
- %PASS Slot 17 channel 4 source accuracy at 1.5V on 3V range Measured: 1.499981V low limit: 1.49982V high limit: 1.50018V
- %PASS Slot 17 channel 4 acquire accuracy at 1.499981333V on 3V range Measured: 1.499961V low limit: 1.499801V high limit: 1.500161V
- %PASS Slot 17 channel 4 source accuracy at 2V on 3V range Measured: 2.000007V low limit: 1.99982V high limit: 2.00018V
- %PASS Slot 17 channel 4 acquire accuracy at 2.000007584V on 3V range Measured: 1.999997V low limit: 1.999827V high limit: 2.000187V
- %PASS Slot 17 channel 4 source accuracy at 2.5V on 3V range Measured: 2.499985V low limit: 2.49982V high limit: 2.50018V
- %PASS Slot 17 channel 4 acquire accuracy at 2.499985759V on 3V range Measured: 2.499926V low limit: 2.499805V high limit: 2.500165V
- %PASS Slot 17 channel 4 source accuracy at 3V on 3V range Measured: 2.999971V low limit: 2.99982V high limit: 3.00018V
- %PASS Slot 17 channel 4 acquire accuracy at 2.999971335V on 3V range Measured: 2.999947V low limit: 2.999791V high limit: 3.000151V
- %PASS Slot 17 channel 4 source accuracy at 0V on 6V range Measured: 0.00001370538V low limit: -0.00036V high limit: 0.00036V

- %PASS Slot 17 channel 4 acquire accuracy at 0.00001370538496V on 6V range Measured: 1.161670E-05V low limit: -0.0003462946V high limit: 0.0003737053V
- %PASS Slot 17 channel 4 source accuracy at 1V on 6V range Measured: 1.000069V low limit: 0.99964V high limit: 1.00036V
- %PASS Slot 17 channel 4 acquire accuracy at 1.000069972V on 6V range Measured: 1.000141V low limit: 0.9997099V high limit: 1.000429V
- %PASS Slot 17 channel 4 source accuracy at 2V on 6V range Measured: 2.000000V low limit: 1.99964V high limit: 2.00036V
- %PASS Slot 17 channel 4 acquire accuracy at 2.000000666V on 6V range Measured: 2.000117V low limit: 1.999640V high limit: 2.000360V
- %PASS Slot 17 channel 4 source accuracy at 3V on 6V range Measured: 3.000043V low limit: 2.99964V high limit: 3.00036V
- %PASS Slot 17 channel 4 acquire accuracy at 3.000043885V on 6V range Measured: 2.999998V low limit: 2.999683V high limit: 3.000403V
- %PASS Slot 17 channel 4 source accuracy at 4V on 6V range Measured: 4.000130V low limit: 3.99964V high limit: 4.00036V
- %PASS Slot 17 channel 4 acquire accuracy at 4.000130976V on 6V range Measured: 4.000080V low limit: 3.999770V high limit: 4.000490V
- %PASS Slot 17 channel 4 source accuracy at 5V on 6V range Measured: 5.000019V low limit: 4.99964V high limit: 5.00036V
- %PASS Slot 17 channel 4 acquire accuracy at 5.000019681V on 6V range Measured: 4.999926V low limit: 4.999659V high limit: 5.000379V
- %PASS Slot 17 channel 4 source accuracy at 6V on 6V range Measured: 6.000029V low limit: 5.99964V high limit: 6.00036V
- %PASS Slot 17 channel 4 acquire accuracy at 6.000029056V on 6V range

- Measured: 6.000050V low limit: 5.999669V high limit: 6.000389V
- %PASS Slot 17 channel 5 source accuracy at 0V on 3V range
 Measured: -0.00001569911V low limit: -0.00018V high limit: 0.00018V
- %PASS Slot 17 channel 5 acquire accuracy at -0.00001569911027V on 3V range Measured: -2.072103E-05V low limit: -0.0001956991V high limit: 0.0001643008V
- %PASS Slot 17 channel 5 source accuracy at 0.5V on 3V range Measured: 0.4999804V low limit: 0.49982V high limit: 0.50018V
- %PASS Slot 17 channel 5 acquire accuracy at 0.4999804263V on 3V range Measured: 0.5000249V low limit: 0.4998004V high limit: 0.5001604V
- %PASS Slot 17 channel 5 source accuracy at 1V on 3V range Measured: 1.000020V low limit: 0.99982V high limit: 1.00018V
- %PASS Slot 17 channel 5 acquire accuracy at 1.000020277V on 3V range Measured: 1.000052V low limit: 0.9998402V high limit: 1.000200V
- %PASS Slot 17 channel 5 source accuracy at 1.5V on 3V range Measured: 1.500005V low limit: 1.49982V high limit: 1.50018V
- %PASS Slot 17 channel 5 acquire accuracy at 1.500005764V on 3V range Measured: 1.499980V low limit: 1.499825V high limit: 1.500185V
- %PASS Slot 17 channel 5 source accuracy at 2V on 3V range Measured: 2.000021V low limit: 1.99982V high limit: 2.00018V
- %PASS Slot 17 channel 5 acquire accuracy at 2.000021507V on 3V range Measured: 2.000011V low limit: 1.999841V high limit: 2.000201V
- %PASS Slot 17 channel 5 source accuracy at 2.5V on 3V range Measured: 2.500037V low limit: 2.49982V high limit: 2.50018V
- %PASS Slot 17 channel 5 acquire accuracy at 2.50003795V on 3V range Measured: 2.500012V low limit: 2.499857V high limit: 2.500217V

- %PASS Slot 17 channel 5 source accuracy at 3V on 3V range Measured: 3.000012V low limit: 2.99982V high limit: 3.00018V
- %PASS Slot 17 channel 5 acquire accuracy at 3.000012755V on 3V range Measured: 3.000016V low limit: 2.999832V high limit: 3.000192V
- %PASS Slot 17 channel 5 source accuracy at 0V on 6V range
 Measured: -0.00002519216V low limit: -0.00036V high limit: 0.00036V
- %PASS Slot 17 channel 5 acquire accuracy at -0.00002519216533V on 6V range Measured: 5.821263E-05V low limit: -0.0003851921V high limit: 0.0003348078V
- %PASS Slot 17 channel 5 source accuracy at 1V on 6V range Measured: 0.9999834V low limit: 0.99964V high limit: 1.00036V
- %PASS Slot 17 channel 5 acquire accuracy at 0.9999834779V on 6V range Measured: 1.000121V low limit: 0.9996234V high limit: 1.000343V
- %PASS Slot 17 channel 5 source accuracy at 2V on 6V range Measured: 1.999983V low limit: 1.99964V high limit: 2.00036V
- %PASS Slot 17 channel 5 acquire accuracy at 1.999983896V on 6V range Measured: 2.000143V low limit: 1.999623V high limit: 2.000343V
- %PASS Slot 17 channel 5 source accuracy at 3V on 6V range Measured: 2.999972V low limit: 2.99964V high limit: 3.00036V
- %PASS Slot 17 channel 5 acquire accuracy at 2.999972386V on 6V range Measured: 2.999952V low limit: 2.999612V high limit: 3.000332V
- %PASS Slot 17 channel 5 source accuracy at 4V on 6V range Measured: 4.000015V low limit: 3.99964V high limit: 4.00036V
- %PASS Slot 17 channel 5 acquire accuracy at 4.00001508V on 6V range Measured: 4.000028V low limit: 3.999655V high limit: 4.000375V

- %PASS Slot 17 channel 5 source accuracy at 5V on 6V range Measured: 4.999956V low limit: 4.99964V high limit: 5.00036V
- %PASS Slot 17 channel 5 acquire accuracy at 4.999956326V on 6V range Measured: 4.999884V low limit: 4.999596V high limit: 5.000316V
- %PASS Slot 17 channel 5 source accuracy at 6V on 6V range Measured: 5.999908V low limit: 5.99964V high limit: 6.00036V
- %PASS Slot 17 channel 5 acquire accuracy at 5.999908386V on 6V range Measured: 5.999936V low limit: 5.999548V high limit: 6.000268V
- %PASS Slot 17 channel 6 source accuracy at 0V on 3V range Measured: 0.00004627525V low limit: -0.00018V high limit: 0.00018V
- %PASS Slot 17 channel 6 acquire accuracy at 0.00004627525273V on 3V range Measured: 6.931313E-05V low limit: -0.0001337247V high limit: 0.0002262752V
- %PASS Slot 17 channel 6 source accuracy at 0.5V on 3V range Measured: 0.5000339V low limit: 0.49982V high limit: 0.50018V
- %PASS Slot 17 channel 6 acquire accuracy at 0.5000339431V on 3V range Measured: 0.5000984V low limit: 0.4998539V high limit: 0.5002139V
- %PASS Slot 17 channel 6 source accuracy at 1V on 3V range Measured: 1.000054V low limit: 0.99982V high limit: 1.00018V
- %PASS Slot 17 channel 6 acquire accuracy at 1.000054253V on 3V range Measured: 1.000106V low limit: 0.9998742V high limit: 1.000234V
- %PASS Slot 17 channel 6 source accuracy at 1.5V on 3V range Measured: 1.500030V low limit: 1.49982V high limit: 1.50018V
- %PASS Slot 17 channel 6 acquire accuracy at 1.50003059V on 3V range Measured: 1.500047V low limit: 1.499850V high limit: 1.500210V
- %PASS Slot 17 channel 6 source accuracy at 2V on 3V range

- Measured: 2.000018V low limit: 1.99982V high limit: 2.00018V
- %PASS Slot 17 channel 6 acquire accuracy at 2.000018398V on 3V range Measured: 2.000017V low limit: 1.999838V high limit: 2.000198V
- %PASS Slot 17 channel 6 source accuracy at 2.5V on 3V range Measured: 2.500012V low limit: 2.49982V high limit: 2.50018V
- %PASS Slot 17 channel 6 acquire accuracy at 2.500012949V on 3V range Measured: 2.500001V low limit: 2.499832V high limit: 2.500192V
- %PASS Slot 17 channel 6 source accuracy at 3V on 3V range Measured: 2.999968V low limit: 2.99982V high limit: 3.00018V
- %PASS Slot 17 channel 6 acquire accuracy at 2.999968489V on 3V range Measured: 2.999986V low limit: 2.999788V high limit: 3.000148V
- %PASS Slot 17 channel 6 source accuracy at 0V on 6V range Measured: 0.00003743058V low limit: -0.00036V high limit: 0.00036V
- %PASS Slot 17 channel 6 acquire accuracy at 0.00003743058496V on 6V range Measured: 5.270300E-05V low limit: -0.0003225694V high limit: 0.0003974305V
- %PASS Slot 17 channel 6 source accuracy at 1V on 6V range Measured: 1.000056V low limit: 0.99964V high limit: 1.00036V
- %PASS Slot 17 channel 6 acquire accuracy at 1.000056267V on 6V range Measured: 1.000218V low limit: 0.9996962V high limit: 1.000416V
- %PASS Slot 17 channel 6 source accuracy at 2V on 6V range Measured: 2.000043V low limit: 1.99964V high limit: 2.00036V
- %PASS Slot 17 channel 6 acquire accuracy at 2.000043793V on 6V range Measured: 2.000111V low limit: 1.999683V high limit: 2.000403V
- %PASS Slot 17 channel 6 source accuracy at 3V on 6V range Measured: 3.000034V low limit: 2.99964V high limit: 3.00036V

- %PASS Slot 17 channel 6 acquire accuracy at 3.000034121V on 6V range Measured: 3.000017V low limit: 2.999674V high limit: 3.000394V
- %PASS Slot 17 channel 6 source accuracy at 4V on 6V range Measured: 4.000143V low limit: 3.99964V high limit: 4.00036V
- %PASS Slot 17 channel 6 acquire accuracy at 4.000143148V on 6V range Measured: 4.000159V low limit: 3.999783V high limit: 4.000503V
- %PASS Slot 17 channel 6 source accuracy at 5V on 6V range Measured: 5.000056V low limit: 4.99964V high limit: 5.00036V
- %PASS Slot 17 channel 6 acquire accuracy at 5.000056373V on 6V range Measured: 5.000064V low limit: 4.999696V high limit: 5.000416V
- %PASS Slot 17 channel 6 source accuracy at 6V on 6V range Measured: 5.999994V low limit: 5.99964V high limit: 6.00036V
- %PASS Slot 17 channel 6 acquire accuracy at 5.999994685V on 6V range Measured: 6.000087V low limit: 5.999634V high limit: 6.000354V
- %PASS Slot 17 channel 7 source accuracy at 0V on 3V range Measured: 0.00001513472V low limit: -0.00018V high limit: 0.00018V
- %PASS Slot 17 channel 7 acquire accuracy at 0.00001513472457V on 3V range Measured: 6.128672E-05V low limit: -0.0001648652V high limit: 0.0001951347V
- %PASS Slot 17 channel 7 source accuracy at 0.5V on 3V range Measured: 0.5000110V low limit: 0.49982V high limit: 0.50018V
- %PASS Slot 17 channel 7 acquire accuracy at 0.5000110768V on 3V range Measured: 0.5001110V low limit: 0.4998310V high limit: 0.5001910V
- %PASS Slot 17 channel 7 source accuracy at 1V on 3V range Measured: 1.000041V low limit: 0.99982V high limit: 1.00018V

- %PASS Slot 17 channel 7 acquire accuracy at 1.000041906V on 3V range Measured: 1.000143V low limit: 0.9998619V high limit: 1.000221V
- %PASS Slot 17 channel 7 source accuracy at 1.5V on 3V range Measured: 1.500019V low limit: 1.49982V high limit: 1.50018V
- %PASS Slot 17 channel 7 acquire accuracy at 1.500019118V on 3V range Measured: 1.500029V low limit: 1.499839V high limit: 1.500199V
- %PASS Slot 17 channel 7 source accuracy at 2V on 3V range Measured: 2.000072V low limit: 1.99982V high limit: 2.00018V
- %PASS Slot 17 channel 7 acquire accuracy at 2.000072559V on 3V range Measured: 2.000109V low limit: 1.999892V high limit: 2.000252V
- %PASS Slot 17 channel 7 source accuracy at 2.5V on 3V range Measured: 2.500073V low limit: 2.49982V high limit: 2.50018V
- %PASS Slot 17 channel 7 acquire accuracy at 2.50007324V on 3V range Measured: 2.500090V low limit: 2.499893V high limit: 2.500253V
- %PASS Slot 17 channel 7 source accuracy at 3V on 3V range Measured: 3.000038V low limit: 2.99982V high limit: 3.00018V
- %PASS Slot 17 channel 7 acquire accuracy at 3.000038325V on 3V range Measured: 3.000066V low limit: 2.999858V high limit: 3.000218V
- %PASS Slot 17 channel 7 source accuracy at 0V on 6V range Measured: 0.00001302374V low limit: -0.00036V high limit: 0.00036V
- %PASS Slot 17 channel 7 acquire accuracy at 0.00001302374703V on 6V range Measured: 1.348574E-05V low limit: -0.0003469762V high limit: 0.0003730237V
- %PASS Slot 17 channel 7 source accuracy at 1V on 6V range Measured: 1.000008V low limit: 0.99964V high limit: 1.00036V
- %PASS Slot 17 channel 7 acquire accuracy at 1.000008849V on 6V range

- Measured: 1.000175V low limit: 0.9996488V high limit: 1.000368V
- %PASS Slot 17 channel 7 source accuracy at 2V on 6V range Measured: 2.000076V low limit: 1.99964V high limit: 2.00036V
- %PASS Slot 17 channel 7 acquire accuracy at 2.000076281V on 6V range Measured: 2.000195V low limit: 1.999716V high limit: 2.000436V
- %PASS Slot 17 channel 7 source accuracy at 3V on 6V range Measured: 3.000041V low limit: 2.99964V high limit: 3.00036V
- %PASS Slot 17 channel 7 acquire accuracy at 3.000041039V on 6V range Measured: 3.000038V low limit: 2.999681V high limit: 3.000401V
- %PASS Slot 17 channel 7 source accuracy at 4V on 6V range Measured: 4.000133V low limit: 3.99964V high limit: 4.00036V
- %PASS Slot 17 channel 7 acquire accuracy at 4.000133384V on 6V range Measured: 4.000099V low limit: 3.999773V high limit: 4.000493V
- %PASS Slot 17 channel 7 source accuracy at 5V on 6V range Measured: 5.000123V low limit: 4.99964V high limit: 5.00036V
- %PASS Slot 17 channel 7 acquire accuracy at 5.000123581V on 6V range Measured: 5.000107V low limit: 4.999763V high limit: 5.000483V
- %PASS Slot 17 channel 7 source accuracy at 6V on 6V range Measured: 6.000054V low limit: 5.99964V high limit: 6.00036V
- %PASS Slot 17 channel 7 acquire accuracy at 6.000054888V on 6V range Measured: 6.000145V low limit: 5.999694V high limit: 6.000414V
- Performing source linearity verification...
- %PASS Slot 17 channel 0 3V Source offset in mV Measured: 0.3683 low limit: -50 high limit: 50
- %PASS Slot 17 channel 0 3V Source gain in mV Measured: 0.9994 low limit: 0.98 high limit: 1.02

- %PASS Slot 17 channel 1 3V Source offset in mV Measured: -0.4489 low limit: -50 high limit: 50
- %PASS Slot 17 channel 1 3V Source gain in mV Measured: 0.9999 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 2 3V Source offset in mV Measured: 0.7818 low limit: -50 high limit: 50
- %PASS Slot 17 channel 2 3V Source gain in mV Measured: 0.9994 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 3 3V Source offset in mV Measured: -1.349E-03 low limit: -50 high limit: 50
- %PASS Slot 17 channel 3 3V Source gain in mV Measured: 1.000 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 4 3V Source offset in mV Measured: 1.121 low limit: -50 high limit: 50
- %PASS Slot 17 channel 4 3V Source gain in mV Measured: 0.9998 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 5 3V Source offset in mV Measured: 0.5044 low limit: -50 high limit: 50
- %PASS Slot 17 channel 5 3V Source gain in mV Measured: 0.9998 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 6 3V Source offset in mV Measured: -4.873E-02 low limit: -50 high limit: 50
- %PASS Slot 17 channel 6 3V Source gain in mV Measured: 0.9997 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 7 3V Source offset in mV

- Measured: 0.5367 low limit: -50 high limit: 50
- %PASS Slot 17 channel 7 3V Source gain in mV Measured: 0.9994 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 0 6V Source offset in mV Measured: 0.9137 low limit: -100 high limit: 100
- %PASS Slot 17 channel 0 6V Source gain in mV Measured: 0.9992 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 1 6V Source offset in mV Measured: -5.010E-02 low limit: -100 high limit: 100
- %PASS Slot 17 channel 1 6V Source gain in mV Measured: 0.9997 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 2 6V Source offset in mV Measured: 1.771 low limit: -100 high limit: 100
- %PASS Slot 17 channel 2 6V Source gain in mV Measured: 0.9990 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 3 6V Source offset in mV Measured: 0.5978 low limit: -100 high limit: 100
- %PASS Slot 17 channel 3 6V Source gain in mV Measured: 0.9996 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 4 6V Source offset in mV Measured: 2.280 low limit: -100 high limit: 100
- %PASS Slot 17 channel 4 6V Source gain in mV Measured: 0.9997 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 5 6V Source offset in mV Measured: 1,296 low limit: -100 high limit: 100

- %PASS Slot 17 channel 5 6V Source gain in mV Measured: 0.9996 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 6 6V Source offset in mV Measured: 0.1213 low limit: -100 high limit: 100
- %PASS Slot 17 channel 6 6V Source gain in mV Measured: 0.9996 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 7 6V Source offset in mV Measured: 1.325 low limit: -100 high limit: 100
- %PASS Slot 17 channel 7 6V Source gain in mV Measured: 0.9992 low limit: 0.98 high limit: 1.02
- %PASS Slot 17 channel 0 linearity at $\,$ 0V on 3V range Measured: 0.000008287719V low limit: -6.292972E-05V high limit: 1.170702E-04V
- %PASS Slot 17 channel 0 linearity at 0.25V on 3V range Measured: 0.2499820V low limit: 0.2499347V high limit: 0.2501147V
- %PASS Slot 17 channel 0 linearity at $\,$.375V on 3V range Measured: 0.3750369V low limit: 0.3749335V high limit: 0.3751135V
- %PASS Slot 17 channel 0 linearity at .5V on 3V range Measured: 0.5000094V low limit: 0.4999323V high limit: 0.5001123V
- %PASS Slot 17 channel 0 linearity at .625V on 3V range Measured: 0.6250042V low limit: 0.6249311V high limit: 0.6251111V
- %PASS Slot 17 channel 0 linearity at .75V on 3V range Measured: 0.7500227V low limit: 0.7499300V high limit: 0.7501100V
- %PASS Slot 17 channel 0 linearity at .875V on 3V range

- Measured: 0.8750420V low limit: 0.8749288V high limit: 0.8751088V
- %PASS Slot 17 channel 0 linearity at 1V on 3V range
 Measured: 1.000038V low limit: 0.9999276V high limit: 1.000107V
- %PASS Slot 17 channel 0 linearity at 1.125V on 3V range
 Measured: 1.125038V low limit: 1.124926V high limit: 1.125106V
- %PASS Slot 17 channel 0 linearity at 1.25V on 3V range Measured: 1.249996V low limit: 1.249925V high limit: 1.250105V
- %PASS Slot 17 channel 0 linearity at 1.375V on 3V range
 Measured: 1.375053V low limit: 1.374924V high limit: 1.375104V
- %PASS Slot 17 channel 0 linearity at 1.5V on 3V range Measured: 1.499992V low limit: 1.499922V high limit: 1.500102V
- %PASS Slot 17 channel 0 linearity at 1.625V on 3V range Measured: 1.625015V low limit: 1.624921V high limit: 1.625101V
- %PASS Slot 17 channel 0 linearity at 1.75V on 3V range
 Measured: 1.750061V low limit: 1.749920V high limit: 1.750100V
- %PASS Slot 17 channel 0 linearity at 1.875V on 3V range Measured: 1.875009V low limit: 1.874919V high limit: 1.875099V
- %PASS Slot 17 channel 0 linearity at 2V on 3V range Measured: 2.000014V low limit: 1.999918V high limit: 2.000098V
- %PASS Slot 17 channel 0 linearity at 2.125V on 3V range Measured: 2.125005V low limit: 2.124917V high limit: 2.125097V
- %PASS Slot 17 channel 0 linearity at 2.25V on 3V range
 Measured: 2.250001V low limit: 2.249915V high limit: 2.250095V
- %PASS Slot 17 channel 0 linearity at 2.375V on 3V range Measured: 2.375017V low limit: 2.374914V high limit: 2.375094V

- %PASS Slot 17 channel 0 linearity at 2.5V on 3V range
 Measured: 2.500013V low limit: 2.499913V high limit: 2.500093V
- %PASS Slot 17 channel 0 linearity at 2.625V on 3V range Measured: 2.624976V low limit: 2.624912V high limit: 2.625092V
- %PASS Slot 17 channel 0 linearity at 2.75V on 3V range Measured: 2.750011V low limit: 2.749911V high limit: 2.750091V
- %PASS Slot 17 channel 0 linearity at 2.875V on 3V range Measured: 2.874969V low limit: 2.874909V high limit: 2.875089V
- %PASS Slot 17 channel 0 linearity at 3V on 3V range Measured: 2.999975V low limit: 2.999908V high limit: 3.000088V
- %PASS Slot 17 channel 0 maximum linearity error on 3V range Measured: 5.128247E-05V high limit: 0.00009V
- %PASS Slot 17 channel 0 linearity at $\,$ 0V on 6V range $\,$ Measured: -0.0001021695V low limit: -1.920128E-04V high limit: 1.679871E-04V $\,$
- %PASS Slot 17 channel 0 linearity at 0.25V on 6V range Measured: 0.2499965V low limit: 0.2498112V high limit: 0.2501712V
- %PASS Slot 17 channel 0 linearity at 0.5V on 6V range Measured: 0.4999530V low limit: 0.4998145V high limit: 0.5001745V
- %PASS Slot 17 channel 0 linearity at .75V on 6V range Measured: 0.7500218V low limit: 0.7498177V high limit: 0.7501777V
- %PASS Slot 17 channel 0 linearity at 1V on 6V range
 Measured: 1.000014V low limit: 0.9998210V high limit: 1.000181V
- %PASS Slot 17 channel 0 linearity at 1.25V on 6V range Measured: 1.249958V low limit: 1.249824V high limit: 1.250184V
- %PASS Slot 17 channel 0 linearity at 1.5V on 6V range

- Measured: 1.499954V low limit: 1.499827V high limit: 1.500187V
- %PASS Slot 17 channel 0 linearity at 1.75V on 6V range Measured: 1.750044V low limit: 1.749830V high limit: 1.750190V
- %PASS Slot 17 channel 0 linearity at 2V on 6V range Measured: 1.999994V low limit: 1.999834V high limit: 2.000194V
- %PASS Slot 17 channel 0 linearity at 2.25V on 6V range
 Measured: 2.250024V low limit: 2.249837V high limit: 2.250197V
- %PASS Slot 17 channel 0 linearity at 2.5V on 6V range Measured: 2.500018V low limit: 2.499840V high limit: 2.500200V
- %PASS Slot 17 channel 0 linearity at 2.75V on 6V range Measured: 2.750070V low limit: 2.749843V high limit: 2.750203V
- %PASS Slot 17 channel 0 linearity at 3V on 6V range Measured: 3.000016V low limit: 2.999847V high limit: 3.000207V
- %PASS Slot 17 channel 0 linearity at 3.25V on 6V range Measured: 3.250094V low limit: 3.249850V high limit: 3.250210V
- %PASS Slot 17 channel 0 linearity at 3.5V on 6V range Measured: 3.500138V low limit: 3.499853V high limit: 3.500213V
- %PASS Slot 17 channel 0 linearity at 3.75V on 6V range Measured: 3.750089V low limit: 3.749856V high limit: 3.750216V
- %PASS Slot 17 channel 0 linearity at 4V on 6V range Measured: 4.000120V low limit: 3.999860V high limit: 4.000220V
- %PASS Slot 17 channel 0 linearity at 4.25V on 6V range Measured: 4.250068V low limit: 4.249863V high limit: 4.250223V
- %PASS Slot 17 channel 0 linearity at 4.5V on 6V range Measured: 4.500123V low limit: 4.499866V high limit: 4.500226V

- %PASS Slot 17 channel 0 linearity at 4.75V on 6V range Measured: 4.750088V low limit: 4.749870V high limit: 4.750230V
- %PASS Slot 17 channel 0 linearity at 5V on 6V range Measured: 5.000027V low limit: 4.999873V high limit: 5.000233V
- %PASS Slot 17 channel 0 linearity at 5.25V on 6V range Measured: 5.249991V low limit: 5.249876V high limit: 5.250236V
- %PASS Slot 17 channel 0 linearity at 5.5V on 6V range Measured: 5.500015V low limit: 5.499879V high limit: 5.500239V
- %PASS Slot 17 channel 0 linearity at 5.75V on 6V range Measured: 5.749951V low limit: 5.749883V high limit: 5.750243V
- %PASS Slot 17 channel 0 linearity at 6V on 6V range Measured: 6.000004V low limit: 5.999886V high limit: 6.000246V
- %PASS Slot 17 channel 0 maximum linearity error on 6V range Measured: 1.112129E-04V high limit: 0.00018V
- %PASS Slot 17 channel 1 linearity at 0V on 3V range

 Measured: -0.000004820903V low limit: -7.075177E-05V high limit:
 1.092482E-04V
- %PASS Slot 17 channel 1 linearity at .25V on 3V range Measured: 0.2500021V low limit: 0.2499294V high limit: 0.2501094V
- %PASS Slot 17 channel 1 linearity at 375V on 3V range Measured: 0.3750288V low limit: 0.3749295V high limit: 0.3751095V
- %PASS Slot 17 channel 1 linearity at 0.5V on 3V range Measured: 0.5000134V low limit: 0.4999295V high limit: 0.5001095V
- %PASS Slot 17 channel 1 linearity at .625V on 3V range

- Measured: 0.6250230V low limit: 0.6249296V high limit: 0.6251096V
- %PASS Slot 17 channel 1 linearity at .75V on 3V range Measured: 0.7500058V low limit: 0.7499297V high limit: 0.7501097V
- %PASS Slot 17 channel 1 linearity at $\,$.875V on 3V range Measured: 0.8750347V low limit: 0.8749298V high limit: 0.8751098V
- %PASS Slot 17 channel 1 linearity at 1V on 3V range
 Measured: 1.000001V low limit: 0.9999299V high limit: 1.000109V
- %PASS Slot 17 channel 1 linearity at 1.125V on 3V range
 Measured: 1.125023V low limit: 1.124930V high limit: 1.125110V
- %PASS Slot 17 channel 1 linearity at 1.25V on 3V range
 Measured: 1.250001V low limit: 1.249930V high limit: 1.250110V
- %PASS Slot 17 channel 1 linearity at 1.375V on 3V range Measured: 1.375018V low limit: 1.374930V high limit: 1.375110V
- %PASS Slot 17 channel 1 linearity at 1.5V on 3V range
 Measured: 1.499982V low limit: 1.499930V high limit: 1.500110V
- %PASS Slot 17 channel 1 linearity at 1.625V on 3V range Measured: 1.625062V low limit: 1.624930V high limit: 1.625110V
- %PASS Slot 17 channel 1 linearity at 1.75V on 3V range Measured: 1.750066V low limit: 1.749930V high limit: 1.750110V
- %PASS Slot 17 channel 1 linearity at 1.875V on 3V range Measured: 1.875042V low limit: 1.874930V high limit: 1.875110V
- %PASS Slot 17 channel 1 linearity at 2V on 3V range
 Measured: 2.000068V low limit: 1.999930V high limit: 2.000110V
- %PASS Slot 17 channel 1 linearity at 2.125V on 3V range
 Measured: 2.125030V low limit: 2.124930V high limit: 2.125110V

- %PASS Slot 17 channel 1 linearity at 2.25V on 3V range
 Measured: 2.250044V low limit: 2.249930V high limit: 2.250110V
- %PASS Slot 17 channel 1 linearity at 2.375V on 3V range Measured: 2.375015V low limit: 2.374930V high limit: 2.375110V
- %PASS Slot 17 channel 1 linearity at 2.5V on 3V range Measured: 2.500028V low limit: 2.499930V high limit: 2.500110V
- %PASS Slot 17 channel 1 linearity at 2.625V on 3V range Measured: 2.625006V low limit: 2.624931V high limit: 2.625111V
- %PASS Slot 17 channel 1 linearity at 2.75V on 3V range Measured: 2.750005V low limit: 2.749931V high limit: 2.750111V
- %PASS Slot 17 channel 1 linearity at 2.875V on 3V range Measured: 2.874977V low limit: 2.874931V high limit: 2.875111V
- %PASS Slot 17 channel 1 linearity at 3V on 3V range Measured: 2.999999V low limit: 2.999931V high limit: 3.000111V
- %PASS Slot 17 channel 1 maximum linearity error on 3V range Measured: 4.823372E-05V high limit: 0.00009V
- %PASS Slot 17 channel 1 linearity at $\,$ 0V on 6V range Measured: 0.00007755534V low limit: -8.073308E-06V high limit: 3.519266E-04V
- %PASS Slot 17 channel 1 linearity at .25V on 6V range Measured: 0.2501765V low limit: 0.2499896V high limit: 0.2503496V
- %PASS Slot 17 channel 1 linearity at $\,$.5V on 6V range Measured: 0.5001542V low limit: 0.4999873V high limit: 0.5003473V
- %PASS Slot 17 channel 1 linearity at .75V on 6V range Measured: 0.7502130V low limit: 0.7499851V high limit: 0.7503451V
- %PASS Slot 17 channel 1 linearity at 1V on 6V range

- Measured: 1.000098V low limit: 0.9999828V high limit: 1.000342V
- %PASS Slot 17 channel 1 linearity at 1.25V on 6V range Measured: 1.250163V low limit: 1.249980V high limit: 1.250340V
- %PASS Slot 17 channel 1 linearity at 1.5V on 6V range Measured: 1.500157V low limit: 1.499978V high limit: 1.500338V
- %PASS Slot 17 channel 1 linearity at 1.75V on 6V range
 Measured: 1.750238V low limit: 1.749976V high limit: 1.750336V
- %PASS Slot 17 channel 1 linearity at 2V on 6V range
 Measured: 2.000104V low limit: 1.999973V high limit: 2.000333V
- %PASS Slot 17 channel 1 linearity at 2.25V on 6V range
 Measured: 2.250170V low limit: 2.249971V high limit: 2.250331V
- %PASS Slot 17 channel 1 linearity at 2.5V on 6V range Measured: 2.500149V low limit: 2.499969V high limit: 2.500329V
- %PASS Slot 17 channel 1 linearity at 2.75V on 6V range Measured: 2.750203V low limit: 2.749966V high limit: 2.750326V
- %PASS Slot 17 channel 1 linearity at 3V on 6V range Measured: 3.000072V low limit: 2.999964V high limit: 3.000324V
- %PASS Slot 17 channel 1 linearity at 3.25V on 6V range Measured: 3.250162V low limit: 3.249962V high limit: 3.250322V
- %PASS Slot 17 channel 1 linearity at 3.5V on 6V range Measured: 3.500213V low limit: 3.499960V high limit: 3.500320V
- %PASS Slot 17 channel 1 linearity at 3.75V on 6V range Measured: 3.750173V low limit: 3.749957V high limit: 3.750317V
- %PASS Slot 17 channel 1 linearity at 4V on 6V range Measured: 4.000248V low limit: 3.999955V high limit: 4.000315V

- %PASS Slot 17 channel 1 linearity at 4.25V on 6V range Measured: 4.250100V low limit: 4.249953V high limit: 4.250313V
- %PASS Slot 17 channel 1 linearity at 4.5V on 6V range Measured: 4.500149V low limit: 4.499950V high limit: 4.500310V
- %PASS Slot 17 channel 1 linearity at 4.75V on 6V range Measured: 4.750125V low limit: 4.749948V high limit: 4.750308V
- %PASS Slot 17 channel 1 linearity at 5V on 6V range Measured: 5.000177V low limit: 4.999946V high limit: 5.000306V
- %PASS Slot 17 channel 1 linearity at 5.25V on 6V range Measured: 5.250044V low limit: 5.249944V high limit: 5.250304V
- %PASS Slot 17 channel 1 linearity at 5.5V on 6V range Measured: 5.500086V low limit: 5.499941V high limit: 5.500301V
- %PASS Slot 17 channel 1 linearity at 5.75V on 6V range Measured: 5.750039V low limit: 5.749939V high limit: 5.750299V
- %PASS Slot 17 channel 1 linearity at 6V on 6V range Measured: 6.000114V low limit: 5.999937V high limit: 6.000297V
- %PASS Slot 17 channel 1 maximum linearity error on 6V range Measured: 1.129144E-04V high limit: 0.00018V
- %PASS Slot 17 channel 2 linearity at 0V on 3V range
 Measured: -0.00001494572V low limit: -7.464807E-05V high limit:
 1.053519E-04V
- %PASS Slot 17 channel 2 linearity at 0.25V on 3V range Measured: 0.2499711V low limit: 0.2499284V high limit: 0.2501084V
- %PASS Slot 17 channel 2 linearity at 375V on 3V range

- Measured: 0.3750287V low limit: 0.3749299V high limit: 0.3751099V
- %PASS Slot 17 channel 2 linearity at .5V on 3V range Measured: 0.5000025V low limit: 0.4999314V high limit: 0.5001114V
- %PASS Slot 17 channel 2 linearity at $\,$.625V on 3V range $\label{eq:measured:0.6250593V} \mbox{ low limit: 0.6249330V high limit: 0.6251130V}$
- %PASS Slot 17 channel 2 linearity at .75V on 3V range Measured: 0.7500231V low limit: 0.7499345V high limit: 0.7501145V
- %PASS Slot 17 channel 2 linearity at .875V on 3V range Measured: 0.8750413V low limit: 0.8749360V high limit: 0.8751160V
- %PASS Slot 17 channel 2 linearity at 1V on 3V range Measured: 1.000039V low limit: 0.9999376V high limit: 1.000117V
- %PASS Slot 17 channel 2 linearity at 1.125V on 3V range
 Measured: 1.125037V low limit: 1.124939V high limit: 1.125119V
- %PASS Slot 17 channel 2 linearity at 1.25V on 3V range Measured: 1.250046V low limit: 1.249940V high limit: 1.250120V
- %PASS Slot 17 channel 2 linearity at 1.375V on 3V range Measured: 1.375048V low limit: 1.374942V high limit: 1.375122V
- %PASS Slot 17 channel 2 linearity at 1.5V on 3V range Measured: 1.500004V low limit: 1.499943V high limit: 1.500123V
- %PASS Slot 17 channel 2 linearity at 1.625V on 3V range Measured: 1.625063V low limit: 1.624945V high limit: 1.625125V
- %PASS Slot 17 channel 2 linearity at 1.75V on 3V range
 Measured: 1.750061V low limit: 1.749946V high limit: 1.750126V
- %PASS Slot 17 channel 2 linearity at 1.875V on 3V range Measured: 1.875062V low limit: 1.874948V high limit: 1.875128V

- %PASS Slot 17 channel 2 linearity at 2V on 3V range Measured: 2.000050V low limit: 1.999949V high limit: 2.000129V
- %PASS Slot 17 channel 2 linearity at 2.125V on 3V range
 Measured: 2.125049V low limit: 2.124951V high limit: 2.125131V
- %PASS Slot 17 channel 2 linearity at 2.25V on 3V range Measured: 2.250057V low limit: 2.249952V high limit: 2.250132V
- %PASS Slot 17 channel 2 linearity at 2.375V on 3V range Measured: 2.375013V low limit: 2.374954V high limit: 2.375134V
- %PASS Slot 17 channel 2 linearity at 2.5V on 3V range Measured: 2.500057V low limit: 2.499956V high limit: 2.500136V
- %PASS Slot 17 channel 2 linearity at 2.625V on 3V range Measured: 2.625021V low limit: 2.624957V high limit: 2.625137V
- %PASS Slot 17 channel 2 linearity at 2.75V on 3V range
 Measured: 2.750064V low limit: 2.749959V high limit: 2.750139V
- %PASS Slot 17 channel 2 linearity at 2.875V on 3V range
 Measured: 2.875018V low limit: 2.874960V high limit: 2.875140V
- %PASS Slot 17 channel 2 linearity at 3V on 3V range Measured: 3.000026V low limit: 2.999962V high limit: 3.000142V
- %PASS Slot 17 channel 2 maximum linearity error on 3V range Measured: 4.726487E-05V high limit: 0.00009V
- %PASS Slot 17 channel 2 linearity at 0V on 6V range

 Measured: -0.00009568306V low limit: -1.666716E-04V high limit:
 1.933283E-04V
- %PASS Slot 17 channel 2 linearity at $\,$.25V on 6V range $\label{eq:pass} \mbox{Measured: } 0.2499453 \mbox{V low limit: } 0.2498345 \mbox{V high limit: } 0.2501945 \mbox{V}$
- %PASS Slot 17 channel 2 linearity at .5V on 6V range

- Measured: 0.4999665V low limit: 0.4998356V high limit: 0.5001956V
- %PASS Slot 17 channel 2 linearity at $.75\mathrm{V}$ on 6V range Measured: $0.7499797\mathrm{V}$ low limit: $0.7498368\mathrm{V}$ high limit: $0.7501968\mathrm{V}$
- %PASS Slot 17 channel 2 linearity at 1V on 6V range
 Measured: 1.000019V low limit: 0.9998380V high limit: 1.000198V
- %PASS Slot 17 channel 2 linearity at 1.25V on 6V range Measured: 1.250024V low limit: 1.249839V high limit: 1.250199V
- %PASS Slot 17 channel 2 linearity at 1.5V on 6V range Measured: 1.500054V low limit: 1.499840V high limit: 1.500200V
- %PASS Slot 17 channel 2 linearity at 1.75V on 6V range
 Measured: 1.750081V low limit: 1.749841V high limit: 1.750201V
- %PASS Slot 17 channel 2 linearity at 2V on 6V range Measured: 2.000092V low limit: 1.999842V high limit: 2.000202V
- %PASS Slot 17 channel 2 linearity at 2.25V on 6V range Measured: 2.250065V low limit: 2.249843V high limit: 2.250203V
- %PASS Slot 17 channel 2 linearity at 2.5V on 6V range Measured: 2.500081V low limit: 2.499845V high limit: 2.500205V
- %PASS Slot 17 channel 2 linearity at 2.75V on 6V range Measured: 2.750081V low limit: 2.749846V high limit: 2.750206V
- %PASS Slot 17 channel 2 linearity at 3V on 6V range Measured: 2.999987V low limit: 2.999847V high limit: 3.000207V
- %PASS Slot 17 channel 2 linearity at 3.25V on 6V range Measured: 3.250089V low limit: 3.249848V high limit: 3.250208V
- %PASS Slot 17 channel 2 linearity at 3.5V on 6V range Measured: 3.500089V low limit: 3.499849V high limit: 3.500209V

- %PASS Slot 17 channel 2 linearity at 3.75V on 6V range Measured: 3.750081V low limit: 3.749851V high limit: 3.750211V
- %PASS Slot 17 channel 2 linearity at 4V on 6V range Measured: 4.000051V low limit: 3.999852V high limit: 4.000212V
- %PASS Slot 17 channel 2 linearity at 4.25V on 6V range Measured: 4.250033V low limit: 4.249853V high limit: 4.250213V
- %PASS Slot 17 channel 2 linearity at 4.5V on 6V range Measured: 4.500040V low limit: 4.499854V high limit: 4.500214V
- %PASS Slot 17 channel 2 linearity at 4.75V on 6V range Measured: 4.750027V low limit: 4.749855V high limit: 4.750215V
- %PASS Slot 17 channel 2 linearity at 5V on 6V range Measured: 5.000012V low limit: 4.999856V high limit: 5.000216V
- %PASS Slot 17 channel 2 linearity at 5.25V on 6V range Measured: 5.250010V low limit: 5.249858V high limit: 5.250218V
- %PASS Slot 17 channel 2 linearity at 5.5V on 6V range Measured: 5.499999V low limit: 5.499859V high limit: 5.500219V
- %PASS Slot 17 channel 2 linearity at 5.75V on 6V range Measured: 5.749979V low limit: 5.749860V high limit: 5.750220V
- %PASS Slot 17 channel 2 linearity at 6V on 6V range Measured: 5.999987V low limit: 5.999861V high limit: 6.000221V
- %PASS Slot 17 channel 2 maximum linearity error on 6V range Measured: 1.090114E-04V high limit: 0.00018V
- %PASS Slot 17 channel 3 linearity at $\,$ 0V on 3V range Measured: 0.00001419801V low limit: -4.251660E-05V high limit: 1.374833E-04V
- %PASS Slot 17 channel 3 linearity at .125V on 3V range

- Measured: 0.1250660V low limit: 0.1249559V high limit: 0.1251359V
- %PASS Slot 17 channel 3 linearity at $\,$.25V on 3V range $\mbox{Measured: } 0.2500049 \mbox{V low limit: } 0.2499545 \mbox{V high limit: } 0.2501345 \mbox{V}$
- %PASS Slot 17 channel 3 linearity at $.375\mathrm{V}$ on 3V range Measured: $0.3750387\mathrm{V}$ low limit: $0.3749530\mathrm{V}$ high limit: $0.3751330\mathrm{V}$
- %PASS Slot 17 channel 3 linearity at 0.5V on 3V range Measured: 0.5000347V low limit: 0.4999515V high limit: 0.5001315V
- %PASS Slot 17 channel 3 linearity at .625V on 3V range Measured: 0.6250655V low limit: 0.6249500V high limit: 0.6251300V
- %PASS Slot 17 channel 3 linearity at $.75\mathrm{V}$ on 3V range Measured: $0.7500128\mathrm{V}$ low limit: $0.7499485\mathrm{V}$ high limit: $0.7501285\mathrm{V}$
- %PASS Slot 17 channel 3 linearity at .875V on 3V range Measured: 0.8750544V low limit: 0.8749470V high limit: 0.8751270V
- %PASS Slot 17 channel 3 linearity at 1V on 3V range Measured: 1.000039V low limit: 0.9999456V high limit: 1.000125V
- %PASS Slot 17 channel 3 linearity at 1.125V on 3V range Measured: 1.125065V low limit: 1.124944V high limit: 1.125124V
- %PASS Slot 17 channel 3 linearity at 1.25V on 3V range Measured: 1.250009V low limit: 1.249942V high limit: 1.250122V
- %PASS Slot 17 channel 3 linearity at 1.375V on 3V range Measured: 1.375037V low limit: 1.374941V high limit: 1.375121V
- %PASS Slot 17 channel 3 linearity at 1.5V on 3V range Measured: 1.500015V low limit: 1.499939V high limit: 1.500119V
- %PASS Slot 17 channel 3 linearity at 1.625V on 3V range Measured: 1.625052V low limit: 1.624938V high limit: 1.625118V

- %PASS Slot 17 channel 3 linearity at 1.75V on 3V range
 Measured: 1.750082V low limit: 1.749936V high limit: 1.750116V
- %PASS Slot 17 channel 3 linearity at 1.875V on 3V range
 Measured: 1.875016V low limit: 1.874935V high limit: 1.875115V
- %PASS Slot 17 channel 3 linearity at 2V on 3V range Measured: 2.000048V low limit: 1.999933V high limit: 2.000113V
- %PASS Slot 17 channel 3 linearity at 2.125V on 3V range Measured: 2.125028V low limit: 2.124932V high limit: 2.125112V
- %PASS Slot 17 channel 3 linearity at 2.25V on 3V range Measured: 2.250055V low limit: 2.249930V high limit: 2.250110V
- %PASS Slot 17 channel 3 linearity at 2.375V on 3V range Measured: 2.374991V low limit: 2.374929V high limit: 2.375109V
- %PASS Slot 17 channel 3 linearity at 2.5V on 3V range Measured: 2.500012V low limit: 2.499927V high limit: 2.500107V
- %PASS Slot 17 channel 3 linearity at 2.625V on 3V range Measured: 2.625004V low limit: 2.624926V high limit: 2.625106V
- %PASS Slot 17 channel 3 linearity at 2.75V on 3V range
 Measured: 2.750031V low limit: 2.749924V high limit: 2.750104V
- %PASS Slot 17 channel 3 linearity at 2.875V on 3V range Measured: 2.874958V low limit: 2.874923V high limit: 2.875103V
- %PASS Slot 17 channel 3 linearity at 3V on 3V range Measured: 2.999999V low limit: 2.999921V high limit: 3.000101V
- %PASS Slot 17 channel 3 maximum linearity error on 3V range Measured: 5.620891E-05V high limit: 0.00009V
- %PASS Slot 17 channel 3 linearity at $\,$ 0V on 6V range $\label{eq:pass} \mbox{Measured: } 0.00004149941V \mbox{ low limit: -9.304435E-06V high limit: }$

- %PASS Slot 17 channel 3 linearity at 0.25V on 6V range Measured: 0.2501191V low limit: 0.2499827V high limit: 0.2503427V
- %PASS Slot 17 channel 3 linearity at .5V on 6V range Measured: 0.5000936V low limit: 0.4999748V high limit: 0.5003348V
- %PASS Slot 17 channel 3 linearity at .75V on 6V range Measured: 0.7501463V low limit: 0.7499668V high limit: 0.7503268V
- %PASS Slot 17 channel 3 linearity at 1V on 6V range
 Measured: 1.000123V low limit: 0.9999589V high limit: 1.000318V
- %PASS Slot 17 channel 3 linearity at 1.25V on 6V range
 Measured: 1.250173V low limit: 1.249951V high limit: 1.250311V
- %PASS Slot 17 channel 3 linearity at 1.5V on 6V range Measured: 1.500156V low limit: 1.499943V high limit: 1.500303V
- %PASS Slot 17 channel 3 linearity at 1.75V on 6V range Measured: 1.750225V low limit: 1.749935V high limit: 1.750295V
- %PASS Slot 17 channel 3 linearity at 2V on 6V range
 Measured: 2.000082V low limit: 1.999927V high limit: 2.000287V
- %PASS Slot 17 channel 3 linearity at 2.25V on 6V range Measured: 2.250121V low limit: 2.249919V high limit: 2.250279V
- %PASS Slot 17 channel 3 linearity at 2.5V on 6V range Measured: 2.500080V low limit: 2.499911V high limit: 2.500271V
- %PASS Slot 17 channel 3 linearity at 2.75V on 6V range Measured: 2.750125V low limit: 2.749903V high limit: 2.750263V
- %PASS Slot 17 channel 3 linearity at 3V on 6V range Measured: 3.000068V low limit: 2.999895V high limit: 3.000255V

- %PASS Slot 17 channel 3 linearity at 3.25V on 6V range Measured: 3.250136V low limit: 3.249887V high limit: 3.250247V
- %PASS Slot 17 channel 3 linearity at 3.5V on 6V range Measured: 3.500164V low limit: 3.499879V high limit: 3.500239V
- %PASS Slot 17 channel 3 linearity at 3.75V on 6V range Measured: 3.750095V low limit: 3.749871V high limit: 3.750231V
- %PASS Slot 17 channel 3 linearity at 4V on 6V range Measured: 4.000154V low limit: 3.999863V high limit: 4.000223V
- %PASS Slot 17 channel 3 linearity at 4.25V on 6V range Measured: 4.249994V low limit: 4.249855V high limit: 4.250215V
- %PASS Slot 17 channel 3 linearity at 4.5V on 6V range Measured: 4.500028V low limit: 4.499847V high limit: 4.500207V
- %PASS Slot 17 channel 3 linearity at 4.75V on 6V range Measured: 4.749972V low limit: 4.749839V high limit: 4.750199V
- %PASS Slot 17 channel 3 linearity at 5V on 6V range Measured: 5.000005V low limit: 4.999831V high limit: 5.000191V
- %PASS Slot 17 channel 3 linearity at 5.25V on 6V range Measured: 5.249950V low limit: 5.249824V high limit: 5.250184V
- %PASS Slot 17 channel 3 linearity at 5.5V on 6V range Measured: 5.499968V low limit: 5.499816V high limit: 5.500176V
- %PASS Slot 17 channel 3 linearity at 5.75V on 6V range Measured: 5.749912V low limit: 5.749808V high limit: 5.750168V
- %PASS Slot 17 channel 3 linearity at 6V on 6V range Measured: 5.999946V low limit: 5.999800V high limit: 6.000160V
- $\mbox{\%PASS}$ Slot 17 channel 3 maximum linearity error on 6V range Measured: 1.291961E-04V high limit: 0.00018V

- %PASS Slot 17 channel 4 linearity at 0V on 3V range

 Measured: -0.00005989007V low limit: -1.286290E-04V high limit:
 5.137093E-05V
- %PASS Slot 17 channel 4 linearity at 0.125V on 3V range Measured: 0.1249909V low limit: 0.1248713V high limit: 0.1250513V
- %PASS Slot 17 channel 4 linearity at 0.25V on 3V range Measured: 0.2499380V low limit: 0.2498712V high limit: 0.2500512V
- %PASS Slot 17 channel 4 linearity at 375V on 3V range
 Measured: 0.3749790V low limit: 0.3748711V high limit: 0.3750511V
- %PASS Slot 17 channel 4 linearity at .5V on 3V range Measured: 0.4999359V low limit: 0.4998711V high limit: 0.5000511V
- %PASS Slot 17 channel 4 linearity at .625V on 3V range Measured: 0.6249752V low limit: 0.6248710V high limit: 0.6250510V
- %PASS Slot 17 channel 4 linearity at .75V on 3V range Measured: 0.7499388V low limit: 0.7498709V high limit: 0.7500509V
- %PASS Slot 17 channel 4 linearity at .875V on 3V range Measured: 0.8749880V low limit: 0.8748709V high limit: 0.8750509V
- %PASS Slot 17 channel 4 linearity at 1V on 3V range Measured: 0.9999289V low limit: 0.9998708V high limit: 1.000050V
- %PASS Slot 17 channel 4 linearity at 1.125V on 3V range
 Measured: 1.124980V low limit: 1.124870V high limit: 1.125050V
- %PASS Slot 17 channel 4 linearity at 1.25V on 3V range
 Measured: 1.249934V low limit: 1.249870V high limit: 1.250050V
- %PASS Slot 17 channel 4 linearity at 1.375V on 3V range Measured: 1.374970V low limit: 1.374870V high limit: 1.375050V

- %PASS Slot 17 channel 4 linearity at 1.5V on 3V range Measured: 1.499928V low limit: 1.499870V high limit: 1.500050V
- %PASS Slot 17 channel 4 linearity at 1.625V on 3V range Measured: 1.624975V low limit: 1.624870V high limit: 1.625050V
- %PASS Slot 17 channel 4 linearity at 1.75V on 3V range Measured: 1.750008V low limit: 1.749870V high limit: 1.750050V
- %PASS Slot 17 channel 4 linearity at 1.875V on 3V range Measured: 1.874964V low limit: 1.874870V high limit: 1.875050V
- %PASS Slot 17 channel 4 linearity at 2V on 3V range
 Measured: 2.000004V low limit: 1.999870V high limit: 2.000050V
- %PASS Slot 17 channel 4 linearity at 2.125V on 3V range
 Measured: 2.124941V low limit: 2.124870V high limit: 2.125050V
- %PASS Slot 17 channel 4 linearity at 2.25V on 3V range Measured: 2.249991V low limit: 2.249870V high limit: 2.250050V
- %PASS Slot 17 channel 4 linearity at 2.375V on 3V range Measured: 2.374944V low limit: 2.374870V high limit: 2.375050V
- %PASS Slot 17 channel 4 linearity at 2.5V on 3V range Measured: 2.499977V low limit: 2.499870V high limit: 2.500050V
- %PASS Slot 17 channel 4 linearity at 2.625V on 3V range
 Measured: 2.624930V low limit: 2.624869V high limit: 2.625049V
- %PASS Slot 17 channel 4 linearity at 2.75V on 3V range Measured: 2.749966V low limit: 2.749869V high limit: 2.750049V
- %PASS Slot 17 channel 4 linearity at 2.875V on 3V range Measured: 2.874915V low limit: 2.874869V high limit: 2.875049V
- %PASS Slot 17 channel 4 linearity at 3V on 3V range Measured: 2.999964V low limit: 2.999869V high limit: 3.000049V

- %PASS Slot 17 channel 4 maximum linearity error on 3V range Measured: 4.848854E-05V high limit: 0.00009V
- %PASS Slot 17 channel 4 linearity at 0V on 6V range
 Measured: -0.00008074434V low limit: -2.000459E-04V high limit:
 1.599540E-04V
- %PASS Slot 17 channel 4 linearity at 0.25V on 6V range
 Measured: 0.2500104V low limit: 0.2498011V high limit: 0.2501611V
- %PASS Slot 17 channel 4 linearity at .5V on 6V range Measured: 0.4999864V low limit: 0.4998023V high limit: 0.5001623V
- %PASS Slot 17 channel 4 linearity at 0.75V on 6V range Measured: 0.7499615V low limit: 0.7498035V high limit: 0.7501635V
- %PASS Slot 17 channel 4 linearity at 1V on 6V range Measured: 0.9999582V low limit: 0.9998046V high limit: 1.000164V
- %PASS Slot 17 channel 4 linearity at 1.25V on 6V range
 Measured: 1.250022V low limit: 1.249805V high limit: 1.250165V
- %PASS Slot 17 channel 4 linearity at 1.5V on 6V range Measured: 1.499936V low limit: 1.499807V high limit: 1.500167V
- %PASS Slot 17 channel 4 linearity at 1.75V on 6V range
 Measured: 1.750026V low limit: 1.749808V high limit: 1.750168V
- %PASS Slot 17 channel 4 linearity at 2V on 6V range
 Measured: 1.999991V low limit: 1.999809V high limit: 2.000169V
- %PASS Slot 17 channel 4 linearity at 2.25V on 6V range
 Measured: 2.249984V low limit: 2.249810V high limit: 2.250170V
- %PASS Slot 17 channel 4 linearity at 2.5V on 6V range Measured: 2.499970V low limit: 2.499811V high limit: 2.500171V

- %PASS Slot 17 channel 4 linearity at 2.75V on 6V range Measured: 2.750035V low limit: 2.749812V high limit: 2.750172V
- %PASS Slot 17 channel 4 linearity at 3V on 6V range Measured: 2.999946V low limit: 2.999814V high limit: 3.000174V
- %PASS Slot 17 channel 4 linearity at 3.25V on 6V range Measured: 3.250013V low limit: 3.249815V high limit: 3.250175V
- %PASS Slot 17 channel 4 linearity at 3.5V on 6V range Measured: 3.500072V low limit: 3.499816V high limit: 3.500176V
- %PASS Slot 17 channel 4 linearity at 3.75V on 6V range Measured: 3.750053V low limit: 3.749817V high limit: 3.750177V
- %PASS Slot 17 channel 4 linearity at 4V on 6V range Measured: 4.000026V low limit: 3.999818V high limit: 4.000178V
- %PASS Slot 17 channel 4 linearity at 4.25V on 6V range Measured: 4.249988V low limit: 4.249820V high limit: 4.250180V
- %PASS Slot 17 channel 4 linearity at 4.5V on 6V range Measured: 4.500066V low limit: 4.499821V high limit: 4.500181V
- %PASS Slot 17 channel 4 linearity at 4.75V on 6V range Measured: 4.749957V low limit: 4.749822V high limit: 4.750182V
- %PASS Slot 17 channel 4 linearity at 5V on 6V range Measured: 5.000005V low limit: 4.999823V high limit: 5.000183V
- %PASS Slot 17 channel 4 linearity at 5.25V on 6V range Measured: 5.249998V low limit: 5.249824V high limit: 5.250184V
- %PASS Slot 17 channel 4 linearity at 5.5V on 6V range Measured: 5.499970V low limit: 5.499825V high limit: 5.500185V
- %PASS Slot 17 channel 4 linearity at 5.75V on 6V range Measured: 5.749932V low limit: 5.749827V high limit: 5.750187V

- %PASS Slot 17 channel 4 linearity at 6V on 6V range Measured: 6.000018V low limit: 5.999828V high limit: 6.000188V
- %PASS Slot 17 channel 4 maximum linearity error on 6V range Measured: 7.611771E-05V high limit: 0.00018V
- %PASS Slot 17 channel 5 linearity at $\,$ 0V on 3V range $\,$ Measured: -0.00001092326V low limit: -7.580731E-05V high limit: 1.041926E-04V
- %PASS Slot 17 channel 5 linearity at $\,$.25V on 3V range $\mbox{Measured: } 0.2499854V \mbox{ low limit: } 0.2499195V \mbox{ high limit: } 0.2500995V \mbox{}$
- %PASS Slot 17 channel 5 linearity at .375V on 3V range Measured: 0.3750216V low limit: 0.3749171V high limit: 0.3750971V
- %PASS Slot 17 channel 5 linearity at .5V on 3V range Measured: 0.4999795V low limit: 0.4999148V high limit: 0.5000948V
- %PASS Slot 17 channel 5 linearity at .625V on 3V range Measured: 0.6250212V low limit: 0.6249124V high limit: 0.6250924V
- %PASS Slot 17 channel 5 linearity at .75V on 3V range Measured: 0.7499745V low limit: 0.7499101V high limit: 0.7500901V
- %PASS Slot 17 channel 5 linearity at .875V on 3V range Measured: 0.8750293V low limit: 0.8749077V high limit: 0.8750877V
- %PASS Slot 17 channel 5 linearity at 1V on 3V range Measured: 0.9999701V low limit: 0.9999054V high limit: 1.000085V
- %PASS Slot 17 channel 5 linearity at 1.125V on 3V range
 Measured: 1.125011V low limit: 1.124903V high limit: 1.125083V

- %PASS Slot 17 channel 5 linearity at 1.25V on 3V range Measured: 1.249961V low limit: 1.249900V high limit: 1.250080V
- %PASS Slot 17 channel 5 linearity at 1.375V on 3V range Measured: 1.374999V low limit: 1.374898V high limit: 1.375078V
- %PASS Slot 17 channel 5 linearity at 1.5V on 3V range Measured: 1.499955V low limit: 1.499896V high limit: 1.500076V
- %PASS Slot 17 channel 5 linearity at 1.625V on 3V range Measured: 1.624996V low limit: 1.624893V high limit: 1.625073V
- %PASS Slot 17 channel 5 linearity at 1.75V on 3V range Measured: 1.750037V low limit: 1.749891V high limit: 1.750071V
- %PASS Slot 17 channel 5 linearity at 1.875V on 3V range Measured: 1.874988V low limit: 1.874889V high limit: 1.875069V
- %PASS Slot 17 channel 5 linearity at 2V on 3V range
 Measured: 2.000017V low limit: 1.999886V high limit: 2.000066V
- %PASS Slot 17 channel 5 linearity at 2.125V on 3V range Measured: 2.124958V low limit: 2.124884V high limit: 2.125064V
- %PASS Slot 17 channel 5 linearity at 2.25V on 3V range
 Measured: 2.250006V low limit: 2.249881V high limit: 2.250061V
- %PASS Slot 17 channel 5 linearity at 2.375V on 3V range Measured: 2.374949V low limit: 2.374879V high limit: 2.375059V
- %PASS Slot 17 channel 5 linearity at 2.5V on 3V range Measured: 2.499986V low limit: 2.499877V high limit: 2.500057V
- %PASS Slot 17 channel 5 linearity at 2.625V on 3V range Measured: 2.624938V low limit: 2.624874V high limit: 2.625054V
- %PASS Slot 17 channel 5 linearity at 2.75V on 3V range Measured: 2.749968V low limit: 2.749872V high limit: 2.750052V

- %PASS Slot 17 channel 5 linearity at 2.875V on 3V range Measured: 2.874909V low limit: 2.874870V high limit: 2.875050V
- %PASS Slot 17 channel 5 linearity at 3V on 3V range Measured: 2.999956V low limit: 2.999867V high limit: 3.000047V
- %PASS Slot 17 channel 5 maximum linearity error on 3V range Measured: 5.638706E-05V high limit: 0.00009V
- %PASS Slot 17 channel 5 linearity at $\,$ 0V on 6V range Measured: -0.00002522760V low limit: -1.549628E-04V high limit: 2.050371E-04V
- %PASS Slot 17 channel 5 linearity at 0.25V on 6V range Measured: 0.2500319V low limit: 0.2498405V high limit: 0.2502005V
- %PASS Slot 17 channel 5 linearity at .5V on 6V range Measured: 0.4999754V low limit: 0.4998360V high limit: 0.5001960V
- %PASS Slot 17 channel 5 linearity at .75V on 6V range Measured: 0.7500044V low limit: 0.7498316V high limit: 0.7501916V
- %PASS Slot 17 channel 5 linearity at 1V on 6V range Measured: 0.9999779V low limit: 0.9998271V high limit: 1.000187V
- %PASS Slot 17 channel 5 linearity at 1.25V on 6V range Measured: 1.250016V low limit: 1.249822V high limit: 1.250182V
- %PASS Slot 17 channel 5 linearity at 1.5V on 6V range Measured: 1.499975V low limit: 1.499818V high limit: 1.500178V
- %PASS Slot 17 channel 5 linearity at 1.75V on 6V range Measured: 1.750040V low limit: 1.749813V high limit: 1.750173V
- %PASS Slot 17 channel 5 linearity at 2V on 6V range Measured: 1.999978V low limit: 1.999809V high limit: 2.000169V

- %PASS Slot 17 channel 5 linearity at 2.25V on 6V range
 Measured: 2.250011V low limit: 2.249804V high limit: 2.250164V
- %PASS Slot 17 channel 5 linearity at 2.5V on 6V range Measured: 2.499963V low limit: 2.499800V high limit: 2.500160V
- %PASS Slot 17 channel 5 linearity at 2.75V on 6V range Measured: 2.750000V low limit: 2.749795V high limit: 2.750155V
- %PASS Slot 17 channel 5 linearity at 3V on 6V range Measured: 2.999959V low limit: 2.999791V high limit: 3.000151V
- %PASS Slot 17 channel 5 linearity at 3.25V on 6V range Measured: 3.250003V low limit: 3.249786V high limit: 3.250146V
- %PASS Slot 17 channel 5 linearity at 3.5V on 6V range Measured: 3.500037V low limit: 3.499782V high limit: 3.500142V
- %PASS Slot 17 channel 5 linearity at 3.75V on 6V range Measured: 3.749988V low limit: 3.749777V high limit: 3.750137V
- %PASS Slot 17 channel 5 linearity at 4V on 6V range Measured: 4.000004V low limit: 3.999773V high limit: 4.000133V
- %PASS Slot 17 channel 5 linearity at 4.25V on 6V range Measured: 4.249936V low limit: 4.249768V high limit: 4.250128V
- %PASS Slot 17 channel 5 linearity at $\,$ 4.5V on 6V range Measured: 4.499991V low limit: 4.499764V high limit: 4.500124V
- %PASS Slot 17 channel 5 linearity at 4.75V on 6V range Measured: 4.749920V low limit: 4.749760V high limit: 4.750120V
- %PASS Slot 17 channel 5 linearity at 5V on 6V range Measured: 4.999939V low limit: 4.999755V high limit: 5.000115V
- %PASS Slot 17 channel 5 linearity at 5.25V on 6V range Measured: 5.249901V low limit: 5.249751V high limit: 5.250111V

- %PASS Slot 17 channel 5 linearity at 5.5V on 6V range Measured: 5.499916V low limit: 5.499746V high limit: 5.500106V
- %PASS Slot 17 channel 5 linearity at 5.75V on 6V range Measured: 5.749845V low limit: 5.749742V high limit: 5.750102V
- %PASS Slot 17 channel 5 linearity at 6V on 6V range Measured: 5.999888V low limit: 5.999737V high limit: 6.000097V
- %PASS Slot 17 channel 5 maximum linearity error on 6V range Measured: 7.704308E-05V high limit: 0.00018V
- %PASS Slot 17 channel 6 linearity at 0V on 3V range Measured: 0.00004924418V low limit: 2.058152E-06V high limit: 1.820581E-04V
- %PASS Slot 17 channel 6 linearity at .125V on 3V range Measured: 0.1251019V low limit: 0.1249970V high limit: 0.1251770V
- %PASS Slot 17 channel 6 linearity at 0.25V on 3V range Measured: 0.2500453V low limit: 0.2499921V high limit: 0.2501721V
- %PASS Slot 17 channel 6 linearity at 375V on 3V range Measured: 0.3750861V low limit: 0.3749871V high limit: 0.3751671V
- %PASS Slot 17 channel 6 linearity at .5V on 3V range Measured: 0.5000409V low limit: 0.4999821V high limit: 0.5001621V
- %PASS Slot 17 channel 6 linearity at $\,$.625V on 3V range Measured: 0.6250785V low limit: 0.6249772V high limit: 0.6251572V
- %PASS Slot 17 channel 6 linearity at .75V on 3V range Measured: 0.7500761V low limit: 0.7499722V high limit: 0.7501522V
- %PASS Slot 17 channel 6 linearity at .875V on 3V range Measured: 0.8750713V low limit: 0.8749672V high limit: 0.8751472V

- %PASS Slot 17 channel 6 linearity at 1V on 3V range
 Measured: 1.000059V low limit: 0.9999623V high limit: 1.000142V
- %PASS Slot 17 channel 6 linearity at 1.125V on 3V range Measured: 1.125057V low limit: 1.124957V high limit: 1.125137V
- %PASS Slot 17 channel 6 linearity at 1.25V on 3V range Measured: 1.250048V low limit: 1.249952V high limit: 1.250132V
- %PASS Slot 17 channel 6 linearity at 1.375V on 3V range Measured: 1.375033V low limit: 1.374947V high limit: 1.375127V
- %PASS Slot 17 channel 6 linearity at 1.5V on 3V range Measured: 1.500036V low limit: 1.499942V high limit: 1.500122V
- %PASS Slot 17 channel 6 linearity at 1.625V on 3V range Measured: 1.625065V low limit: 1.624937V high limit: 1.625117V
- %PASS Slot 17 channel 6 linearity at 1.75V on 3V range Measured: 1.750049V low limit: 1.749932V high limit: 1.750112V
- %PASS Slot 17 channel 6 linearity at 1.875V on 3V range
 Measured: 1.875044V low limit: 1.874927V high limit: 1.875107V
- %PASS Slot 17 channel 6 linearity at 2V on 3V range Measured: 2.000023V low limit: 1.999922V high limit: 2.000102V
- %PASS Slot 17 channel 6 linearity at 2.125V on 3V range Measured: 2.125006V low limit: 2.124917V high limit: 2.125097V
- %PASS Slot 17 channel 6 linearity at 2.25V on 3V range
 Measured: 2.249999V low limit: 2.249912V high limit: 2.250092V
- %PASS Slot 17 channel 6 linearity at 2.375V on 3V range Measured: 2.374983V low limit: 2.374907V high limit: 2.375087V
- %PASS Slot 17 channel 6 linearity at 2.5V on 3V range
 Measured: 2.500012V low limit: 2.499902V high limit: 2.500082V

- %PASS Slot 17 channel 6 linearity at 2.625V on 3V range Measured: 2.624960V low limit: 2.624897V high limit: 2.625077V
- %PASS Slot 17 channel 6 linearity at 2.75V on 3V range Measured: 2.749988V low limit: 2.749892V high limit: 2.750072V
- %PASS Slot 17 channel 6 linearity at 2.875V on 3V range Measured: 2.874926V low limit: 2.874887V high limit: 2.875067V
- %PASS Slot 17 channel 6 linearity at 3V on 3V range Measured: 2.999965V low limit: 2.999882V high limit: 3.000062V
- %PASS Slot 17 channel 6 maximum linearity error on 3V range Measured: 5.155959E-05V high limit: 0.00009V
- %PASS Slot 17 channel 6 linearity at $\,$ 0V on 6V range Measured: 0.00004170154V low limit: -7.951768E-05V high limit: 2.804823E-04V
- %PASS Slot 17 channel 6 linearity at 0.25V on 6V range Measured: 0.2501028V low limit: 0.2499183V high limit: 0.2502783V
- %PASS Slot 17 channel 6 linearity at 0.5V on 6V range Measured: 0.5000555V low limit: 0.4999163V high limit: 0.5002763V
- %PASS Slot 17 channel 6 linearity at 0.75V on 6V range Measured: 0.7500976V low limit: 0.7499142V high limit: 0.7502742V
- %PASS Slot 17 channel 6 linearity at 1V on 6V range
 Measured: 1.000060V low limit: 0.9999121V high limit: 1.000272V
- %PASS Slot 17 channel 6 linearity at 1.25V on 6V range Measured: 1.250104V low limit: 1.249910V high limit: 1.250270V
- %PASS Slot 17 channel 6 linearity at 1.5V on 6V range Measured: 1.500062V low limit: 1.499907V high limit: 1.500267V

- %PASS Slot 17 channel 6 linearity at 1.75V on 6V range
 Measured: 1.750111V low limit: 1.749905V high limit: 1.750265V
- %PASS Slot 17 channel 6 linearity at 2V on 6V range
 Measured: 2.000047V low limit: 1.999903V high limit: 2.000263V
- %PASS Slot 17 channel 6 linearity at 2.25V on 6V range Measured: 2.250110V low limit: 2.249901V high limit: 2.250261V
- %PASS Slot 17 channel 6 linearity at 2.5V on 6V range Measured: 2.500050V low limit: 2.499899V high limit: 2.500259V
- %PASS Slot 17 channel 6 linearity at 2.75V on 6V range Measured: 2.750085V low limit: 2.749897V high limit: 2.750257V
- %PASS Slot 17 channel 6 linearity at 3V on 6V range Measured: 3.000035V low limit: 2.999895V high limit: 3.000255V
- %PASS Slot 17 channel 6 linearity at 3.25V on 6V range Measured: 3.250167V low limit: 3.249893V high limit: 3.250253V
- %PASS Slot 17 channel 6 linearity at 3.5V on 6V range Measured: 3.500188V low limit: 3.499891V high limit: 3.500251V
- %PASS Slot 17 channel 6 linearity at 3.75V on 6V range Measured: 3.750129V low limit: 3.749889V high limit: 3.750249V
- %PASS Slot 17 channel 6 linearity at 4V on 6V range Measured: 4.000144V low limit: 3.999887V high limit: 4.000247V
- %PASS Slot 17 channel 6 linearity at 4.25V on 6V range Measured: 4.250073V low limit: 4.249885V high limit: 4.250245V
- %PASS Slot 17 channel 6 linearity at 4.5V on 6V range Measured: 4.500126V low limit: 4.499882V high limit: 4.500242V
- %PASS Slot 17 channel 6 linearity at 4.75V on 6V range Measured: 4.750044V low limit: 4.749880V high limit: 4.750240V

- %PASS Slot 17 channel 6 linearity at 5V on 6V range Measured: 5.000057V low limit: 4.999878V high limit: 5.000238V
- %PASS Slot 17 channel 6 linearity at 5.25V on 6V range Measured: 5.250017V low limit: 5.249876V high limit: 5.250236V
- %PASS Slot 17 channel 6 linearity at 5.5V on 6V range Measured: 5.500029V low limit: 5.499874V high limit: 5.500234V
- %PASS Slot 17 channel 6 linearity at 5.75V on 6V range Measured: 5.749950V low limit: 5.749872V high limit: 5.750232V
- %PASS Slot 17 channel 6 linearity at 6V on 6V range Measured: 5.999992V low limit: 5.999870V high limit: 6.000230V
- %PASS Slot 17 channel 6 maximum linearity error on 6V range Measured: 1.175524E-04V high limit: 0.00018V
- %PASS Slot 17 channel 7 linearity at 0V on 3V range

 Measured: 0.00002153722V low limit: -5.135489E-05V high limit:
 1.286451E-04V
- %PASS Slot 17 channel 7 linearity at .125V on 3V range Measured: 0.1250387V low limit: 0.1249501V high limit: 0.1251301V
- %PASS Slot 17 channel 7 linearity at 0.25V on 3V range Measured: 0.2499992V low limit: 0.2499516V high limit: 0.2501316V
- %PASS Slot 17 channel 7 linearity at 375V on 3V range
 Measured: 0.3750537V low limit: 0.3749531V high limit: 0.3751331V
- %PASS Slot 17 channel 7 linearity at 0.5V on 3V range Measured: 0.5000218V low limit: 0.4999546V high limit: 0.5001346V
- %PASS Slot 17 channel 7 linearity at .625V on 3V range Measured: 0.6250720V low limit: 0.6249561V high limit: 0.6251361V

- %PASS Slot 17 channel 7 linearity at .75V on 3V range Measured: 0.7500435V low limit: 0.7499576V high limit: 0.7501376V
- %PASS Slot 17 channel 7 linearity at .875V on 3V range Measured: 0.8750570V low limit: 0.8749591V high limit: 0.8751391V
- %PASS Slot 17 channel 7 linearity at 1V on 3V range Measured: 1.000054V low limit: 0.9999606V high limit: 1.000140V
- %PASS Slot 17 channel 7 linearity at 1.125V on 3V range Measured: 1.125060V low limit: 1.124962V high limit: 1.125142V
- %PASS Slot 17 channel 7 linearity at 1.25V on 3V range Measured: 1.250068V low limit: 1.249963V high limit: 1.250143V
- %PASS Slot 17 channel 7 linearity at 1.375V on 3V range Measured: 1.375065V low limit: 1.374965V high limit: 1.375145V
- %PASS Slot 17 channel 7 linearity at 1.5V on 3V range
 Measured: 1.500029V low limit: 1.499966V high limit: 1.500146V
- %PASS Slot 17 channel 7 linearity at 1.625V on 3V range
 Measured: 1.625080V low limit: 1.624968V high limit: 1.625148V
- %PASS Slot 17 channel 7 linearity at 1.75V on 3V range
 Measured: 1.750076V low limit: 1.749969V high limit: 1.750149V
- %PASS Slot 17 channel 7 linearity at 1.875V on 3V range
 Measured: 1.875089V low limit: 1.874971V high limit: 1.875151V
- %PASS Slot 17 channel 7 linearity at 2V on 3V range
 Measured: 2.000079V low limit: 1.999972V high limit: 2.000152V
- %PASS Slot 17 channel 7 linearity at 2.125V on 3V range Measured: 2.125074V low limit: 2.124974V high limit: 2.125154V
- %PASS Slot 17 channel 7 linearity at 2.25V on 3V range Measured: 2.250090V low limit: 2.249975V high limit: 2.250155V

- %PASS Slot 17 channel 7 linearity at 2.375V on 3V range Measured: 2.375040V low limit: 2.374977V high limit: 2.375157V
- %PASS Slot 17 channel 7 linearity at 2.5V on 3V range Measured: 2.500082V low limit: 2.499978V high limit: 2.500158V
- %PASS Slot 17 channel 7 linearity at 2.625V on 3V range Measured: 2.625045V low limit: 2.624980V high limit: 2.625160V
- %PASS Slot 17 channel 7 linearity at 2.75V on 3V range Measured: 2.750087V low limit: 2.749981V high limit: 2.750161V
- %PASS Slot 17 channel 7 linearity at 2.875V on 3V range Measured: 2.875038V low limit: 2.874983V high limit: 2.875163V
- %PASS Slot 17 channel 7 linearity at 3V on 3V range Measured: 3.000046V low limit: 2.999984V high limit: 3.000164V
- %PASS Slot 17 channel 7 maximum linearity error on 3V range Measured: 4.244934E-05V high limit: 0.00009V
- %PASS Slot 17 channel 7 linearity at $\,$ 0V on 6V range Measured: 0.00002502766V low limit: -9.302283E-05V high limit: 2.669771E-04V
- %PASS Slot 17 channel 7 linearity at 0.25V on 6V range Measured: 0.2501049V low limit: 0.2499072V high limit: 0.2502672V
- %PASS Slot 17 channel 7 linearity at 0.5V on 6V range
 Measured: 0.5000684V low limit: 0.4999075V high limit: 0.5002675V
- %PASS Slot 17 channel 7 linearity at .75V on 6V range Measured: 0.7500433V low limit: 0.7499078V high limit: 0.7502678V
- %PASS Slot 17 channel 7 linearity at 1V on 6V range
 Measured: 1.000022V low limit: 0.9999080V high limit: 1.000268V

- %PASS Slot 17 channel 7 linearity at 1.25V on 6V range Measured: 1.250080V low limit: 1.249908V high limit: 1.250268V
- %PASS Slot 17 channel 7 linearity at 1.5V on 6V range Measured: 1.500072V low limit: 1.499908V high limit: 1.500268V
- %PASS Slot 17 channel 7 linearity at 1.75V on 6V range Measured: 1.750143V low limit: 1.749908V high limit: 1.750268V
- %PASS Slot 17 channel 7 linearity at 2V on 6V range Measured: 2.000090V low limit: 1.999909V high limit: 2.000269V
- %PASS Slot 17 channel 7 linearity at 2.25V on 6V range Measured: 2.250150V low limit: 2.249909V high limit: 2.250269V
- %PASS Slot 17 channel 7 linearity at 2.5V on 6V range Measured: 2.500125V low limit: 2.499909V high limit: 2.500269V
- %PASS Slot 17 channel 7 linearity at 2.75V on 6V range Measured: 2.750159V low limit: 2.749910V high limit: 2.750270V
- %PASS Slot 17 channel 7 linearity at 3V on 6V range Measured: 3.000047V low limit: 2.999910V high limit: 3.000270V
- %PASS Slot 17 channel 7 linearity at 3.25V on 6V range Measured: 3.250098V low limit: 3.249910V high limit: 3.250270V
- %PASS Slot 17 channel 7 linearity at 3.5V on 6V range Measured: 3.500137V low limit: 3.499910V high limit: 3.500270V
- %PASS Slot 17 channel 7 linearity at 3.75V on 6V range Measured: 3.750109V low limit: 3.749911V high limit: 3.750271V
- %PASS Slot 17 channel 7 linearity at 4V on 6V range Measured: 4.000141V low limit: 3.999911V high limit: 4.000271V
- %PASS Slot 17 channel 7 linearity at 4.25V on 6V range Measured: 4.250084V low limit: 4.249911V high limit: 4.250271V

- %PASS Slot 17 channel 7 linearity at 4.5V on 6V range Measured: 4.500155V low limit: 4.499911V high limit: 4.500271V
- %PASS Slot 17 channel 7 linearity at 4.75V on 6V range Measured: 4.750102V low limit: 4.749912V high limit: 4.750272V
- %PASS Slot 17 channel 7 linearity at 5V on 6V range Measured: 5.000124V low limit: 4.999912V high limit: 5.000272V
- %PASS Slot 17 channel 7 linearity at 5.25V on 6V range Measured: 5.250103V low limit: 5.249912V high limit: 5.250272V
- %PASS Slot 17 channel 7 linearity at 5.5V on 6V range Measured: 5.500032V low limit: 5.499913V high limit: 5.500273V
- %PASS Slot 17 channel 7 linearity at 5.75V on 6V range Measured: 5.749976V low limit: 5.749913V high limit: 5.750273V
- %PASS Slot 17 channel 7 linearity at 6V on 6V range Measured: 6.000055V low limit: 5.999913V high limit: 6.000273V
- %PASS Slot 17 channel 7 maximum linearity error on 6V range Measured: 1.165406E-04V high limit: 0.00018V
- ...checking DAC code transitions...
- %PASS Slot 17 channel 0 raw DAC codes linearity at 3.05V on 3V range Measured: 3.048803V low limit: 3.048718V high limit: 3.048898V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 3.04995269703212V on 3V range

 Measured: 3.048763V low limit: 3.048671V high limit: 3.048851V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 3.04990539406424V on 3V range

 Measured: 3.048718V low limit: 3.048624V high limit: 3.048804V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 3.04985809109636V on 3V

Measured: 3.048672V low limit: 3.048576V high limit: 3.048756V

%PASS - Slot 17 channel 0 raw DAC codes linearity at $\,$ 3.04981078812848V on 3V range

Measured: 3.048619V low limit: 3.048529V high limit: 3.048709V

% PASS - Slot 17 channel 0 raw DAC codes linearity at $\,$ 3.04966887922484V on 3V range

Measured: 3.048484V low limit: 3.048387V high limit: 3.048567V

%PASS - Slot 17 channel 0 raw DAC codes linearity at $\,$ 3.04962157625696V on 3V range

Measured: 3.048432V low limit: 3.048340V high limit: 3.048520V

- %PASS Slot 17 channel 0 raw DAC codes linearity at 3.0492904554818V on 3V range Measured: 3.048106V low limit: 3.048009V high limit: 3.048189V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 3.04924315251392V on 3V range

 Measured: 3.048049V low limit: 3.047962V high limit: 3.048142V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 3.04853360799573V on 3V range

 Measured: 3.047346V low limit: 3.047253V high limit: 3.047433V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 3.04848630502785V on 3V range

 Measured: 3.047291V low limit: 3.047205V high limit: 3.047385V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 3.04701991302358V on 3V range

 Measured: 3.045833V low limit: 3.045740V high limit: 3.045920V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 3.0469726100557V on 3V range Measured: 3.045782V low limit: 3.045692V high limit: 3.045872V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 3.04399252307927V on 3V

Measured: 3.042809V low limit: 3.042714V high limit: 3.042894V

%PASS - Slot 17 channel 0 raw DAC codes linearity at $\,$ 3.04394522011139V on 3V range

Measured: 3.042752V low limit: 3.042667V high limit: 3.042847V

%PASS - Slot 17 channel 0 raw DAC codes linearity at $\,$ 3.03793774319066V on 3V range

Measured: 3.036757V low limit: 3.036662V high limit: 3.036842V

%PASS - Slot 17 channel 0 raw DAC codes linearity at $\,$ 3.03789044022278V on 3V range

Measured: 3.036702V low limit: 3.036615V high limit: 3.036795V

%PASS - Slot 17 channel 0 raw DAC codes linearity at $\,$ 3.02582818341344V on 3V range

Measured: 3.024652V low limit: 3.024559V high limit: 3.024739V

%PASS - Slot 17 channel 0 raw DAC codes linearity at $\,$ 3.02578088044556V on 3V range

Measured: 3.024597V low limit: 3.024512V high limit: 3.024692V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 3.00160906385901V on 3V range

Measured: 3.000443V low limit: 3.000352V high limit: 3.000532V

% PASS - Slot 17 channel 0 raw DAC codes linearity at $\,$ 3.00156176089113V on 3V range

Measured: 3.000388V low limit: 3.000305V high limit: 3.000485V

%PASS - Slot 17 channel 0 raw DAC codes linearity at $\,$ 2.95317082475013V on 3V range

Measured: 2.952027V low limit: 2.951939V high limit: 2.952119V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 2.95312352178225V on 3V range

Measured: 2.951965V low limit: 2.951892V high limit: 2.952072V

- %PASS Slot 17 channel 0 raw DAC codes linearity at 2.85629434653239V on 3V range

 Measured: 2.855188V low limit: 2.855113V high limit: 2.855293V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 2.85624704356451V on 3V range

 Measured: 2.855150V low limit: 2.855066V high limit: 2.855246V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 2.66254139009689V on 3V range

 Measured: 2.661541V low limit: 2.661461V high limit: 2.661641V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 2.66249408712902V on 3V range

 Measured: 2.661502V low limit: 2.661413V high limit: 2.661593V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 2.27503547722591V on 3V range

 Measured: 2.274253V low limit: 2.274156V high limit: 2.274336V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 2.27498817425803V on 3V range

 Measured: 2.274201V low limit: 2.274109V high limit: 2.274289V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 1.50002365148394V on 3V range

 Measured: 1.499665V low limit: 1.499546V high limit: 1.499726V
- %PASS Slot 17 channel 0 raw DAC codes linearity at -.05V on 3V range Measured: -0.04960647V low limit: -4.967271E-02V high limit: -4.949271E-02V
- %PASS Slot 17 channel 0 raw DAC codes maximum linearity error on 3V range

- Measured: 2.879674E-05V high limit: 0.00009V
- %PASS Slot 17 channel 0 raw DAC code binary transition 0 to 1 on 3V range Measured: 3.953700E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 1 to 2 on 3V range Measured: 4.522899E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 2 to 3 on 3V range Measured: 4.562400E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 3 to 4 on 3V range Measured: 5.302199E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 7 to 8 on 3V range Measured: 5.197200E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 15 to 16 on 3V range Measured: 5.683100E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 31 to 32 on 3V range Measured: 5.442399E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 63 to 64 on 3V range Measured: 5.078999E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 127 to 128 on 3V range Measured: 5.652600E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

- %PASS Slot 17 channel 0 raw DAC code binary transition 255 to 256 on 3V range Measured: 5.464299E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 511 to 512 on 3V range Measured: 5.433699E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition $\,$ 1023 to $\,$ 1024 on 3V $\,$ range

Measured: 5.516899E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition $\,$ 2047 to $\,$ 2048 on 3V range

Measured: $6.178000\mbox{E-}05\mbox{V}$ low limit: $-4.269775\mbox{E-}05\mbox{V}$ high limit: $1.373022\mbox{E-}04\mbox{V}$

% PASS - Slot 17 channel 0 raw DAC code binary transition $\,$ 4095 to $\,$ 4096 on 3V $\,$ range

Measured: 3.747899E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

 $\mbox{\%PASS}$ - Slot 17 channel 0 raw DAC code binary transition $\,$ 8191 to $\,$ 8192 on 3V $\,$ range

Measured: $3.848599 \hbox{E-}05 V$ low limit: $\text{-}4.269775 \hbox{E-}05 V$ high limit: $1.373022 \hbox{E-}04 V$

%PASS - Slot 17 channel 0 raw DAC code binary transition 16383 to 16384 on 3V range

Measured: $5.192799 \hbox{E-}05 V$ low limit: $\text{-}4.269775 \hbox{E-}05 V$ high limit: $1.373022 \hbox{E-}04 V$

%PASS - Slot 17 channel 0 raw DAC code binary transition $\,$ 32767 to $\,$ 32768 on 3V range

Measured: 5.516800E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

- $\mbox{\it \%PASS}$ Slot 17 channel 0 raw DAC code binary transitions maximum difference on $3\mbox{\it V}$ range
 - Measured: 6.178000E-05V high limit: 1.373022E-04V
- $\mbox{\%PASS}$ Slot 17 channel 0 raw DAC code binary transitions minimum difference on 3V range
 - Measured: 3.747899E-05V low limit: -4.269775E-05V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.1V on 6V range Measured: 6.096521V low limit: 6.096346V high limit: 6.096706V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.09990539406424V on 6V range

 Measured: 6.096429V low limit: 6.096251V high limit: 6.096611V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.09981078812848V on 6V range

 Measured: 6.096331V low limit: 6.096157V high limit: 6.096517V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.09971618219272V on 6V range

 Measured: 6.096240V low limit: 6.096062V high limit: 6.096422V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.09962157625696V on 6V range

 Measured: 6.096140V low limit: 6.095967V high limit: 6.096327V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.09933775844968V on 6V range

 Measured: 6.095866V low limit: 6.095684V high limit: 6.096044V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.09924315251392V on 6V range

 Measured: 6.095759V low limit: 6.095589V high limit: 6.095949V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.09858091096361V on 6V range

 Measured: 6.095113V low limit: 6.094928V high limit: 6.095288V

- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.09848630502785V on 6V range

 Measured: 6.095003V low limit: 6.094833V high limit: 6.095193V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.09706721599146V on 6V range

 Measured: 6.093598V low limit: 6.093415V high limit: 6.093775V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.0969726100557V on 6V range Measured: 6.093491V low limit: 6.093320V high limit: 6.093680V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.09403982604715V on 6V range

 Measured: 6.090571V low limit: 6.090390V high limit: 6.090750V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.09394522011139V on 6V range

 Measured: 6.090467V low limit: 6.090295V high limit: 6.090655V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.08798504615854V on 6V range

 Measured: 6.084527V low limit: 6.084339V high limit: 6.084699V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.08789044022278V on 6V range

 Measured: 6.084411V low limit: 6.084245V high limit: 6.084605V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.07587548638132V on 6V range

 Measured: 6.072424V low limit: 6.072238V high limit: 6.072598V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.07578088044556V on 6V range

 Measured: 6.072315V low limit: 6.072144V high limit: 6.072504V
- % PASS Slot 17 channel 0 raw DAC codes linearity at $\,$ 6.05165636682689V on 6V range

- Measured: 6.048220V low limit: 6.048037V high limit: 6.048397V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.05156176089113V on 6V range

 Measured: 6.048113V low limit: 6.047942V high limit: 6.048302V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.00321812771801V on 6V range
 - Measured: 5.999814V low limit: 5.999633V high limit: 5.999993V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 6.00312352178225V on 6V range

 Measured: 5.999703V low limit: 5.999539V high limit: 5.999899V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 5.90634164950027V on 6V range

 Measured: 5.902998V low limit: 5.902826V high limit: 5.903186V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 5.90624704356451V on 6V range

 Measured: 5.902890V low limit: 5.902732V high limit: 5.903092V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 5.71249408712902V on 6V range

 Measured: 5.709296V low limit: 5.709118V high limit: 5.709478V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 5.32508278019379V on 6V range

 Measured: 5.322165V low limit: 5.321985V high limit: 5.322345V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 5.32498817425803V on 6V range

 Measured: 5.322082V low limit: 5.321890V high limit: 5.322250V

- %PASS Slot 17 channel 0 raw DAC codes linearity at 4.55007095445182V on 6V range

 Measured: 4.547749V low limit: 4.547529V high limit: 4.547889V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 4.54997634851606V on 6V range

 Measured: 4.547646V low limit: 4.547435V high limit: 4.547795V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 3.00004730296788V on 6V range

 Measured: 2.998876V low limit: 2.998618V high limit: 2.998978V
- %PASS Slot 17 channel 0 raw DAC codes linearity at 2.99995269703212V on 6V range

 Measured: 2.998764V low limit: 2.998524V high limit: 2.998884V
- %PASS Slot 17 channel 0 raw DAC codes linearity at -.1V on 6V range Measured: -0.09910929V low limit: -9.920354E-02V high limit: -9.884354E-02V
- %PASS Slot 17 channel 0 raw DAC codes maximum linearity error on 6V range Measured: 8.575527E-05V high limit: 0.00018V
- %PASS Slot 17 channel 0 raw DAC code binary transition 0 to 1 on 6V range Measured: 9.238500E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 1 to 2 on 6V range Measured: 9.750699E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition $\,2$ to $\,3$ on 6V range Measured: 9.120200E-05V low limit: -8.539550E-05V high limit: $\,2.746044\text{E-}04\text{V}$
- %PASS Slot 17 channel 0 raw DAC code binary transition 3 to 4 on 6V range Measured: 9.987199E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

- %PASS Slot 17 channel 0 raw DAC code binary transition 7 to 8 on 6V range Measured: 1.073149E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition $\,$ 15 to $\,$ 16 on 6V range Measured: 1.092410E-04V low limit: -8.539550E-05V high limit: $\,$ 2.746044E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 31 to 32 on 6V range Measured: 1.062650E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 63 to 64 on 6V range Measured: 1.039430E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 127 to 128 on 6V range Measured: 1.163780E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 255 to 256 on 6V range Measured: 1.094170E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 511 to 512 on 6V range Measured: 1.076650E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 0 raw DAC code binary transition 1023 to 1024 on 6V range

 Measured: 1.104239E-04V low limit: -8.539550E-05V high limit:

2.746044E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition $\,$ 2047 to $\,$ 2048 on 6V $\,$ range

Measured: 1.085409E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition $\,$ 4095 to $\,$ 4096 on 6V range

Measured: $6.974800 \hbox{E-}05 V$ low limit: $-8.539550 \hbox{E-}05 V$ high limit: $2.746044 \hbox{E-}04 V$

%PASS - Slot 17 channel 0 raw DAC code binary transition $\,$ 8191 to $\,$ 8192 on 6V range

Measured: 8.292699E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition $\,$ 16383 to $\,$ 16384 on 6V range

Measured: 1.034180E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition $\,$ 32767 to $\,$ 32768 on 6V range

Measured: 1.115620E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

 $\mbox{\%PASS}$ - Slot 17 channel 0 raw DAC code binary transitions maximum difference on $\mbox{6V range}$

Measured: 1.163780E-04V high limit: 2.746044E-04V

 $\mbox{\%PASS}$ - Slot 17 channel 0 raw DAC code binary transitions minimum difference on $\mbox{6V range}$

Measured: 6.974800E-05V low limit: -8.539550E-05V

- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.05V on 3V range Measured: 3.049515V low limit: 3.049434V high limit: 3.049614V
- %PASS Slot 17 channel 1 raw DAC codes linearity at $\,$ 3.04995269703212V on 3V range

Measured: 3.049473V low limit: 3.049387V high limit: 3.049567V

 $\mbox{\%PASS}$ - Slot 17 channel 1 raw DAC codes linearity at $\,$ 3.04990539406424V on 3V range

- Measured: 3.049426V low limit: 3.049340V high limit: 3.049520V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.04985809109636V on 3V range

 Measured: 3.049384V low limit: 3.049292V high limit: 3.049472V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.04981078812848V on 3V range

 Measured: 3.049332V low limit: 3.049245V high limit: 3.049425V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.04966887922484V on 3V range

 Measured: 3.049197V low limit: 3.049103V high limit: 3.049283V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.04962157625696V on 3V range

 Measured: 3.049146V low limit: 3.049056V high limit: 3.049236V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.0492904554818V on 3V range Measured: 3.048819V low limit: 3.048725V high limit: 3.048905V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.04924315251392V on 3V range

 Measured: 3.048765V low limit: 3.048677V high limit: 3.048857V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.04853360799573V on 3V range

 Measured: 3.048062V low limit: 3.047968V high limit: 3.048148V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.04848630502785V on 3V range

 Measured: 3.048012V low limit: 3.047921V high limit: 3.048101V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.04701991302358V on 3V range

 Measured: 3.046551V low limit: 3.046454V high limit: 3.046634V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.0469726100557V on 3V range

- Measured: 3.046500V low limit: 3.046407V high limit: 3.046587V
- %PASS Slot 17 channel 1 raw DAC codes linearity at $\,$ 3.04399252307927V on 3V range
 - Measured: 3.043522V low limit: 3.043427V high limit: 3.043607V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.04394522011139V on 3V range
 - Measured: 3.043468V low limit: 3.043380V high limit: 3.043560V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.03793774319066V on 3V range

 Measured: 3.037463V low limit: 3.037372V high limit: 3.037552V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.03789044022278V on 3V range

 Measured: 3.037417V low limit: 3.037325V high limit: 3.037505V
- % PASS - Slot 17 channel 1 raw DAC codes linearity at $\,$ 3.02582818341344V on 3V range
 - Measured: 3.025358V low limit: 3.025263V high limit: 3.025443V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.02578088044556V on 3V range

 Measured: 3.025303V low limit: 3.025216V high limit: 3.025396V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.00160906385901V on 3V range

 Measured: 3.001134V low limit: 3.001044V high limit: 3.001224V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.00156176089113V on 3V range

 Measured: 3.001081V low limit: 3.000997V high limit: 3.001177V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 2.95317082475013V on 3V range

 Measured: 2.952693V low limit: 2.952607V high limit: 2.952787V

- %PASS Slot 17 channel 1 raw DAC codes linearity at 2.95312352178225V on 3V range

 Measured: 2.952640V low limit: 2.952560V high limit: 2.952740V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 2.85629434653239V on 3V range

 Measured: 2.855810V low limit: 2.855732V high limit: 2.855912V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 2.85624704356451V on 3V range

 Measured: 2.855769V low limit: 2.855685V high limit: 2.855865V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 2.66254139009689V on 3V range

 Measured: 2.662065V low limit: 2.661983V high limit: 2.662163V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 2.66249408712902V on 3V range

 Measured: 2.662032V low limit: 2.661936V high limit: 2.662116V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 2.27503547722591V on 3V range

 Measured: 2.274582V low limit: 2.274485V high limit: 2.274665V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 2.27498817425803V on 3V range

 Measured: 2.274536V low limit: 2.274438V high limit: 2.274618V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 1.50002365148394V on 3V range

 Measured: 1.499595V low limit: 1.499489V high limit: 1.499669V
- %PASS Slot 17 channel 1 raw DAC codes linearity at $\,$ 1.49997634851606V on 3V range
 - Measured: 1.499546V low limit: 1.499441V high limit: 1.499621V

- %PASS Slot 17 channel 1 raw DAC codes linearity at -.05V on 3V range Measured: -0.05043091V low limit: -0.05050350V high limit: -0.05032350V
- %PASS Slot 17 channel 1 raw DAC codes maximum linearity error on 3V range Measured: 1.740551E-05V high limit: 0.00009V
- %PASS Slot 17 channel 1 raw DAC code binary transition 0 to 1 on 3V range Measured: 4.137599E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 1 to 2 on 3V range Measured: 4.759399E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 2 to 3 on 3V range Measured: 4.168199E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition $\,$ 3 to $\,$ 4 on 3V range Measured: 5.149100E-05V low limit: -4.269775E-05V high limit: $\,$ 1.373022E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 7 to 8 on 3V range Measured: 5.109599E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 15 to 16 on 3V range Measured: 5.446800E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 31 to 32 on 3V range Measured: 5.008900E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 63 to 64 on 3V range Measured: 5.013300E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

- %PASS Slot 17 channel 1 raw DAC code binary transition 127 to 128 on 3V range Measured: 5.367900E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 255 to 256 on 3V range Measured: 4.584200E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 511 to 512 on 3V range Measured: 5.512399E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 1023 to 1024 on 3V range

 Measured: 5.385499E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 2047 to 2048 on 3V range

 Measured: 5.289100E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

- %PASS Slot 17 channel 1 raw DAC code binary transition 32767 to 32768 on 3V

Measured: $4.807500 \hbox{E-}05 V$ low limit: $-4.269775 \hbox{E-}05 V$ high limit: $1.373022 \hbox{E-}04 V$

 $\mbox{\%PASS}$ - Slot 17 channel 1 raw DAC code binary transitions maximum difference on $3\mbox{V}$ range

Measured: 5.512399E-05V high limit: 1.373022E-04V

 $\mbox{\%PASS}$ - Slot 17 channel 1 raw DAC code binary transitions minimum difference on $3\mbox{V}$ range

Measured: 3.288200E-05V low limit: -4.269775E-05V

- %PASS Slot 17 channel 1 raw DAC codes linearity at 6.1V on 6V range Measured: 6.098181V low limit: 6.098007V high limit: 6.098367V
- %PASS Slot 17 channel 1 raw DAC codes linearity at $\,$ 6.09990539406424V on 6V range

Measured: 6.098093V low limit: 6.097913V high limit: 6.098273V

% PASS - Slot 17 channel 1 raw DAC codes linearity at $\,$ 6.09981078812848V on 6V range

Measured: 6.097994V low limit: 6.097818V high limit: 6.098178V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 6.09971618219272V on 6V range

Measured: 6.097901V low limit: 6.097724V high limit: 6.098084V

% PASS - Slot 17 channel 1 raw DAC codes linearity at $\,$ 6.09962157625696V on 6V range

Measured: 6.097805V low limit: 6.097629V high limit: 6.097989V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 6.09933775844968V on 6V range

Measured: 6.097528V low limit: 6.097345V high limit: 6.097705V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 6.09924315251392V on 6V range

Measured: 6.097423V low limit: 6.097251V high limit: 6.097611V

- %PASS Slot 17 channel 1 raw DAC codes linearity at 6.09858091096361V on 6V range

 Measured: 6.096773V low limit: 6.096589V high limit: 6.096949V
- % PASS - Slot 17 channel 1 raw DAC codes linearity at $\,$ 6.09848630502785 V on 6V range

Measured: 6.096660V low limit: 6.096494V high limit: 6.096854V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 6.09706721599146V on 6V range

Measured: 6.095258V low limit: 6.095076V high limit: 6.095436V

- %PASS Slot 17 channel 1 raw DAC codes linearity at 6.0969726100557V on 6V range Measured: 6.095156V low limit: 6.094981V high limit: 6.095341V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 6.09403982604715V on 6V range

 Measured: 6.092232V low limit: 6.092049V high limit: 6.092409V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 6.09394522011139V on 6V range

 Measured: 6.092128V low limit: 6.091955V high limit: 6.092315V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 6.08798504615854V on 6V range

 Measured: 6.086182V low limit: 6.085996V high limit: 6.086356V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 6.08789044022278V on 6V range

 Measured: 6.086077V low limit: 6.085902V high limit: 6.086262V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 6.07587548638132V on 6V range

 Measured: 6.074072V low limit: 6.073890V high limit: 6.074250V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 6.07578088044556V on 6V

Measured: 6.073971V low limit: 6.073796V high limit: 6.074156V

%PASS - Slot 17 channel 1 raw DAC codes linearity at $\,$ 6.05165636682689V on 6V range

Measured: 6.049863V low limit: 6.049678V high limit: 6.050038V

%PASS - Slot 17 channel 1 raw DAC codes linearity at $\,$ 6.05156176089113V on 6V range

Measured: 6.049754V low limit: 6.049584V high limit: 6.049944V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 6.00321812771801V on 6V range

Measured: 6.001434V low limit: 6.001255V high limit: 6.001615V

%PASS - Slot 17 channel 1 raw DAC codes linearity at $\,$ 6.00312352178225V on 6V range

Measured: 6.001329V low limit: 6.001160V high limit: 6.001520V

%PASS - Slot 17 channel 1 raw DAC codes linearity at $\,$ 5.90634164950027V on 6V range

Measured: 5.904581V low limit: 5.904408V high limit: 5.904768V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 5.90624704356451V on 6V range

Measured: 5.904478V low limit: 5.904313V high limit: 5.904673V

% PASS - Slot 17 channel 1 raw DAC codes linearity at $\,$ 5.71258869306477V on 6V range

Measured: 5.710879V low limit: 5.710713V high limit: 5.711073V

% PASS - Slot 17 channel 1 raw DAC codes linearity at $\,$ 5.71249408712902V on 6V range

Measured: 5.710795V low limit: 5.710618V high limit: 5.710978V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 5.32508278019379V on 6V range

Measured: 5.323496V low limit: 5.323324V high limit: 5.323684V

- %PASS Slot 17 channel 1 raw DAC codes linearity at 5.32498817425803V on 6V range

 Measured: 5.323427V low limit: 5.323229V high limit: 5.323589V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 4.55007095445182V on 6V range

 Measured: 4.548752V low limit: 4.548546V high limit: 4.548906V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 4.54997634851606V on 6V range

 Measured: 4.548665V low limit: 4.548451V high limit: 4.548811V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 3.00004730296788V on 6V range
 Measured: 2.999214V low limit: 2.998990V high limit: 2.999350V
- %PASS Slot 17 channel 1 raw DAC codes linearity at 2.99995269703212V on 6V range

 Measured: 2.999114V low limit: 2.998895V high limit: 2.999255V
- %PASS Slot 17 channel 1 raw DAC codes linearity at -.1V on 6V range

 Measured: -0.09999783V low limit: -0.1001221V high limit: -9.976212E-02V
- %PASS Slot 17 channel 1 raw DAC codes maximum linearity error on 6V range Measured: 5.570922E-05V high limit: 0.00018V
- %PASS Slot 17 channel 1 raw DAC code binary transition 0 to 1 on 6V range Measured: 8.787500E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 1 to 2 on 6V range Measured: 9.851400E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 2 to 3 on 6V range Measured: 9.365399E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

- %PASS Slot 17 channel 1 raw DAC code binary transition 3 to 4 on 6V range Measured: 9.536200E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 7 to 8 on 6V range Measured: 1.049509E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 15 to 16 on 6V range Measured: 1.121749E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 31 to 32 on 6V range Measured: 1.017550E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 63 to 64 on 6V range Measured: 1.041190E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 127 to 128 on 6V range Measured: 1.046440E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 255 to 256 on 6V range Measured: 1.015359E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 1 raw DAC code binary transition 511 to 512 on 6V range Measured: 1.088029E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- % PASS Slot 17 channel 1 raw DAC code binary transition $\,$ 1023 to $\,$ 1024 on 6V range

Measured: 1.044690E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transition $\,$ 2047 to $\,$ 2048 on 6V range

Measured: 1.035930E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transition $\,$ 4095 to $\,$ 4096 on 6V range

Measured: $8.406600\mbox{E-}05\mbox{V}$ low limit: $\mbox{-}8.539550\mbox{E-}05\mbox{V}$ high limit: $2.746044\mbox{E-}04\mbox{V}$

%PASS - Slot 17 channel 1 raw DAC code binary transition $\,$ 8191 to $\,$ 8192 on 6V range

Measured: 6.891700E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transition $\,$ 16383 to $\,$ 16384 on 6V range

Measured: 8.726200E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transition 32767 to 32768 on 6V range

Measured: 9.930199E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

 $\mbox{\%PASS}$ - Slot 17 channel 1 raw DAC code binary transitions maximum difference on $6\mbox{V}$ range

Measured: 1.121749E-04V high limit: 2.746044E-04V

 $\mbox{\%PASS}$ - Slot 17 channel 1 raw DAC code binary transitions minimum difference on $\mbox{6V range}$

Measured: 6.891700E-05V low limit: -8.539550E-05V

- %PASS Slot 17 channel 2 raw DAC codes linearity at 3.05V on 3V range Measured: 3.049238V low limit: 3.049160V high limit: 3.049340V
- $\mbox{\%PASS}$ Slot 17 channel 2 raw DAC codes linearity at $\,$ 3.04995269703212V on 3V range

- Measured: 3.049196V low limit: 3.049112V high limit: 3.049292V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 3.04990539406424V on 3V range

 Measured: 3.049153V low limit: 3.049065V high limit: 3.049245V
- % PASS - Slot 17 channel 2 raw DAC codes linearity at $\,$ 3.04985809109636 V on 3V range

Measured: 3.049107V low limit: 3.049018V high limit: 3.049198V

- %PASS Slot 17 channel 2 raw DAC codes linearity at 3.04981078812848V on 3V range

 Measured: 3.049061V low limit: 3.048970V high limit: 3.049150V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 3.04966887922484V on 3V range

 Measured: 3.048922V low limit: 3.048829V high limit: 3.049009V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 3.04962157625696V on 3V range

 Measured: 3.048872V low limit: 3.048781V high limit: 3.048961V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 3.0492904554818V on 3V range Measured: 3.048543V low limit: 3.048450V high limit: 3.048630V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 3.04924315251392V on 3V range

 Measured: 3.048494V low limit: 3.048403V high limit: 3.048583V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 3.04853360799573V on 3V range

 Measured: 3.047787V low limit: 3.047694V high limit: 3.047874V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 3.04848630502785V on 3V range

 Measured: 3.047739V low limit: 3.047647V high limit: 3.047827V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 3.04701991302358V on 3V

Measured: 3.046272V low limit: 3.046181V high limit: 3.046361V

- %PASS Slot 17 channel 2 raw DAC codes linearity at 3.0469726100557V on 3V range Measured: 3.046225V low limit: 3.046134V high limit: 3.046314V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 3.04399252307927V on 3V range

Measured: 3.043248V low limit: 3.043155V high limit: 3.043335V

%PASS - Slot 17 channel 2 raw DAC codes linearity at $\,$ 3.04394522011139V on 3V range

Measured: 3.043197V low limit: 3.043108V high limit: 3.043288V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 3.03793774319066V on 3V range

Measured: 3.037197V low limit: 3.037103V high limit: 3.037283V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 3.03789044022278V on 3V range

Measured: 3.037146V low limit: 3.037056V high limit: 3.037236V

%PASS - Slot 17 channel 2 raw DAC codes linearity at $\,$ 3.02582818341344V on 3V range

Measured: 3.025093V low limit: 3.025000V high limit: 3.025180V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 3.02578088044556V on 3V range

Measured: 3.025041V low limit: 3.024953V high limit: 3.025133V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 3.00160906385901V on 3V range

Measured: 3.000884V low limit: 3.000794V high limit: 3.000974V

%PASS - Slot 17 channel 2 raw DAC codes linearity at $\,$ 3.00156176089113V on 3V range

Measured: 3.000830V low limit: 3.000746V high limit: 3.000926V

- %PASS Slot 17 channel 2 raw DAC codes linearity at 2.95317082475013V on 3V range
 - Measured: 2.952474V low limit: 2.952380V high limit: 2.952560V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 2.95312352178225V on 3V range
 - Measured: 2.952417V low limit: 2.952333V high limit: 2.952513V
- %PASS Slot 17 channel 2 raw DAC codes linearity at $\,$ 2.85629434653239V on 3V range
 - Measured: 2.855630V low limit: 2.855554V high limit: 2.855734V
- %PASS Slot 17 channel 2 raw DAC codes linearity at $\,$ 2.85624704356451V on 3V range
 - Measured: 2.855593V low limit: 2.855506V high limit: 2.855686V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 2.66254139009689V on 3V range
 - Measured: 2.661975V low limit: 2.661901V high limit: 2.662081V
- %PASS Slot 17 channel 2 raw DAC codes linearity at $\,$ 2.66249408712902V on 3V range
 - Measured: 2.661942V low limit: 2.661853V high limit: 2.662033V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 2.27503547722591V on 3V range

 Measured: 2.274684V low limit: 2.274595V high limit: 2.274775V
- % PASS - Slot 17 channel 2 raw DAC codes linearity at $\,$ 2.27498817425803 V on 3V range
 - Measured: 2.274643V low limit: 2.274547V high limit: 2.274727V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 1.50002365148394V on 3V range

 Measured: 1.500104V low limit: 1.499983V high limit: 1.500163V
- % PASS - Slot 17 channel 2 raw DAC codes linearity at $\,$ 1.49997634851606 V on 3V range

- Measured: 1.500054V low limit: 1.499935V high limit: 1.500115V
- %PASS Slot 17 channel 2 raw DAC codes linearity at -.05V on 3V range Measured: -0.04917929V low limit: -4.924124E-02V high limit: -4.906124E-02V
- %PASS Slot 17 channel 2 raw DAC codes maximum linearity error on 3V range Measured: 3.108331E-05V high limit: 0.00009V
- %PASS Slot 17 channel 2 raw DAC code binary transition 0 to 1 on 3V range Measured: 4.251399E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 1 to 2 on 3V range Measured: 4.290900E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 2 to 3 on 3V range Measured: 4.549199E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 3 to 4 on 3V range Measured: 4.614800E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 7 to 8 on 3V range Measured: 4.973799E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 15 to 16 on 3V range Measured: 4.877500E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 31 to 32 on 3V range Measured: 4.811900E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 63 to 64 on 3V range

Measured: $4.763799 \hbox{E-}05 V$ low limit: $-4.269775 \hbox{E-}05 V$ high limit: $1.373022 \hbox{E-}04 V$

- %PASS Slot 17 channel 2 raw DAC code binary transition 127 to 128 on 3V range Measured: 5.131499E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 255 to 256 on 3V range Measured: 5.039599E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 511 to 512 on 3V range Measured: 5.223500E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 1023 to 1024 on 3V range

 Measured: 5.385399E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 2047 to 2048 on 3V range

 Measured: 5.687499E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 8191 to 8192 on 3V range

 Measured: 3.314500E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 16383 to 16384 on 3V range

 Measured: 4.067500E-05V low limit: -4.269775E-05V high limit:

%PASS - Slot 17 channel 2 raw DAC code binary transition 32767 to 32768 on 3V range

Measured: 4.965199E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

 $\mbox{\%PASS}$ - Slot 17 channel 2 raw DAC code binary transitions maximum difference on $3\mbox{V}$ range

Measured: 5.687499E-05V high limit: 1.373022E-04V

 $\mbox{\it \%PASS}$ - Slot 17 channel 2 raw DAC code binary transitions minimum difference on 3V range

Measured: 3.314500E-05V low limit: -4.269775E-05V

- %PASS Slot 17 channel 2 raw DAC codes linearity at 6.1V on 6V range Measured: 6.096114V low limit: 6.095937V high limit: 6.096297V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 6.09990539406424V on 6V range

 Measured: 6.096017V low limit: 6.095842V high limit: 6.096202V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 6.09981078812848V on 6V range

 Measured: 6.095923V low limit: 6.095748V high limit: 6.096108V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 6.09971618219272V on 6V range

 Measured: 6.095826V low limit: 6.095653V high limit: 6.096013V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 6.09962157625696V on 6V range

 Measured: 6.095726V low limit: 6.095559V high limit: 6.095919V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 6.09933775844968V on 6V range

Measured: 6.095447V low limit: 6.095275V high limit: 6.095635V

- %PASS Slot 17 channel 2 raw DAC codes linearity at $\,$ 6.09924315251392V on 6V range
 - Measured: 6.095351V low limit: 6.095181V high limit: 6.095541V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 6.09858091096361V on 6V range
 - Measured: 6.094695V low limit: 6.094519V high limit: 6.094879V
- % PASS - Slot 17 channel 2 raw DAC codes linearity at $\,$ 6.09848630502785 V on 6V range
 - Measured: 6.094598V low limit: 6.094424V high limit: 6.094784V
- %PASS Slot 17 channel 2 raw DAC codes linearity at $\,$ 6.09706721599146V on 6V range
 - Measured: 6.093180V low limit: 6.093007V high limit: 6.093367V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 6.0969726100557V on 6V range Measured: 6.093089V low limit: 6.092912V high limit: 6.093272V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 6.09403982604715V on 6V range

 Measured: 6.090157V low limit: 6.089982V high limit: 6.090342V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 6.09394522011139V on 6V range

 Measured: 6.090061V low limit: 6.089888V high limit: 6.090248V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 6.08798504615854V on 6V range

 Measured: 6.084108V low limit: 6.083933V high limit: 6.084293V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 6.08789044022278V on 6V range

 Measured: 6.084010V low limit: 6.083838V high limit: 6.084198V
- %PASS Slot 17 channel 2 raw DAC codes linearity at $\,$ 6.07587548638132V on 6V range $\,$ Measured: 6.072014V low limit: 6.071835V high limit: 6.072195V $\,$

- %PASS Slot 17 channel 2 raw DAC codes linearity at 6.07578088044556V on 6V range

 Measured: 6.071917V low limit: 6.071740V high limit: 6.072100V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 6.05165636682689V on 6V range

 Measured: 6.047821V low limit: 6.047638V high limit: 6.047998V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 6.05156176089113V on 6V range

 Measured: 6.047722V low limit: 6.047544V high limit: 6.047904V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 6.00321812771801V on 6V range

 Measured: 5.999418V low limit: 5.999245V high limit: 5.999605V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 6.00312352178225V on 6V range

 Measured: 5.999321V low limit: 5.999151V high limit: 5.999511V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 5.90634164950027V on 6V range

 Measured: 5.902629V low limit: 5.902459V high limit: 5.902819V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 5.90624704356451V on 6V range

 Measured: 5.902535V low limit: 5.902364V high limit: 5.902724V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 5.71258869306477V on 6V range

 Measured: 5.709049V low limit: 5.708886V high limit: 5.709246V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 5.71249408712902V on 6V range Measured: 5.708968V low limit: 5.708792V high limit: 5.709152V
- %PASS Slot 17 channel 2 raw DAC codes linearity at 5.32508278019379V on 6V

Measured: 5.321917V low limit: 5.321741V high limit: 5.322101V

% PASS - Slot 17 channel 2 raw DAC codes linearity at $\,$ 5.32498817425803V on 6V range

Measured: 5.321840V low limit: 5.321647V high limit: 5.322007V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 4.55007095445182V on 6V range

Measured: 4.547661V low limit: 4.547451V high limit: 4.547811V

% PASS - Slot 17 channel 2 raw DAC codes linearity at $\,$ 4.54997634851606 V on 6V range

Measured: 4.547581V low limit: 4.547357V high limit: 4.547717V

% PASS - Slot 17 channel 2 raw DAC codes linearity at $\,$ 3.00004730296788V on 6V range

Measured: 2.999148V low limit: 2.998871V high limit: 2.999231V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 2.99995269703212V on 6V range

Measured: 2.999044V low limit: 2.998777V high limit: 2.999137V

%PASS - Slot 17 channel 2 raw DAC codes linearity at -.1V on 6V range Measured: -0.09821762V low limit: -9.828820E-02V high limit: -9.792820E-02V

- %PASS Slot 17 channel 2 raw DAC codes maximum linearity error on 6V range Measured: 1.094244E-04V high limit: 0.00018V
- %PASS Slot 17 channel 2 raw DAC code binary transition 0 to 1 on 6V range Measured: 9.711299E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 1 to 2 on 6V range Measured: 9.347900E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

- %PASS Slot 17 channel 2 raw DAC code binary transition 2 to 3 on 6V range Measured: 9.737599E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 3 to 4 on 6V range Measured: 9.965300E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 7 to 8 on 6V range Measured: 9.579899E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition $\,$ 15 to $\,$ 16 on 6V range Measured: 9.671900E-05V low limit: -8.539550E-05V high limit: $\,$ 2.746044E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 31 to 32 on 6V range Measured: 9.089599E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 63 to 64 on 6V range Measured: 9.601800E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 127 to 128 on 6V range Measured: 9.860200E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 255 to 256 on 6V range Measured: 9.772599E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 2 raw DAC code binary transition 511 to 512 on 6V range Measured: 9.921500E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- $\mbox{\%PASS}$ Slot 17 channel 2 raw DAC code binary transition $\,$ 1023 to $\,$ 1024 on 6V range

Measured: 9.702599E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transition $\,$ 2047 to $\,$ 2048 on 6V range

Measured: 9.431099E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transition $\,$ 4095 to $\,$ 4096 on 6V $\,$ range

Measured: 8.025699E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transition $\,$ 8191 to $\,$ 8192 on 6V range

Measured: 7.605299E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transition $\,$ 16383 to $\,$ 16384 on 6V $\,$ range

Measured: 7.990599E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

% PASS - Slot 17 channel 2 raw DAC code binary transition $\,$ 32767 to $\,$ 32768 on 6V range

Measured: 1.037690E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

 $\mbox{\%PASS}$ - Slot 17 channel 2 raw DAC code binary transitions maximum difference on $6\mbox{V}$ range

Measured: 1.037690E-04V high limit: 2.746044E-04V

 $\mbox{\%PASS}$ - Slot 17 channel 2 raw DAC code binary transitions minimum difference on $\mbox{6V range}$

Measured: 7.605299E-05V low limit: -8.539550E-05V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 3.05V on 3V range Measured: 3.050285V low limit: 3.050200V high limit: 3.050380V

- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.04995269703212V on 3V range

 Measured: 3.050240V low limit: 3.050153V high limit: 3.050333V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.04990539406424V on 3V range

 Measured: 3.050194V low limit: 3.050105V high limit: 3.050285V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.04985809109636V on 3V range

 Measured: 3.050147V low limit: 3.050058V high limit: 3.050238V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.04981078812848V on 3V range

 Measured: 3.050099V low limit: 3.050011V high limit: 3.050191V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.04966887922484V on 3V range

 Measured: 3.049962V low limit: 3.049869V high limit: 3.050049V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.04962157625696V on 3V range

 Measured: 3.049914V low limit: 3.049821V high limit: 3.050001V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.0492904554818V on 3V range Measured: 3.049581V low limit: 3.049490V high limit: 3.049670V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.04924315251392V on 3V range

 Measured: 3.049533V low limit: 3.049443V high limit: 3.049623V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.04853360799573V on 3V range

 Measured: 3.048830V low limit: 3.048733V high limit: 3.048913V
- %PASS Slot 17 channel 3 raw DAC codes linearity at $\,$ 3.04848630502785V on 3V range

- Measured: 3.048778V low limit: 3.048686V high limit: 3.048866V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.04701991302358V on 3V range

 Measured: 3.047313V low limit: 3.047220V high limit: 3.047400V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.0469726100557V on 3V range Measured: 3.047265V low limit: 3.047172V high limit: 3.047352V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.04399252307927V on 3V range

 Measured: 3.044286V low limit: 3.044192V high limit: 3.044372V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.04394522011139V on 3V range

 Measured: 3.044236V low limit: 3.044145V high limit: 3.044325V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.03793774319066V on 3V range

 Measured: 3.038231V low limit: 3.038137V high limit: 3.038317V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.03789044022278V on 3V range

 Measured: 3.038180V low limit: 3.038089V high limit: 3.038269V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.02582818341344V on 3V range

 Measured: 3.026117V low limit: 3.026026V high limit: 3.026206V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.02578088044556V on 3V range

 Measured: 3.026065V low limit: 3.025979V high limit: 3.026159V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.00160906385901V on 3V range

 Measured: 3.001892V low limit: 3.001805V high limit: 3.001985V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.00156176089113V on 3V

Measured: 3.001844V low limit: 3.001758V high limit: 3.001938V

%PASS - Slot 17 channel 3 raw DAC codes linearity at $\,$ 2.95317082475013V on 3V range

Measured: 2.953445V low limit: 2.953362V high limit: 2.953542V

%PASS - Slot 17 channel 3 raw DAC codes linearity at $\,$ 2.95312352178225V on 3V range

Measured: 2.953398V low limit: 2.953315V high limit: 2.953495V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 2.85629434653239V on 3V range

Measured: 2.856556V low limit: 2.856478V high limit: 2.856658V

% PASS - Slot 17 channel 3 raw DAC codes linearity at $\,$ 2.85624704356451V on 3V range

Measured: 2.856515V low limit: 2.856431V high limit: 2.856611V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 2.66254139009689V on 3V range

Measured: 2.662786V low limit: 2.662708V high limit: 2.662888V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 2.66249408712902V on 3V range

Measured: 2.662750V low limit: 2.662661V high limit: 2.662841V

%PASS - Slot 17 channel 3 raw DAC codes linearity at $\,$ 2.27503547722591V on 3V range

Measured: 2.275263V low limit: 2.275170V high limit: 2.275350V

%PASS - Slot 17 channel 3 raw DAC codes linearity at $\,$ 2.27498817425803V on 3V range

Measured: 2.275216V low limit: 2.275122V high limit: 2.275302V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 1.50002365148394V on 3V range

Measured: 1.500208V low limit: 1.500092V high limit: 1.500272V

- %PASS Slot 17 channel 3 raw DAC codes linearity at 1.49997634851606V on 3V range

 Measured: 1.500155V low limit: 1.500045V high limit: 1.500225V
- %PASS Slot 17 channel 3 raw DAC codes linearity at -.05V on 3V range Measured: -0.04999323V low limit: -5.006186E-02V high limit: -4.988186E-02V
- %PASS Slot 17 channel 3 raw DAC codes maximum linearity error on 3V range Measured: 2.584543E-05V high limit: 0.00009V
- %PASS Slot 17 channel 3 raw DAC code binary transition 0 to 1 on 3V range Measured: 4.501000E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 1 to 2 on 3V range Measured: 4.623599E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 2 to 3 on 3V range Measured: 4.737499E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 3 to 4 on 3V range Measured: 4.733100E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 7 to 8 on 3V range Measured: 4.859999E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 15 to 16 on 3V range Measured: 4.838100E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 31 to 32 on 3V range Measured: 5.240999E-05V low limit: -4.269775E-05V high limit:

- %PASS Slot 17 channel 3 raw DAC code binary transition 63 to 64 on 3V range Measured: 4.759299E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 127 to 128 on 3V range Measured: 5.061500E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 255 to 256 on 3V range Measured: 5.070199E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 511 to 512 on 3V range Measured: 5.192799E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 1023 to 1024 on 3V range

 Measured: 4.825099E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 2047 to 2048 on 3V range

 Measured: 4.724300E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 4095 to 4096 on 3V range

 Measured: 4.032500E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- % PASS Slot 17 channel 3 raw DAC code binary transition $\,$ 8191 to $\,$ 8192 on 3V range

Measured: 3.581499E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition $\,$ 16383 to $\,$ 16384 on 3V range

Measured: $4.676099\hbox{E-}05V$ low limit: $\text{-}4.269775\hbox{E-}05V$ high limit: $1.373022\hbox{E-}04V$

%PASS - Slot 17 channel 3 raw DAC code binary transition 32767 to 32768 on 3V range

Measured: 5.337299E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

 $\mbox{\%PASS}$ - Slot 17 channel 3 raw DAC code binary transitions maximum difference on 3V range

Measured: 5.337299E-05V high limit: 1.373022E-04V

 $\mbox{\%PASS}$ - Slot 17 channel 3 raw DAC code binary transitions minimum difference on 3V range

Measured: 3.581499E-05V low limit: -4.269775E-05V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 6.1V on 6V range Measured: 6.098411V low limit: 6.098229V high limit: 6.098589V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 6.09990539406424V on 6V range

Measured: 6.098309V low limit: 6.098134V high limit: 6.098494V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 6.09981078812848V on 6V range

Measured: 6.098214V low limit: 6.098040V high limit: 6.098400V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 6.09971618219272V on 6V range

Measured: 6.098122V low limit: 6.097945V high limit: 6.098305V

%PASS - Slot 17 channel 3 raw DAC codes linearity at $\,$ 6.09962157625696V on 6V range

Measured: 6.098020V low limit: 6.097851V high limit: 6.098211V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 6.09933775844968V on 6V

Measured: 6.097743V low limit: 6.097567V high limit: 6.097927V

%PASS - Slot 17 channel 3 raw DAC codes linearity at $\,$ 6.09924315251392V on 6V range

Measured: 6.097640V low limit: 6.097472V high limit: 6.097832V

%PASS - Slot 17 channel 3 raw DAC codes linearity at $\,$ 6.09858091096361V on 6V range

Measured: 6.096987V low limit: 6.096810V high limit: 6.097170V

%PASS - Slot 17 channel 3 raw DAC codes linearity at $\,$ 6.09848630502785V on 6V range

Measured: 6.096882V low limit: 6.096716V high limit: 6.097076V

% PASS - Slot 17 channel 3 raw DAC codes linearity at $\,$ 6.09706721599146V on 6V range

Measured: 6.095473V low limit: 6.095297V high limit: 6.095657V

- %PASS Slot 17 channel 3 raw DAC codes linearity at 6.0969726100557V on 6V range Measured: 6.095375V low limit: 6.095203V high limit: 6.095563V
- %PASS Slot 17 channel 3 raw DAC codes linearity at $\,$ 6.09403982604715V on 6V range

Measured: 6.092450V low limit: 6.092271V high limit: 6.092631V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 6.09394522011139V on 6V range

Measured: 6.092349V low limit: 6.092176V high limit: 6.092536V

%PASS - Slot 17 channel 3 raw DAC codes linearity at $\,$ 6.08798504615854V on 6V range

Measured: 6.086399V low limit: 6.086219V high limit: 6.086579V

%PASS - Slot 17 channel 3 raw DAC codes linearity at $\,$ 6.08789044022278V on 6V range

Measured: 6.086291V low limit: 6.086124V high limit: 6.086484V

- %PASS Slot 17 channel 3 raw DAC codes linearity at 6.07587548638132V on 6V range
 - Measured: 6.074292V low limit: 6.074114V high limit: 6.074474V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 6.07578088044556V on 6V range
 - Measured: 6.074187V low limit: 6.074019V high limit: 6.074379V
- $\rm \%PASS$ Slot 17 channel 3 raw DAC codes linearity at $\,$ 6.05165636682689V on 6V range
 - Measured: 6.050086V low limit: 6.049904V high limit: 6.050264V
- % PASS Slot 17 channel 3 raw DAC codes linearity at $\,$ 6.05156176089113V on 6V range
 - Measured: 6.049979V low limit: 6.049809V high limit: 6.050169V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 6.00321812771801V on 6V range

 Measured: 6.001661V low limit: 6.001484V high limit: 6.001844V
- % PASS Slot 17 channel 3 raw DAC codes linearity at $\,$ 6.00312352178225V on 6V range
 - Measured: 6.001558V low limit: 6.001389V high limit: 6.001749V
- $\mbox{\%PASS}$ Slot 17 channel 3 raw DAC codes linearity at $\,$ 5.90634164950027V on 6V range
 - Measured: 5.904815V low limit: 5.904644V high limit: 5.905004V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 5.90624704356451V on 6V range

 Measured: 5.904714V low limit: 5.904549V high limit: 5.904909V
- %PASS Slot 17 channel 3 raw DAC codes linearity at $\,$ 5.71258869306477V on 6V range
 - Measured: 5.711127V low limit: 5.710964V high limit: 5.711324V
- $\mbox{\%PASS}$ Slot 17 channel 3 raw DAC codes linearity at $\,$ 5.71249408712902V on 6V range

- Measured: 5.711048V low limit: 5.710869V high limit: 5.711229V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 5.32508278019379V on 6V range

 Measured: 5.323785V low limit: 5.323603V high limit: 5.323963V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 5.32498817425803V on 6V range

 Measured: 5.323712V low limit: 5.323509V high limit: 5.323869V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 4.55007095445182V on 6V range

 Measured: 4.549108V low limit: 4.548883V high limit: 4.549243V
- %PASS Slot 17 channel 3 raw DAC codes linearity at 3.00004730296788V on 6V range

 Measured: 2.999705V low limit: 2.999443V high limit: 2.999803V
- %PASS Slot 17 channel 3 raw DAC codes linearity at -.1V on 6V range Measured: -0.09935526V low limit: -9.943658E-02V high limit: -9.907658E-02V
- %PASS Slot 17 channel 3 raw DAC codes maximum linearity error on 6V range Measured: 9.868546E-05V high limit: 0.00018V
- %PASS Slot 17 channel 3 raw DAC code binary transition 0 to 1 on 6V range Measured: 1.012730E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

- %PASS Slot 17 channel 3 raw DAC code binary transition 1 to 2 on 6V range Measured: 9.518700E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 2 to 3 on 6V range Measured: 9.247199E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 3 to 4 on 6V range Measured: 1.014480E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 7 to 8 on 6V range Measured: 1.026739E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 15 to 16 on 6V range Measured: 1.047749E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 31 to 32 on 6V range Measured: 9.829499E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 63 to 64 on 6V range Measured: 1.002650E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 127 to 128 on 6V range Measured: 1.080149E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 255 to 256 on 6V range Measured: 1.050820E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 3 raw DAC code binary transition 511 to 512 on 6V range Measured: 1.069210E-04V low limit: -8.539550E-05V high limit:

%PASS - Slot 17 channel 3 raw DAC code binary transition $\,$ 1023 to $\,$ 1024 on 6V range

Measured: 1.030250E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition $\,$ 2047 to $\,$ 2048 on 6V range

Measured: 1.008349E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition $\,$ 4095 to $\,$ 4096 on 6V range

Measured: 7.903000E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition 8191 to 8192 on 6V range

Measured: 7.342599E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition $\,$ 16383 to $\,$ 16384 on 6V range

Measured: 9.286600E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

% PASS - Slot 17 channel 3 raw DAC code binary transition $\,$ 32767 to $\,$ 32768 on 6V range

Measured: $1.079279 \hbox{E-}04 V$ low limit: -8.539550 \hbox{E-}05 V high limit: } $2.746044 \hbox{E-}04 V$

 $\mbox{\%PASS}$ - Slot 17 channel 3 raw DAC code binary transitions maximum difference on $6\mbox{V}$ range

Measured: 1.080149E-04V high limit: 2.746044E-04V

 $\mbox{\%PASS}$ - Slot 17 channel 3 raw DAC code binary transitions minimum difference on $6\mbox{V}$ range

- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.05V on 3V range Measured: 3.050475V low limit: 3.050389V high limit: 3.050569V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.04995269703212V on 3V range

 Measured: 3.050428V low limit: 3.050342V high limit: 3.050522V
- % PASS Slot 17 channel 4 raw DAC codes linearity at $\,$ 3.04990539406424V on 3V range

Measured: 3.050381V low limit: 3.050295V high limit: 3.050475V

%PASS - Slot 17 channel 4 raw DAC codes linearity at $\,$ 3.04985809109636V on 3V range

Measured: 3.050331V low limit: 3.050247V high limit: 3.050427V

 $\mbox{\%PASS}$ - Slot 17 channel 4 raw DAC codes linearity at $\,$ 3.04981078812848V on 3V range

Measured: 3.050287V low limit: 3.050200V high limit: 3.050380V

- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.04966887922484V on 3V range

 Measured: 3.050144V low limit: 3.050058V high limit: 3.050238V
- % PASS - Slot 17 channel 4 raw DAC codes linearity at $\,$ 3.04962157625696V on 3V range

Measured: 3.050102V low limit: 3.050011V high limit: 3.050191V

- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.0492904554818V on 3V range Measured: 3.049768V low limit: 3.049680V high limit: 3.049860V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.04924315251392V on 3V range

 Measured: 3.049723V low limit: 3.049632V high limit: 3.049812V
- %PASS Slot 17 channel 4 raw DAC codes linearity at $\,$ 3.04853360799573V on 3V range

- Measured: 3.049012V low limit: 3.048923V high limit: 3.049103V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.04848630502785V on 3V range

 Measured: 3.048968V low limit: 3.048876V high limit: 3.049056V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.04701991302358V on 3V range
 Measured: 3.047499V low limit: 3.047410V high limit: 3.047590V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.0469726100557V on 3V range Measured: 3.047453V low limit: 3.047362V high limit: 3.047542V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.04399252307927V on 3V range

 Measured: 3.044475V low limit: 3.044383V high limit: 3.044563V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.04394522011139V on 3V range

 Measured: 3.044425V low limit: 3.044336V high limit: 3.044516V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.03793774319066V on 3V range

 Measured: 3.038418V low limit: 3.038329V high limit: 3.038509V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.03789044022278V on 3V range
 Measured: 3.038374V low limit: 3.038282V high limit: 3.038462V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.02582818341344V on 3V range

 Measured: 3.026310V low limit: 3.026222V high limit: 3.026402V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.02578088044556V on 3V range

 Measured: 3.026264V low limit: 3.026175V high limit: 3.026355V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.00160906385901V on 3V

Measured: 3.002094V low limit: 3.002008V high limit: 3.002188V

%PASS - Slot 17 channel 4 raw DAC codes linearity at $\,$ 3.00156176089113V on 3V range

Measured: 3.002051V low limit: 3.001961V high limit: 3.002141V

%PASS - Slot 17 channel 4 raw DAC codes linearity at $\,$ 2.95317082475013V on 3V range

Measured: 2.953665V low limit: 2.953580V high limit: 2.953760V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 2.95312352178225V on 3V range

Measured: 2.953621V low limit: 2.953533V high limit: 2.953713V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 2.85629434653239V on 3V range

Measured: 2.856806V low limit: 2.856724V high limit: 2.856904V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 2.85624704356451V on 3V range

Measured: 2.856776V low limit: 2.856676V high limit: 2.856856V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 2.66254139009689V on 3V range

Measured: 2.663094V low limit: 2.663011V high limit: 2.663191V

% PASS - Slot 17 channel 4 raw DAC codes linearity at $\,$ 2.66249408712902V on 3V range

Measured: 2.663062V low limit: 2.662964V high limit: 2.663144V

%PASS - Slot 17 channel 4 raw DAC codes linearity at $\,$ 2.27503547722591V on 3V range

Measured: 2.275682V low limit: 2.275585V high limit: 2.275765V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 2.27498817425803V on 3V range

Measured: 2.275638V low limit: 2.275538V high limit: 2.275718V

- %PASS Slot 17 channel 4 raw DAC codes linearity at 1.50002365148394V on 3V range

 Measured: 1.500841V low limit: 1.500734V high limit: 1.500914V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 1.49997634851606V on 3V range

 Measured: 1.500799V low limit: 1.500687V high limit: 1.500867V
- %PASS Slot 17 channel 4 raw DAC codes linearity at -.05V on 3V range Measured: -0.04890182V low limit: -4.896813E-02V high limit: -4.878813E-02V
- %PASS Slot 17 channel 4 raw DAC codes maximum linearity error on 3V range Measured: 2.368557E-05V high limit: 0.00009V
- %PASS Slot 17 channel 4 raw DAC code binary transition 0 to 1 on 3V range Measured: 4.728700E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 1 to 2 on 3V range Measured: 4.763700E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 2 to 3 on 3V range Measured: 4.947699E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 3 to 4 on 3V range Measured: 4.430900E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 7 to 8 on 3V range Measured: 4.212099E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 15 to 16 on 3V range Measured: 4.492299E-05V low limit: -4.269775E-05V high limit:

- %PASS Slot 17 channel 4 raw DAC code binary transition 31 to 32 on 3V range Measured: 0.000043609V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 63 to 64 on 3V range Measured: 4.662999E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 127 to 128 on 3V range Measured: 5.052699E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 255 to 256 on 3V range Measured: 4.365300E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 511 to 512 on 3V range Measured: 4.592900E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 1023 to 1024 on 3V range

 Measured: 4.382799E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 2047 to 2048 on 3V range

 Measured: 4.465999E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- $\mbox{\%PASS}$ Slot 17 channel 4 raw DAC code binary transition $\,$ 8191 to $\,$ 8192 on 3V range

Measured: 3.213699E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition 16383 to 16384 on 3V range

Measured: $4.400300 \hbox{E-}05 V$ low limit: $-4.269775 \hbox{E-}05 V$ high limit: $1.373022 \hbox{E-}04 V$

%PASS - Slot 17 channel 4 raw DAC code binary transition 32767 to 32768 on 3V range

Measured: 4.229500E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

 $\mbox{\%PASS}$ - Slot 17 channel 4 raw DAC code binary transitions maximum difference on $3\mbox{V}$ range

Measured: 5.052699E-05V high limit: 1.373022E-04V

 $\mbox{\it \%PASS}$ - Slot 17 channel 4 raw DAC code binary transitions minimum difference on $3\mbox{\it V}$ range

Measured: 3.003599E-05V low limit: -4.269775E-05V

- %PASS Slot 17 channel 4 raw DAC codes linearity at 6.1V on 6V range Measured: 6.100678V low limit: 6.100485V high limit: 6.100845V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 6.09990539406424V on 6V range

 Measured: 6.100572V low limit: 6.100390V high limit: 6.100750V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 6.09981078812848V on 6V range

 Measured: 6.100470V low limit: 6.100296V high limit: 6.100656V

%PASS - Slot 17 channel 4 raw DAC codes linearity at $\,$ 6.09971618219272V on 6V range

Measured: 6.100376V low limit: 6.100201V high limit: 6.100561V

 $\mbox{\%PASS}$ - Slot 17 channel 4 raw DAC codes linearity at $\mbox{\ }$ 6.09962157625696V on 6V range

- Measured: 6.100286V low limit: 6.100107V high limit: 6.100467V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 6.09933775844968V on 6V range

 Measured: 6.099999V low limit: 6.099823V high limit: 6.100183V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 6.09924315251392V on 6V range

 Measured: 6.099907V low limit: 6.099728V high limit: 6.100088V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 6.09858091096361V on 6V range

 Measured: 6.099240V low limit: 6.099066V high limit: 6.099426V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 6.09848630502785V on 6V range

 Measured: 6.099149V low limit: 6.098972V high limit: 6.099332V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 6.09706721599146V on 6V range

 Measured: 6.097725V low limit: 6.097553V high limit: 6.097913V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 6.0969726100557V on 6V range Measured: 6.097634V low limit: 6.097458V high limit: 6.097818V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 6.09403982604715V on 6V range

 Measured: 6.094699V low limit: 6.094526V high limit: 6.094886V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 6.09394522011139V on 6V range

 Measured: 6.094610V low limit: 6.094432V high limit: 6.094792V
- %PASS Slot 17 channel 4 raw DAC codes linearity at $\,$ 6.08798504615854V on 6V range $\,$ Measured: 6.088650V low limit: 6.088473V high limit: 6.088833V $\,$
- %PASS Slot 17 channel 4 raw DAC codes linearity at 6.08789044022278V on 6V

Measured: 6.088555V low limit: 6.088379V high limit: 6.088739V

%PASS - Slot 17 channel 4 raw DAC codes linearity at $\,$ 6.07587548638132V on 6V range

Measured: 6.076542V low limit: 6.076367V high limit: 6.076727V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 6.07578088044556V on 6V range

Measured: 6.076451V low limit: 6.076272V high limit: 6.076632V

%PASS - Slot 17 channel 4 raw DAC codes linearity at $\,$ 6.05165636682689V on 6V range

Measured: 6.052324V low limit: 6.052154V high limit: 6.052514V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 6.05156176089113V on 6V range

Measured: 6.052240V low limit: 6.052059V high limit: 6.052419V

% PASS - Slot 17 channel 4 raw DAC codes linearity at $\,$ 6.00321812771801V on 6V range

Measured: 6.003894V low limit: 6.003729V high limit: 6.004089V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 6.00312352178225V on 6V range

Measured: 6.003816V low limit: 6.003634V high limit: 6.003994V

% PASS - Slot 17 channel 4 raw DAC codes linearity at $\,$ 5.90634164950027V on 6V range

Measured: 5.907048V low limit: 5.906878V high limit: 5.907238V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 5.90624704356451V on 6V range

Measured: 5.906958V low limit: 5.906783V high limit: 5.907143V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 5.71258869306477V on 6V range

Measured: 5.713334V low limit: 5.713176V high limit: 5.713536V

- %PASS Slot 17 channel 4 raw DAC codes linearity at 5.71249408712902V on 6V range

 Measured: 5.713281V low limit: 5.713081V high limit: 5.713441V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 5.32508278019379V on 6V range

 Measured: 5.325945V low limit: 5.325772V high limit: 5.326132V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 5.32498817425803V on 6V range

 Measured: 5.325877V low limit: 5.325677V high limit: 5.326037V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 4.55007095445182V on 6V range

 Measured: 4.551155V low limit: 4.550964V high limit: 4.551324V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 4.54997634851606V on 6V range

 Measured: 4.551073V low limit: 4.550869V high limit: 4.551229V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 3.00004730296788V on 6V range

 Measured: 3.001568V low limit: 3.001348V high limit: 3.001708V
- %PASS Slot 17 channel 4 raw DAC codes linearity at 2.99995269703212V on 6V range

 Measured: 3.001485V low limit: 3.001253V high limit: 3.001613V
- %PASS Slot 17 channel 4 raw DAC codes linearity at -.1V on 6V range Measured: -0.09775946V low limit: -9.788363E-02V high limit: -9.752363E-02V
- %PASS Slot 17 channel 4 raw DAC codes maximum linearity error on 6V range Measured: 5.583165E-05V high limit: 0.00018V
- %PASS Slot 17 channel 4 raw DAC code binary transition $\,$ 0 to $\,$ 1 on 6V range Measured: 1.063519E-04V low limit: -8.539550E-05V high limit:

- %PASS Slot 17 channel 4 raw DAC code binary transition 1 to 2 on 6V range Measured: 1.022800E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 2 to 3 on 6V range Measured: 9.312899E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 3 to 4 on 6V range Measured: 9.089600E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 7 to 8 on 6V range Measured: 9.177200E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 15 to 16 on 6V range Measured: 9.107200E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 31 to 32 on 6V range Measured: 9.063299E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 63 to 64 on 6V range Measured: 8.861999E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 127 to 128 on 6V range Measured: 9.514300E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 4 raw DAC code binary transition 255 to 256 on 6V range Measured: 9.177200E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition 511 to 512 on 6V range Measured: 8.411000E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition $\,$ 1023 to $\,$ 1024 on 6V range

Measured: 7.775999E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition $\,$ 2047 to $\,$ 2048 on 6V range

Measured: $9.080799 \hbox{E-}05 V$ low limit: $-8.539550 \hbox{E-}05 V$ high limit: $2.746044 \hbox{E-}04 V$

%PASS - Slot 17 channel 4 raw DAC code binary transition $\,$ 4095 to $\,$ 4096 on 6V range

Measured: $5.346099 \hbox{E-}05 V$ low limit: $-8.539550 \hbox{E-}05 V$ high limit: $2.746044 \hbox{E-}04 V$

 $\mbox{\%PASS}$ - Slot 17 channel 4 raw DAC code binary transition $\,$ 8191 to $\,$ 8192 on 6V $\,$ range

Measured: 6.760300E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition 16383 to 16384 on 6V range

Measured: 8.266399E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

 $\mbox{\%PASS}$ - Slot 17 channel 4 raw DAC code binary transition $\,$ 32767 to $\,$ 32768 on 6V $\,$ range

Measured: 8.336500E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

 $\mbox{\%PASS}$ - Slot 17 channel 4 raw DAC code binary transitions maximum difference on $6\mbox{V}$ range

Measured: 1.063519E-04V high limit: 2.746044E-04V

- $\mbox{\it \%PASS}$ Slot 17 channel 4 raw DAC code binary transitions minimum difference on $\mbox{\it 6V}$ range
 - Measured: 5.346099E-05V low limit: -8.539550E-05V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 3.05V on 3V range Measured: 3.049859V low limit: 3.049768V high limit: 3.049948V
- %PASS Slot 17 channel 5 raw DAC codes linearity at $\,$ 3.04995269703212V on 3V range
 - Measured: 3.049809V low limit: 3.049721V high limit: 3.049901V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 3.04990539406424V on 3V range
 - Measured: 3.049763V low limit: 3.049674V high limit: 3.049854V
- % PASS - Slot 17 channel 5 raw DAC codes linearity at $\,$ 3.04985809109636 V on 3V range
 - Measured: 3.049716V low limit: 3.049626V high limit: 3.049806V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 3.04981078812848V on 3V range

 Measured: 3.049669V low limit: 3.049579V high limit: 3.049759V
- % PASS - Slot 17 channel 5 raw DAC codes linearity at $\,$ 3.04966887922484V on 3V range
 - Measured: 3.049524V low limit: 3.049437V high limit: 3.049617V
- % PASS - Slot 17 channel 5 raw DAC codes linearity at $\,$ 3.04962157625696 V on 3V range
 - Measured: 3.049481V low limit: 3.049390V high limit: 3.049570V
- %PASS Slot 17 channel 5 raw DAC codes linearity at $\,$ 3.0492904554818V on 3V range Measured: 3.049150V low limit: 3.049059V high limit: 3.049239V
- % PASS Slot 17 channel 5 raw DAC codes linearity at $\;$ 3.04924315251392V on 3V range
 - Measured: 3.049100V low limit: 3.049011V high limit: 3.049191V

- %PASS Slot 17 channel 5 raw DAC codes linearity at 3.04853360799573V on 3V range
 - Measured: 3.048392V low limit: 3.048302V high limit: 3.048482V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 3.04848630502785V on 3V range
 - Measured: 3.048345V low limit: 3.048255V high limit: 3.048435V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 3.04701991302358V on 3V range
 - Measured: 3.046876V low limit: 3.046789V high limit: 3.046969V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 3.0469726100557V on 3V range Measured: 3.046831V low limit: 3.046741V high limit: 3.046921V
- %PASS Slot 17 channel 5 raw DAC codes linearity at $\,$ 3.04399252307927V on 3V range
 - Measured: 3.043850V low limit: 3.043762V high limit: 3.043942V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 3.04394522011139V on 3V range

 Measured: 3.043806V low limit: 3.043715V high limit: 3.043895V
- % PASS - Slot 17 channel 5 raw DAC codes linearity at $\,$ 3.03793774319066 V on 3V range
 - Measured: 3.037795V low limit: 3.037709V high limit: 3.037889V
- % PASS Slot 17 channel 5 raw DAC codes linearity at $\;$ 3.03789044022278V on 3V range
 - Measured: 3.037748V low limit: 3.037661V high limit: 3.037841V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 3.02582818341344V on 3V range

 Measured: 3.025688V low limit: 3.025602V high limit: 3.025782V
- %PASS Slot 17 channel 5 raw DAC codes linearity at $\,$ 3.02578088044556V on 3V range
 - Measured: 3.025645V low limit: 3.025554V high limit: 3.025734V

- %PASS Slot 17 channel 5 raw DAC codes linearity at 3.00160906385901V on 3V range

 Measured: 3.001475V low limit: 3.001388V high limit: 3.001568V
- % PASS - Slot 17 channel 5 raw DAC codes linearity at $\,$ 3.00156176089113 V on 3V range

Measured: 3.001425V low limit: 3.001341V high limit: 3.001521V

- %PASS Slot 17 channel 5 raw DAC codes linearity at 2.95317082475013V on 3V range

 Measured: 2.953043V low limit: 2.952960V high limit: 2.953140V
- % PASS Slot 17 channel 5 raw DAC codes linearity at $\,$ 2.95312352178225V on 3V range

Measured: 2.953000V low limit: 2.952913V high limit: 2.953093V

- %PASS Slot 17 channel 5 raw DAC codes linearity at 2.85629434653239V on 3V range
 - Measured: 2.856186V low limit: 2.856105V high limit: 2.856285V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 2.85624704356451V on 3V range
 - Measured: 2.856155V low limit: 2.856058V high limit: 2.856238V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 2.66254139009689V on 3V range
 - Measured: 2.662481V low limit: 2.662395V high limit: 2.662575V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 2.66249408712902V on 3V range
 - Measured: 2.662450 V low limit: 2.662348 V high limit: 2.662528 V
- %PASS Slot 17 channel 5 raw DAC codes linearity at $\,$ 2.27503547722591V on 3V range
 - Measured: 2.275067V low limit: 2.274975V high limit: 2.275155V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 2.27498817425803V on 3V

Measured: 2.275031V low limit: 2.274927V high limit: 2.275107V

- %PASS Slot 17 channel 5 raw DAC codes linearity at 1.50002365148394V on 3V range
 - Measured: 1.500242V low limit: 1.500134V high limit: 1.500314V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 1.49997634851606V on 3V range

 Measured: 1.500198V low limit: 1.500087V high limit: 1.500267V
- %PASS Slot 17 channel 5 raw DAC codes linearity at -.05V on 3V range Measured: -0.04948079V low limit: -4.954704E-02V high limit: -4.936704E-02V
- %PASS Slot 17 channel 5 raw DAC codes maximum linearity error on 3V range Measured: 2.374475E-05V high limit: 0.00009V
- %PASS Slot 17 channel 5 raw DAC code binary transition $\,$ 0 to $\,$ 1 on 3V range Measured: 5.000200E-05V low limit: -4.269775E-05V high limit: $\,$ 1.373022E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 1 to 2 on 3V range Measured: 4.601699E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 2 to 3 on 3V range Measured: 4.702400E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 3 to 4 on 3V range Measured: 4.715599E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 7 to 8 on 3V range Measured: 4.299600E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

- %PASS Slot 17 channel 5 raw DAC code binary transition 15 to 16 on 3V range Measured: 4.956299E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 31 to 32 on 3V range Measured: 4.671800E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 63 to 64 on 3V range Measured: 4.527199E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 127 to 128 on 3V range Measured: 4.435299E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 255 to 256 on 3V range Measured: 4.680500E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 511 to 512 on 3V range Measured: 4.369699E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 1023 to 1024 on 3V range

 Measured: 5.026500E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 2047 to 2048 on 3V range

 Measured: 4.347700E-05V low limit: -4.269775E-05V high limit:

1.373022E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition $\,$ 4095 to $\,$ 4096 on 3V $\,$ range

Measured: 3.051699E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition $\,$ 8191 to $\,$ 8192 on 3V range

Measured: $3.086800\mbox{E-}05\mbox{V}$ low limit: -4.269775 E-05V high limit: $1.373022\mbox{E-}04\mbox{V}$

%PASS - Slot 17 channel 5 raw DAC code binary transition $\,$ 16383 to $\,$ 16384 on 3V range

Measured: 3.581499E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition $\,$ 32767 to $\,$ 32768 on 3V range

Measured: $4.422199\hbox{E-}05V$ low limit: $-4.269775\hbox{E-}05V$ high limit: $1.373022\hbox{E-}04V$

 $\mbox{\it \%PASS}$ - Slot 17 channel 5 raw DAC code binary transitions maximum difference on 3V range

Measured: 5.026500E-05V high limit: 1.373022E-04V

 $\mbox{\it \%PASS}$ - Slot 17 channel 5 raw DAC code binary transitions minimum difference on $3\mbox{\it V}$ range

Measured: 3.051699E-05V low limit: -4.269775E-05V

- %PASS Slot 17 channel 5 raw DAC codes linearity at 6.1V on 6V range Measured: 6.098939V low limit: 6.098749V high limit: 6.099109V
- %PASS Slot 17 channel 5 raw DAC codes linearity at $\,$ 6.09990539406424V on 6V range

Measured: 6.098836V low limit: 6.098655V high limit: 6.099015V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 6.09981078812848V on 6V range

Measured: 6.098745V low limit: 6.098560V high limit: 6.098920V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 6.09971618219272V on 6V range

Measured: 6.098641V low limit: 6.098466V high limit: 6.098826V

- %PASS Slot 17 channel 5 raw DAC codes linearity at 6.09962157625696V on 6V range

 Measured: 6.098551V low limit: 6.098371V high limit: 6.098731V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 6.09933775844968V on 6V range

 Measured: 6.098260V low limit: 6.098087V high limit: 6.098447V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 6.09924315251392V on 6V range

 Measured: 6.098173V low limit: 6.097993V high limit: 6.098353V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 6.09858091096361V on 6V range

 Measured: 6.097513V low limit: 6.097331V high limit: 6.097691V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 6.09848630502785V on 6V range

 Measured: 6.097416V low limit: 6.097236V high limit: 6.097596V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 6.09706721599146V on 6V range

 Measured: 6.095994V low limit: 6.095818V high limit: 6.096178V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 6.0969726100557V on 6V range Measured: 6.095899V low limit: 6.095723V high limit: 6.096083V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 6.09403982604715V on 6V range

 Measured: 6.092960V low limit: 6.092792V high limit: 6.093152V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 6.09394522011139V on 6V range

 Measured: 6.092872V low limit: 6.092697V high limit: 6.093057V
- $\mbox{\%PASS}$ Slot 17 channel 5 raw DAC codes linearity at $\,$ 6.08798504615854V on 6V range

- Measured: 6.086916V low limit: 6.086739V high limit: 6.087099V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 6.08789044022278V on 6V range

 Measured: 6.086819V low limit: 6.086645V high limit: 6.087005V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 6.07587548638132V on 6V range

 Measured: 6.074810V low limit: 6.074634V high limit: 6.074994V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 6.05165636682689V on 6V range

 Measured: 6.050595V low limit: 6.050425V high limit: 6.050785V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 6.05156176089113V on 6V range

 Measured: 6.050504V low limit: 6.050330V high limit: 6.050690V
- %PASS Slot 17 channel 5 raw DAC codes linearity at $\,$ 6.00321812771801V on 6V range $\,$ Measured: 6.002173V low limit: 6.002006V high limit: 6.002366V $\,$
- %PASS Slot 17 channel 5 raw DAC codes linearity at 6.00312352178225V on 6V range

 Measured: 6.002082V low limit: 6.001912V high limit: 6.002272V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 5.90634164950027V on 6V range

 Measured: 5.905336V low limit: 5.905169V high limit: 5.905529V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 5.90624704356451V on 6V range

 Measured: 5.905249V low limit: 5.905074V high limit: 5.905434V

- %PASS Slot 17 channel 5 raw DAC codes linearity at $\,$ 5.71258869306477V on 6V range
 - Measured: 5.711655V low limit: 5.711493V high limit: 5.711853V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 5.71249408712902V on 6V range
 - Measured: 5.711588V low limit: 5.711399V high limit: 5.711759V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 5.32508278019379V on 6V range
 - Measured: 5.324316V low limit: 5.324143V high limit: 5.324503V
- % PASS Slot 17 channel 5 raw DAC codes linearity at $\,$ 5.32498817425803V on 6V range
 - Measured: 5.324250V low limit: 5.324048V high limit: 5.324408V
- %PASS Slot 17 channel 5 raw DAC codes linearity at $\,$ 4.55007095445182V on 6V range
 - Measured: 4.549636 V low limit: 4.549441 V high limit: 4.549801 V
- %PASS Slot 17 channel 5 raw DAC codes linearity at 4.54997634851606V on 6V range
 - Measured: 4.549561V low limit: 4.549347V high limit: 4.549707V
- $\mbox{\%PASS}$ Slot 17 channel 5 raw DAC codes linearity at $\,$ 2.99995269703212V on 6V range
 - Measured: 3.000177V low limit: 2.999944V high limit: 3.000304V
- %PASS Slot 17 channel 5 raw DAC codes linearity at -.1V on 6V range Measured: -0.09864841V low limit: -9.876614E-02V high limit: -9.840614E-02V
- %PASS Slot 17 channel 5 raw DAC codes maximum linearity error on 6V range Measured: 6.227726E-05V high limit: 0.00018V

- %PASS Slot 17 channel 5 raw DAC code binary transition 0 to 1 on 6V range Measured: 1.033310E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 1 to 2 on 6V range Measured: 9.094000E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 2 to 3 on 6V range Measured: 1.034619E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 3 to 4 on 6V range Measured: 9.032700E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 7 to 8 on 6V range Measured: 8.743700E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 15 to 16 on 6V range Measured: 9.711399E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 31 to 32 on 6V range Measured: 9.544999E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 63 to 64 on 6V range Measured: 8.844400E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 5 raw DAC code binary transition 127 to 128 on 6V range Measured: 9.733200E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- % PASS Slot 17 channel 5 raw DAC code binary transition $\,$ 255 to $\,$ 256 on 6V range

Measured: 9.308499E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

- %PASS Slot 17 channel 5 raw DAC code binary transition 511 to 512 on 6V range Measured: 9.128999E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- % PASS Slot 17 channel 5 raw DAC code binary transition $\,$ 1023 to $\,$ 1024 on 6V range

Measured: 9.168400E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition 2047 to 2048 on 6V range

Measured: 8.717499E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition $\,$ 4095 to $\,$ 4096 on 6V range

Measured: 6.755899E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition $\,$ 8191 to $\,$ 8192 on 6V range

Measured: 6.536999E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

% PASS - Slot 17 channel 5 raw DAC code binary transition $\,$ 16383 to $\,$ 16384 on 6V range

Measured: 7.460800E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition 32767 to 32768 on 6V range

Measured: 8.726200E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transitions maximum difference on

6V range

Measured: 1.034619E-04V high limit: 2.746044E-04V

 $\mbox{\%PASS}$ - Slot 17 channel 5 raw DAC code binary transitions minimum difference on $6\mbox{V}$ range

Measured: 6.536999E-05V low limit: -8.539550E-05V

- %PASS Slot 17 channel 6 raw DAC codes linearity at 3.05V on 3V range Measured: 3.049245V low limit: 3.049154V high limit: 3.049334V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 3.04995269703212V on 3V range

 Measured: 3.049195V low limit: 3.049107V high limit: 3.049287V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 3.04990539406424V on 3V range

 Measured: 3.049146V low limit: 3.049059V high limit: 3.049239V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 3.04985809109636V on 3V range

 Measured: 3.049098V low limit: 3.049012V high limit: 3.049192V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 3.04981078812848V on 3V range

 Measured: 3.049054V low limit: 3.048965V high limit: 3.049145V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 3.04966887922484V on 3V range

 Measured: 3.048911V low limit: 3.048823V high limit: 3.049003V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 3.04962157625696V on 3V range

 Measured: 3.048870V low limit: 3.048776V high limit: 3.048956V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 3.0492904554818V on 3V range Measured: 3.048535V low limit: 3.048445V high limit: 3.048625V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 3.04924315251392V on 3V

Measured: 3.048489V low limit: 3.048397V high limit: 3.048577V

%PASS - Slot 17 channel 6 raw DAC codes linearity at $\,$ 3.04853360799573V on 3V range

Measured: 3.047775V low limit: 3.047688V high limit: 3.047868V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 3.04848630502785V on 3V range

Measured: 3.047731V low limit: 3.047641V high limit: 3.047821V

%PASS - Slot 17 channel 6 raw DAC codes linearity at $\,$ 3.04701991302358V on 3V range

Measured: 3.046262V low limit: 3.046175V high limit: 3.046355V

- %PASS Slot 17 channel 6 raw DAC codes linearity at 3.0469726100557V on 3V range Measured: 3.046221V low limit: 3.046127V high limit: 3.046307V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 3.04399252307927V on 3V range

 Measured: 3.043235V low limit: 3.043148V high limit: 3.043328V

% PASS - Slot 17 channel 6 raw DAC codes linearity at $\,$ 3.04394522011139 V on 3V range

Measured: 3.043191V low limit: 3.043101V high limit: 3.043281V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 3.03793774319066V on 3V range

Measured: 3.037182V low limit: 3.037095V high limit: 3.037275V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 3.03789044022278V on 3V range

Measured: 3.037140V low limit: 3.037048V high limit: 3.037228V

%PASS - Slot 17 channel 6 raw DAC codes linearity at $\,$ 3.02582818341344V on 3V range

Measured: 3.025075V low limit: 3.024988V high limit: 3.025168V

- %PASS Slot 17 channel 6 raw DAC codes linearity at $\,$ 3.02578088044556V on 3V range
 - Measured: 3.025032V low limit: 3.024941V high limit: 3.025121V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 3.00160906385901V on 3V range
 - Measured: 3.000862V low limit: 3.000776V high limit: 3.000956V
- % PASS Slot 17 channel 6 raw DAC codes linearity at $\,$ 3.00156176089113V on 3V range
 - Measured: 3.000816V low limit: 3.000728V high limit: 3.000908V
- $\mbox{\%PASS}$ Slot 17 channel 6 raw DAC codes linearity at $\,$ 2.95317082475013V on 3V range
 - Measured: 2.952433V low limit: 2.952350V high limit: 2.952530V
- % PASS Slot 17 channel 6 raw DAC codes linearity at $\,$ 2.95312352178225V on 3V range
 - Measured: 2.952390V low limit: 2.952303V high limit: 2.952483V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 2.85629434653239V on 3V range
 - Measured: 2.855575V low limit: 2.855498V high limit: 2.855678V
- %PASS Slot 17 channel 6 raw DAC codes linearity at $\,$ 2.85624704356451V on 3V range
 - Measured: 2.855544V low limit: 2.855451V high limit: 2.855631V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 2.66254139009689V on 3V range
 - Measured: 2.661878V low limit: 2.661795V high limit: 2.661975V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 2.66249408712902V on 3V range
 - Measured: 2.661845V low limit: 2.661748V high limit: 2.661928V
- $\mbox{\%PASS}$ Slot 17 channel 6 raw DAC codes linearity at $\,$ 2.27503547722591V on 3V range

- Measured: 2.274483V low limit: 2.274389V high limit: 2.274569V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 2.27498817425803V on 3V range

 Measured: 2.274446V low limit: 2.274342V high limit: 2.274522V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 1.50002365148394V on 3V range

 Measured: 1.499690V low limit: 1.499577V high limit: 1.499757V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 1.49997634851606V on 3V range

 Measured: 1.499648V low limit: 1.499530V high limit: 1.499710V
- %PASS Slot 17 channel 6 raw DAC codes linearity at -.05V on 3V range Measured: -0.04998420V low limit: -5.004587E-02V high limit: -4.986587E-02V
- %PASS Slot 17 channel 6 raw DAC codes maximum linearity error on 3V range Measured: 2.833123E-05V high limit: 0.00009V
- %PASS Slot 17 channel 6 raw DAC code binary transition 0 to 1 on 3V range Measured: 5.013300E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 1 to 2 on 3V range Measured: 4.873199E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 2 to 3 on 3V range Measured: 4.803099E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition $\,$ 3 to $\,$ 4 on 3V range Measured: 4.431000E-05V low limit: -4.269775E-05V high limit: $\,$ 1.373022E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 7 to 8 on 3V range

- Measured: $4.063100\text{E}{-}05\text{V}$ low limit: $-4.269775\text{E}{-}05\text{V}$ high limit: $1.373022\text{E}{-}04\text{V}$
- %PASS Slot 17 channel 6 raw DAC code binary transition 15 to 16 on 3V range Measured: 4.610500E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 31 to 32 on 3V range Measured: 4.435299E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 63 to 64 on 3V range Measured: 4.111399E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 127 to 128 on 3V range Measured: 4.360999E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 255 to 256 on 3V range Measured: 4.260199E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 511 to 512 on 3V range Measured: 4.378499E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 1023 to 1024 on 3V range

 Measured: 4.641200E-05V low limit: -4.269775E-05V high limit:

Measured: 4.641200E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition $\,$ 2047 to $\,$ 2048 on 3V range

Measured: 4.317099E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition 4095 to 4096 on 3V

Measured: 3.112999E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition $\,$ 8191 to $\,$ 8192 on 3V range

Measured: $3.310000\mbox{E-}05\mbox{V}$ low limit: $\mbox{-}4.269775\mbox{E-}05\mbox{V}$ high limit: $1.373022\mbox{E-}04\mbox{V}$

%PASS - Slot 17 channel 6 raw DAC code binary transition 16383 to 16384 on 3V range

Measured: 3.726100E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition $\,$ 32767 to $\,$ 32768 on 3V $\,$ range

Measured: $4.172600\mbox{E-}05\mbox{V}$ low limit: $-4.269775\mbox{E-}05\mbox{V}$ high limit: $1.373022\mbox{E-}04\mbox{V}$

 $\mbox{\%PASS}$ - Slot 17 channel 6 raw DAC code binary transitions maximum difference on $3\mbox{V}$ range

Measured: 5.013300E-05V high limit: 1.373022E-04V

 $\mbox{\%PASS}$ - Slot 17 channel 6 raw DAC code binary transitions minimum difference on $3\mbox{V}$ range

Measured: 3.112999E-05V low limit: -4.269775E-05V

- %PASS Slot 17 channel 6 raw DAC codes linearity at 6.1V on 6V range Measured: 6.097700V low limit: 6.097510V high limit: 6.097870V
- %PASS Slot 17 channel 6 raw DAC codes linearity at $\,$ 6.09990539406424V on 6V range

Measured: 6.097595V low limit: 6.097416V high limit: 6.097776V

%PASS - Slot 17 channel 6 raw DAC codes linearity at $\,$ 6.09981078812848V on 6V range

Measured: 6.097496V low limit: 6.097321V high limit: 6.097681V

- %PASS Slot 17 channel 6 raw DAC codes linearity at 6.09971618219272V on 6V range
 - Measured: 6.097394V low limit: 6.097227V high limit: 6.097587V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 6.09962157625696V on 6V range
 - Measured: 6.097306V low limit: 6.097132V high limit: 6.097492V
- % PASS Slot 17 channel 6 raw DAC codes linearity at $\,$ 6.09933775844968V on 6V range
 - Measured: 6.097016V low limit: 6.096848V high limit: 6.097208V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 6.09924315251392V on 6V range
 - Measured: 6.096927V low limit: 6.096754V high limit: 6.097114V
- % PASS Slot 17 channel 6 raw DAC codes linearity at $\,$ 6.09858091096361V on 6V range
 - Measured: 6.096260V low limit: 6.096092V high limit: 6.096452V
- $\mbox{\%PASS}$ Slot 17 channel 6 raw DAC codes linearity at $\,$ 6.09848630502785V on 6V range
 - Measured: 6.09617V low limit: 6.095997V high limit: 6.096357V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 6.09706721599146V on 6V range
 - Measured: 6.094743V low limit: 6.094579V high limit: 6.094939V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 6.0969726100557V on 6V range Measured: 6.094659V low limit: 6.094484V high limit: 6.094844V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 6.09403982604715V on 6V range
 - Measured: 6.091720V low limit: 6.091553V high limit: 6.091913V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 6.09394522011139V on 6V range
 - Measured: 6.091633V low limit: 6.091458V high limit: 6.091818V

- %PASS Slot 17 channel 6 raw DAC codes linearity at 6.08798504615854V on 6V range
 - Measured: 6.085669V low limit: 6.085500V high limit: 6.085860V
- % PASS Slot 17 channel 6 raw DAC codes linearity at $\,$ 6.08789044022278V on 6V range
 - Measured: 6.085583V low limit: 6.085406V high limit: 6.085766V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 6.07587548638132V on 6V range
 - Measured: 6.073565V low limit: 6.073396V high limit: 6.073756V
- %PASS Slot 17 channel 6 raw DAC codes linearity at $\,$ 6.07578088044556V on 6V range
 - Measured: 6.073479V low limit: 6.073301V high limit: 6.073661V
- % PASS Slot 17 channel 6 raw DAC codes linearity at $\,$ 6.05165636682689V on 6V range
 - Measured: 6.049357V low limit: 6.049187V high limit: 6.049547V
- % PASS Slot 17 channel 6 raw DAC codes linearity at $\,$ 6.05156176089113V on 6V range
 - Measured: 6.049269V low limit: 6.049092V high limit: 6.049452V
- % PASS Slot 17 channel 6 raw DAC codes linearity at $\,$ 6.00321812771801V on 6V range
 - Measured: 6.000937V low limit: 6.000769V high limit: 6.001129V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 6.00312352178225V on 6V range
 - Measured: 6.000848V low limit: 6.000674V high limit: 6.001034V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 5.90634164950027V on 6V range
 - Measured: 5.904103V low limit: 5.903932V high limit: 5.904292V
- %PASS Slot 17 channel 6 raw DAC codes linearity at 5.90624704356451V on 6V

Measured: 5.904019V low limit: 5.903838V high limit: 5.904198V

 $\mbox{\%PASS}$ - Slot 17 channel 6 raw DAC codes linearity at $\,$ 5.71258869306477V on 6V range

Measured: 5.710429V low limit: 5.710260V high limit: 5.710620V

%PASS - Slot 17 channel 6 raw DAC codes linearity at $\,$ 5.71249408712902V on 6V range

Measured: 5.710363V low limit: 5.710165V high limit: 5.710525V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 5.32508278019379V on 6V range

Measured: 5.323090V low limit: 5.322915V high limit: 5.323275V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 5.32498817425803V on 6V range

Measured: 5.323033V low limit: 5.322820V high limit: 5.323180V

%PASS - Slot 17 channel 6 raw DAC codes linearity at $\,$ 4.55007095445182V on 6V range

Measured: 4.548432V low limit: 4.548224V high limit: 4.548584V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 4.54997634851606V on 6V range

Measured: 4.548355V low limit: 4.548130V high limit: 4.548490V

% PASS - Slot 17 channel 6 raw DAC codes linearity at $\,$ 3.00004730296788V on 6V range

Measured: 2.999084V low limit: 2.998844V high limit: 2.999204V

% PASS - Slot 17 channel 6 raw DAC codes linearity at $\,$ 2.99995269703212V on 6V range

Measured: 2.999002V low limit: 2.998749V high limit: 2.999109V

%PASS - Slot 17 channel 6 raw DAC codes linearity at -.1V on 6V range Measured: -0.09982576V low limit: -9.991714E-02V high limit: -9.955714E-02V

- %PASS Slot 17 channel 6 raw DAC codes maximum linearity error on 6V range Measured: 8.862312E-05V high limit: 0.00018V
- %PASS Slot 17 channel 6 raw DAC code binary transition 0 to 1 on 6V range Measured: 1.053010E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 1 to 2 on 6V range Measured: 9.847100E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition $\,2$ to $\,3$ on 6V range Measured: 1.019729E-04V low limit: -8.539550E-05V high limit: $\,2.746044\text{E-}04\text{V}$
- %PASS Slot 17 channel 6 raw DAC code binary transition 3 to 4 on 6V range Measured: 8.809399E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 7 to 8 on 6V range Measured: 8.875099E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 15 to 16 on 6V range Measured: 9.037099E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 31 to 32 on 6V range Measured: 8.389100E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 63 to 64 on 6V range Measured: 8.647399E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 127 to 128 on 6V range Measured: 8.673700E-05V low limit: -8.539550E-05V high limit:

- %PASS Slot 17 channel 6 raw DAC code binary transition 255 to 256 on 6V range Measured: 8.607999E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 511 to 512 on 6V range Measured: 8.743699E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 6 raw DAC code binary transition 1023 to 1024 on 6V range

Measured: 8.861900E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition $\,$ 2047 to $\,$ 2048 on 6V range

Measured: 8.393500E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition $\,$ 4095 to $\,$ 4096 on 6V range

Measured: $6.628899 \hbox{E-}05 V$ low limit: $\text{-}8.539550 \hbox{E-}05 V$ high limit: $2.746044 \hbox{E-}04 V$

% PASS - Slot 17 channel 6 raw DAC code binary transition $\,$ 8191 to $\,$ 8192 on 6V range

Measured: 5.748899E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

 $\mbox{\%PASS}$ - Slot 17 channel 6 raw DAC code binary transition $\,$ 16383 to $\,$ 16384 on 6V range

Measured: 7.675400E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

 $\mbox{\%PASS}$ - Slot 17 channel 6 raw DAC code binary transition $\,$ 32767 to $\,$ 32768 on 6V range

Measured: 8.231399E-05V low limit: -8.539550E-05V high limit:

- $\mbox{\%PASS}$ Slot 17 channel 6 raw DAC code binary transitions maximum difference on $\mbox{6V range}$
 - Measured: 1.053010E-04V high limit: 2.746044E-04V
- $\mbox{\%PASS}$ Slot 17 channel 6 raw DAC code binary transitions minimum difference on $\mbox{6V}$ range
 - Measured: 5.748899E-05V low limit: -8.539550E-05V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.05V on 3V range Measured: 3.048978V low limit: 3.048895V high limit: 3.049075V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.04995269703212V on 3V range
 Measured: 3.048929V low limit: 3.048847V high limit: 3.049027V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.04990539406424V on 3V range

 Measured: 3.048880V low limit: 3.048800V high limit: 3.048980V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.04985809109636V on 3V range

 Measured: 3.048836V low limit: 3.048753V high limit: 3.048933V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.04981078812848V on 3V range

 Measured: 3.048792V low limit: 3.048705V high limit: 3.048885V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.04962157625696V on 3V range

 Measured: 3.048604V low limit: 3.048516V high limit: 3.048696V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.0492904554818V on 3V range

- Measured: 3.048272V low limit: 3.048185V high limit: 3.048365V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.04924315251392V on 3V range

 Measured: 3.048228V low limit: 3.048138V high limit: 3.048318V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.04853360799573V on 3V range
 - Measured: 3.047513V low limit: 3.047429V high limit: 3.047609V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.04848630502785V on 3V range

 Measured: 3.047475V low limit: 3.047382V high limit: 3.047562V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.04701991302358V on 3V range

 Measured: 3.046003V low limit: 3.045916V high limit: 3.046096V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.0469726100557V on 3V range Measured: 3.045960V low limit: 3.045869V high limit: 3.046049V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.04399252307927V on 3V range

 Measured: 3.042979V low limit: 3.042890V high limit: 3.043070V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.04394522011139V on 3V range

 Measured: 3.042936V low limit: 3.042843V high limit: 3.043023V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.03793774319066V on 3V range

 Measured: 3.036927V low limit: 3.036839V high limit: 3.037019V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.03789044022278V on 3V range

 Measured: 3.036885V low limit: 3.036791V high limit: 3.036971V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.02582818341344V on 3V

Measured: 3.024824V low limit: 3.024736V high limit: 3.024916V

%PASS - Slot 17 channel 7 raw DAC codes linearity at $\,$ 3.02578088044556V on 3V range

Measured: 3.024779V low limit: 3.024688V high limit: 3.024868V

%PASS - Slot 17 channel 7 raw DAC codes linearity at $\,$ 3.00160906385901V on 3V range

Measured: 3.000616V low limit: 3.000529V high limit: 3.000709V

%PASS - Slot 17 channel 7 raw DAC codes linearity at $\,$ 3.00156176089113V on 3V range

Measured: 3.000571V low limit: 3.000482V high limit: 3.000662V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 2.95317082475013V on 3V range

Measured: 2.952201V low limit: 2.952116V high limit: 2.952296V

% PASS - Slot 17 channel 7 raw DAC codes linearity at $\,$ 2.95312352178225V on 3V range

Measured: 2.952158V low limit: 2.952069V high limit: 2.952249V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 2.85629434653239V on 3V range

Measured: 2.855375V low limit: 2.855291V high limit: 2.855471V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 2.85624704356451V on 3V range

Measured: 2.855345V low limit: 2.855244V high limit: 2.855424V

% PASS - Slot 17 channel 7 raw DAC codes linearity at $\,$ 2.66254139009689V on 3V range

Measured: 2.661726V low limit: 2.661640V high limit: 2.661820V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 2.66249408712902V on 3V range

Measured: 2.661698V low limit: 2.661593V high limit: 2.661773V

- %PASS Slot 17 channel 7 raw DAC codes linearity at 2.27503547722591V on 3V range

 Measured: 2.274436V low limit: 2.274338V high limit: 2.274518V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 2.27498817425803V on 3V range

 Measured: 2.274398V low limit: 2.274291V high limit: 2.274471V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 1.50002365148394V on 3V range

 Measured: 1.499846V low limit: 1.499735V high limit: 1.499915V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 1.49997634851606V on 3V range

 Measured: 1.499803V low limit: 1.499688V high limit: 1.499868V
- %PASS Slot 17 channel 7 raw DAC codes linearity at -.05V on 3V range Measured: -0.04941268V low limit: -4.947161E-02V high limit: -4.929161E-02V
- %PASS Slot 17 channel 7 raw DAC codes maximum linearity error on 3V range Measured: 3.107252E-05V high limit: 0.00009V
- %PASS Slot 17 channel 7 raw DAC code binary transition 0 to 1 on 3V range Measured: 4.903899E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 1 to 2 on 3V range Measured: 4.916899E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 2 to 3 on 3V range Measured: 4.431000E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 3 to 4 on 3V range Measured: 4.373999E-05V low limit: -4.269775E-05V high limit:

1.373022E-04V

- %PASS Slot 17 channel 7 raw DAC code binary transition 7 to 8 on 3V range Measured: 4.680499E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 15 to 16 on 3V range Measured: 4.391600E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 31 to 32 on 3V range Measured: 3.787400E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 63 to 64 on 3V range Measured: 4.325899E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 127 to 128 on 3V range Measured: 4.308400E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 255 to 256 on 3V range Measured: 4.242599E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 511 to 512 on 3V range Measured: 4.487899E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 1023 to 1024 on 3V range

 Measured: 4.492299E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 2047 to 2048 on 3V range

 Measured: 4.303999E-05V low limit: -4.269775E-05V high limit:

%PASS - Slot 17 channel 7 raw DAC code binary transition $\,$ 4095 to $\,$ 4096 on 3V range

Measured: 2.933600E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

 $\mbox{\%PASS}$ - Slot 17 channel 7 raw DAC code binary transition $\,$ 8191 to $\,$ 8192 on 3V range

Measured: 2.732199E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transition 16383 to 16384 on 3V range

Measured: 3.800499E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transition 32767 to 32768 on 3V range

Measured: 4.325899E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

 $\mbox{\it \%PASS}$ - Slot 17 channel 7 raw DAC code binary transitions maximum difference on 3V range

Measured: 4.916899E-05V high limit: 1.373022E-04V

 $\mbox{\%PASS}$ - Slot 17 channel 7 raw DAC code binary transitions minimum difference on 3V range

Measured: 2.732199E-05V low limit: -4.269775E-05V

- %PASS Slot 17 channel 7 raw DAC codes linearity at 6.1V on 6V range Measured: 6.097079V low limit: 6.096885V high limit: 6.097245V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 6.09990539406424V on 6V range

Measured: 6.096974V low limit: 6.096790V high limit: 6.097150V

 $\mbox{\%PASS}$ - Slot 17 channel 7 raw DAC codes linearity at $\,$ 6.09981078812848V on 6V

Measured: 6.096878V low limit: 6.096696V high limit: 6.097056V

%PASS - Slot 17 channel 7 raw DAC codes linearity at $\,$ 6.09971618219272V on 6V range

Measured: 6.096779V low limit: 6.096601V high limit: 6.096961V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 6.09962157625696V on 6V range

Measured: 6.096685V low limit: 6.096506V high limit: 6.096866V

%PASS - Slot 17 channel 7 raw DAC codes linearity at $\,$ 6.09933775844968V on 6V range

Measured: 6.096393V low limit: 6.096223V high limit: 6.096583V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 6.09924315251392V on 6V range

Measured: 6.096307V low limit: 6.096128V high limit: 6.096488V

% PASS - Slot 17 channel 7 raw DAC codes linearity at $\,$ 6.09858091096361V on 6V range

Measured: 6.095643V low limit: 6.095467V high limit: 6.095827V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 6.09848630502785V on 6V range

Measured: 6.095546V low limit: 6.095372V high limit: 6.095732V

% PASS - Slot 17 channel 7 raw DAC codes linearity at $\,$ 6.09706721599146V on 6V range

Measured: 6.094120V low limit: 6.093954V high limit: 6.094314V

%PASS - Slot 17 channel 7 raw DAC codes linearity at $\,$ 6.0969726100557V on 6V range Measured: 6.094033V low limit: 6.093859V high limit: 6.094219V

%PASS - Slot 17 channel 7 raw DAC codes linearity at $\,$ 6.09403982604715V on 6V range

Measured: 6.091095V low limit: 6.090929V high limit: 6.091289V

- %PASS Slot 17 channel 7 raw DAC codes linearity at $\,$ 6.09394522011139V on 6V range
 - Measured: 6.091010V low limit: 6.090834V high limit: 6.091194V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 6.08798504615854V on 6V range
 - Measured: 6.085046 V low limit: 6.084878 V high limit: 6.085238 V
- % PASS Slot 17 channel 7 raw DAC codes linearity at $\,$ 6.08789044022278V on 6V range
 - Measured: 6.084957V low limit: 6.084784V high limit: 6.085144V
- % PASS Slot 17 channel 7 raw DAC codes linearity at $\,$ 6.07587548638132V on 6V range
 - Measured: 6.072945V low limit: 6.072777V high limit: 6.073137V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 6.07578088044556V on 6V range

 Measured: 6.072861V low limit: 6.072683V high limit: 6.073043V
- $\mbox{\%PASS}$ Slot 17 channel 7 raw DAC codes linearity at $\mbox{\ \ }6.05165636682689\mbox{V}$ on 6V range
 - Measured: 6.048747V low limit: 6.048576V high limit: 6.048936V
- %PASS Slot 17 channel 7 raw DAC codes linearity at $\,$ 6.05156176089113V on 6V range $\,$ Measured: 6.048656V low limit: 6.048481V high limit: 6.048841V $\,$
- $\mbox{\%PASS}$ Slot 17 channel 7 raw DAC codes linearity at $\,$ 6.00321812771801V on 6V range
 - Measured: 6.000344V low limit: 6.000172V high limit: 6.000532V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 6.00312352178225V on 6V range

 Measured: 6.000250V low limit: 6.000078V high limit: 6.000438V
- %PASS Slot 17 channel 7 raw DAC codes linearity at $\,$ 5.90634164950027V on 6V range

- Measured: 5.903527V low limit: 5.903365V high limit: 5.903725V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 5.90624704356451V on 6V range

 Measured: 5.903442V low limit: 5.903271V high limit: 5.903631V
- $\mbox{\%PASS}$ Slot 17 channel 7 raw DAC codes linearity at $\,$ 5.71258869306477V on 6V range

Measured: 5.709910V low limit: 5.709752V high limit: 5.710112V

- %PASS Slot 17 channel 7 raw DAC codes linearity at 5.71249408712902V on 6V range
 - Measured: 5.709856V low limit: 5.709657V high limit: 5.710017V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 5.32508278019379V on 6V range

Measured: 5.322696V low limit: 5.322524V high limit: 5.322884V

- %PASS Slot 17 channel 7 raw DAC codes linearity at $\,$ 5.32498817425803V on 6V range
 - Measured: 5.322641V low limit: 5.322430V high limit: 5.322790V
- %PASS Slot 17 channel 7 raw DAC codes linearity at $\,$ 4.55007095445182V on 6V range

Measured: 4.548272V low limit: 4.548069V high limit: 4.548429V

- %PASS Slot 17 channel 7 raw DAC codes linearity at 4.54997634851606V on 6V range
 - Measured: 4.548200V low limit: 4.547974V high limit: 4.548334V
- %PASS Slot 17 channel 7 raw DAC codes linearity at 3.00004730296788V on 6V range

Measured: 2.999392V low limit: 2.999159V high limit: 2.999519V

% PASS - Slot 17 channel 7 raw DAC codes linearity at $\,$ 2.99995269703212V on 6V range

Measured: 2.999310V low limit: 2.999064V high limit: 2.999424V

- %PASS Slot 17 channel 7 raw DAC codes linearity at -.1V on 6V range Measured: -0.09855879V low limit: -9.866092E-02V high limit: -9.830092E-02V
- %PASS Slot 17 channel 7 raw DAC codes maximum linearity error on 6V range Measured: 7.786868E-05V high limit: 0.00018V
- %PASS Slot 17 channel 7 raw DAC code binary transition 0 to 1 on 6V range Measured: 1.053889E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 1 to 2 on 6V range Measured: 9.571200E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 2 to 3 on 6V range Measured: 9.912800E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 3 to 4 on 6V range Measured: 9.391700E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 7 to 8 on 6V range Measured: 8.607999E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 15 to 16 on 6V range Measured: 9.698199E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 31 to 32 on 6V range Measured: 8.682400E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 63 to 64 on 6V range Measured: 8.516000E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

- %PASS Slot 17 channel 7 raw DAC code binary transition 127 to 128 on 6V range Measured: 8.936399E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 255 to 256 on 6V range Measured: 8.397799E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 511 to 512 on 6V range Measured: 9.058900E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V
- %PASS Slot 17 channel 7 raw DAC code binary transition 1023 to 1024 on 6V range

 Measured: 9.453000E-05V low limit: -8.539550E-05V high limit:

2.746044E-04V

 $\mbox{\%PASS}$ - Slot 17 channel 7 raw DAC code binary transition $\,$ 2047 to $\,$ 2048 on 6V $\,$ range

Measured: 8.502900E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

 $\mbox{\%PASS}$ - Slot 17 channel 7 raw DAC code binary transition $\,$ 4095 to $\,$ 4096 on 6V range

Measured: $5.429200\mbox{E-}05\mbox{V}$ low limit: -8.539550 E-05V high limit: $2.746044\mbox{E-}04\mbox{V}$

%PASS - Slot 17 channel 7 raw DAC code binary transition $\,$ 8191 to $\,$ 8192 on 6V range

Measured: 5.477400E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transition $\,$ 16383 to $\,$ 16384 on 6V $\,$ range

Measured: 7.193800E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transition 32767 to 32768 on 6V range

Measured: $8.284000\hbox{E-}05V$ low limit: $\text{-}8.539550\hbox{E-}05V$ high limit: $2.746044\hbox{E-}04V$

 $\mbox{\%PASS}$ - Slot 17 channel 7 raw DAC code binary transitions maximum difference on $6\mbox{V}$ range

Measured: 1.053889E-04V high limit: 2.746044E-04V

 $\mbox{\%PASS}$ - Slot 17 channel 7 raw DAC code binary transitions minimum difference on $6\mbox{V}$ range

Measured: 5.429200E-05V low limit: -8.539550E-05V

%JOB_END - ****PASSED**** CTO_DIB External Verification of slot 17 (500933B) at 2:52:43 PM

$Slot18_CALCUB_ExternalCal$

- %JOB_START Beginning CUB External Calibration test on slot 18 at 2:30:28

 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB

 # 30562A2 Rev 1137A
 - %PASS CALCUB Vforce external calibration test, -1 volts.

 Measured: -0.9996 low limit: -1.200 high limit: -0.7999
 - %PASS CALCUB Vforce external calibration test, 0 volts.

 Measured: 0.0003091 low limit: -0.2000 high limit: 0.2000
 - %PASS CALCUB Vforce external calibration test, 0 volts.

 Measured: 0.0003072 low limit: -0.2000 high limit: 0.2000
 - %PASS CALCUB Vforce external calibration test, 2 volts.

 Measured: 2.000 low limit: 1.799 high limit: 2.200
 - %PASS CALCUB Vforce external calibration test, 3 volts.

 Measured: 3.000 low limit: 2.799 high limit: 3.200
 - %PASS CALCUB Vforce external calibration test, 5 volts.

 Measured: 5.000 low limit: 4.799 high limit: 5.200
 - %PASS CALCUB Vforce external calibration test, 7 volts.

 Measured: 7.000 low limit: 6.799 high limit: 7.200
 - %PASS CALCUB Vforce external calibration test, 9 volts.

 Measured: 9.000 low limit: 8.799 high limit: 9.200
 - %PASS CALCUB Vforce external calibration test, 10 volts.

 Measured: 10.000 low limit: 9.799 high limit: 10.200
 - %PASS CALCUB Vforce external calibration test, 24 volts.

 Measured: 24.000 low limit: 23.799 high limit: 24.200
 - %PASS CALCUB Vforce external calibration test, -2 volts.

- Measured: -1.999 low limit: -2.200 high limit: -1.799
- %PASS CALCUB Vforce external calibration test, -5 volts.

 Measured: -4.999 low limit: -5.200 high limit: -4.799
- %PASS CALCUB Vforce external calibration test, -7 volts.

 Measured: -6.999 low limit: -7.200 high limit: -6.799
- %PASS CALCUB Vforce external calibration test, -10 volts.

 Measured: -9.999 low limit: -10.200 high limit: -9.799
- %PASS CALCUB Vforce external calibration test, -24 volts.

 Measured: -23.999 low limit: -24.200 high limit: -23.799
- %PASS CALCUB Vforce external calibration test, -9 volts.

 Measured: -8.999 low limit: -9.200 high limit: -8.799
- %PASS CALCUB Vforce external calibration test, 1 volts.

 Measured: 1.000 low limit: 0.7999 high limit: 1.200
- %PASS CALCUB Vforce external calibration test, 19 volts.

 Measured: 19.000 low limit: 18.799 high limit: 19.200
- %PASS CALCUB Vforce external calibration test, 21 volts.

 Measured: 21.000 low limit: 20.799 high limit: 21.200
- %PASS CALCUB Vforce external calibration test, 4 volts.

 Measured: 4.000 low limit: 3.799 high limit: 4.200
- %PASS CALCUB Vforce external calibration test, 6 volts. Measured: 6.000 low limit: 5.799 high limit: 6.200
- %PASS CALCUB Vforce external calibration test, 20 volts.

 Measured: 20.000 low limit: 19.799 high limit: 20.200
- %PASS CALCUB Vforce external calibration test, -19 volts.

 Measured: -18.999 low limit: -19.200 high limit: -18.799

- %PASS CALCUB Vforce external calibration test, -20 volts.

 Measured: -19.999 low limit: -20.200 high limit: -19.799
- %PASS CALCUB Vforce external calibration test, -21 volts.

 Measured: -20.999 low limit: -21.200 high limit: -20.799
- %PASS CALCUB Vforce external calibration test, 23 volts.

 Measured: 23.000 low limit: 22.799 high limit: 23.200
- %PASS CALCUB Vforce external calibration test, 22 volts.

 Measured: 22.000 low limit: 21.799 high limit: 22.200
- %PASS CALCUB Vforce external calibration test, 18 volts.

 Measured: 18.000 low limit: 17.799 high limit: 18.200
- %PASS CALCUB Vforce external calibration test, 17 volts.

 Measured: 17.000 low limit: 16.799 high limit: 17.200
- %PASS CALCUB Vforce external calibration test, 16 volts.

 Measured: 16.000 low limit: 15.799 high limit: 16.200
- %PASS CALCUB Vforce external calibration test, 15 volts.

 Measured: 15.000 low limit: 14.799 high limit: 15.200
- %PASS CALCUB Vforce external calibration test, 14 volts.

 Measured: 14.000 low limit: 13.799 high limit: 14.200
- %PASS CALCUB Vforce external calibration test, 13 volts.

 Measured: 13.000 low limit: 12.799 high limit: 13.200
- %PASS CALCUB Vforce external calibration test, 12 volts.

 Measured: 12.000 low limit: 11.799 high limit: 12.200
- %PASS CALCUB Vforce external calibration test, 11 volts.

 Measured: 11.000 low limit: 10.799 high limit: 11.200
- %PASS CALCUB Vforce external calibration test, 8 volts.

 Measured: 8.000 low limit: 7.799 high limit: 8.200

- %PASS CALCUB Vforce external calibration test, -3 volts.

 Measured: -2.999 low limit: -3.200 high limit: -2.799
- %PASS CALCUB Vforce external calibration test, -4 volts.

 Measured: -3.999 low limit: -4.200 high limit: -3.799
- %PASS CALCUB Vforce external calibration test, -6 volts.

 Measured: -5.999 low limit: -6.200 high limit: -5.799
- %PASS CALCUB Vforce external calibration test, -8 volts.

 Measured: -7.999 low limit: -8.200 high limit: -7.799
- %PASS CALCUB Vforce external calibration test, -11 volts.

 Measured: -10.999 low limit: -11.200 high limit: -10.799
- %PASS CALCUB Vforce external calibration test, -12 volts.

 Measured: -11.999 low limit: -12.200 high limit: -11.799
- %PASS CALCUB Vforce external calibration test, -13 volts.

 Measured: -12.999 low limit: -13.200 high limit: -12.799
- %PASS CALCUB Vforce external calibration test, -14 volts.

 Measured: -13.999 low limit: -14.200 high limit: -13.799
- %PASS CALCUB Vforce external calibration test, -15 volts.

 Measured: -14.999 low limit: -15.200 high limit: -14.799
- %PASS CALCUB Vforce external calibration test, -16 volts.

 Measured: -15.999 low limit: -16.200 high limit: -15.799
- %PASS CALCUB Vforce external calibration test, -17 volts.

 Measured: -16.999 low limit: -17.200 high limit: -16.799
- %PASS CALCUB Vforce external calibration test, -18 volts.

 Measured: -17.999 low limit: -18.200 high limit: -17.799
- %PASS CALCUB Vforce external calibration test, -22 volts.

- Measured: -21.999 low limit: -22.200 high limit: -21.799
- %PASS CALCUB Vforce external calibration test, -23 volts.

 Measured: -22.999 low limit: -23.200 high limit: -22.799
- %PASS CALCUB IForce external calibration test, 200 na.

 Measured: -0.000002000 low limit: -2.001E-06 high limit: -1.998E-06
- %PASS CALCUB IForce external calibration test, 2 ua.

 Measured: 0.00002000 low limit: 1.998E-05 high limit: 2.001E-05
- %PASS CALCUB IForce external calibration test, 2 ua.

 Measured: -0.00001999 low limit: -2.001E-05 high limit: -1.998E-05
- %PASS CALCUB IForce external calibration test, 20 ua.

 Measured: 0.0001999 low limit: 1.998E-04 high limit: 2.001E-04
- %PASS CALCUB IForce external calibration test, 20 ua.

 Measured: -0.0001999 low limit: -2.001E-04 high limit: -1.998E-04
- %PASS CALCUB IForce external calibration test, 200 ua.

 Measured: 0.001999 low limit: 1.998E-03 high limit: 2.001E-03
- %PASS CALCUB IForce external calibration test, 200 ua.

 Measured: -0.001999 low limit: -2.001E-03 high limit: -1.998E-03
- %PASS CALCUB IForce external calibration test, 2 ma.

 Measured: 0.02000 low limit: 1.998E-02 high limit: 2.001E-02
- %PASS CALCUB IForce external calibration test, 2 ma.

 Measured: -0.02000 low limit: -2.001E-02 high limit: -1.998E-02
- %PASS CALCUB IForce external calibration test, 20 ma.

 Measured: 0.1000 low limit: 9.990E-02 high limit: 0.1000

- %PASS CALCUB IForce external calibration test, 20 ma.

 Measured: -0.09999 low limit: -0.1000 high limit: -9.990E-02
- %PASS CALCUB IForce external calibration test, 200 ma.

 Measured: 0.2000 low limit: 0.1994 high limit: 0.2005
- %PASS CALCUB IForce external calibration test, 200 ma.

 Measured: -0.2000 low limit: -0.2005 high limit: -0.1994
- %PASS Flash readback error of force DAC record 0 Measured: 8.697E-04 expected: 8.697E-04
- %PASS Flash readback error of external measurement record 0 Measured: 0.0003091 expected: 0.0003091
- %PASS Flash readback error of internal measurement record 0 Measured: 1.520E-03 expected: 1.520E-03
- %PASS Flash readback error of voltage flag record 0 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 1 Measured: 2.000 expected: 2.000
- %PASS Flash readback error of external measurement record 1 Measured: 2.000 expected: 2.000
- %PASS Flash readback error of internal measurement record 1 Measured: 1.999 expected: 1.999
- %PASS Flash readback error of voltage flag record 1 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 2 Measured: 5.000 expected: 5.000
- %PASS Flash readback error of external measurement record 2 Measured: 5.000 expected: 5.000

- %PASS Flash readback error of internal measurement record 2 Measured: 4.997 expected: 4.997
- %PASS Flash readback error of voltage flag record 2 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 3 Measured: 7.000 expected: 7.000
- %PASS Flash readback error of external measurement record 3 Measured: 7.000 expected: 7.000
- %PASS Flash readback error of internal measurement record 3 Measured: 6.995 expected: 6.995
- %PASS Flash readback error of voltage flag record 3 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 4 Measured: 9.998 expected: 9.998
- %PASS Flash readback error of external measurement record 4 Measured: 10.000 expected: 10.000
- %PASS Flash readback error of internal measurement record 4 Measured: 9.994 expected: 9.994
- %PASS Flash readback error of voltage flag record 4 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 5 Measured: 23.995 expected: 23.995
- %PASS Flash readback error of external measurement record 5 Measured: 24.000 expected: 24.000
- %PASS Flash readback error of internal measurement record 5

- Measured: 23.981 expected: 23.981
- %PASS Flash readback error of voltage flag record 5 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 6 Measured: -1.999 expected: -1.999
- %PASS Flash readback error of external measurement record 6 Measured: -1.999 expected: -1.999
- %PASS Flash readback error of internal measurement record 6 Measured: -1.995 expected: -1.995
- %PASS Flash readback error of voltage flag record 6 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 7 Measured: -4.998 expected: -4.998
- %PASS Flash readback error of external measurement record 7 Measured: -4.999 expected: -4.999
- %PASS Flash readback error of internal measurement record 7 Measured: -4.993 expected: -4.993
- %PASS Flash readback error of voltage flag record 7 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 8 Measured: -6.998 expected: -6.998
- %PASS Flash readback error of external measurement record 8 Measured: -6.999 expected: -6.999
- %PASS Flash readback error of internal measurement record 8 Measured: -6.991 expected: -6.991

- %PASS Flash readback error of voltage flag record 8 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 9 Measured: -9.997 expected: -9.997
- %PASS Flash readback error of external measurement record 9 Measured: -9.999 expected: -9.999
- %PASS Flash readback error of internal measurement record 9 Measured: -9.983 expected: -9.983
- %PASS Flash readback error of voltage flag record 9 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 10 Measured: -23.995 expected: -23.995
- %PASS Flash readback error of external measurement record 10 Measured: -23.999 expected: -23.999
- %PASS Flash readback error of internal measurement record 10 Measured: -23.970 expected: -23.970
- %PASS Flash readback error of voltage flag record 10 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 11 Measured: 9999 expected: 9999
- %PASS Flash readback error of external measurement record 11 Measured: 0 expected: 0
- %PASS Flash readback error of internal measurement record 11 Measured: 6.823E-04 expected: 6.823E-04
- %PASS Flash readback error of voltage flag record 11 Measured: 0 expected: 0 $\,$

- %PASS Flash readback error of force DAC record 12 Measured: 0.8133 expected: 0.8133
- %PASS Flash readback error of external measurement record 12 Measured: 0.2 expected: 0.2
- %PASS Flash readback error of internal measurement record 12 Measured: 0.2003 expected: 0.2003
- %PASS Flash readback error of voltage flag record 12 Measured: 0 expected: 0
- %PASS Flash readback error of force DAC record 13 Measured: 9999 expected: 9999
- %PASS Flash readback error of external measurement record 13 Measured: 0 expected: 0 $\,$
- %PASS Flash readback error of internal measurement record 13 Measured: 7.436E-05 expected: 7.436E-05
- %PASS Flash readback error of voltage flag record 13 Measured: 0 expected: 0
- %PASS Flash readback error of force DAC record 14 Measured: 2.190 expected: 2.190
- %PASS Flash readback error of external measurement record 14 Measured: 0.02 expected: 0.02
- %PASS Flash readback error of internal measurement record 14 Measured: 2.010E-02 expected: 2.010E-02
- %PASS Flash readback error of voltage flag record 14 Measured: 0 expected: 0
- % PASS Flash readback error of force DAC record 15

- Measured: 9999 expected: 9999
- %PASS Flash readback error of external measurement record 15 Measured: 0 expected: 0
- %PASS Flash readback error of internal measurement record 15 Measured: 6.941E-06 expected: 6.941E-06
- %PASS Flash readback error of voltage flag record 15 Measured: 0 expected: 0
- %PASS Flash readback error of force DAC record 16 Measured: 4.001 expected: 4.001
- %PASS Flash readback error of external measurement record 16 Measured: 0.002 expected: 0.002
- %PASS Flash readback error of internal measurement record 16 Measured: 2.005E-03 expected: 2.005E-03
- %PASS Flash readback error of voltage flag record 16 Measured: 0 expected: 0
- %PASS Flash readback error of force DAC record 17 Measured: 9999 expected: 9999
- %PASS Flash readback error of external measurement record 17 Measured: 0 expected: 0
- %PASS Flash readback error of internal measurement record 17 Measured: 7.210E-07 expected: 7.210E-07
- %PASS Flash readback error of voltage flag record 17 Measured: 0 expected: 0
- %PASS Flash readback error of force DAC record 18 Measured: 3.959 expected: 3.959

- %PASS Flash readback error of external measurement record 18 Measured: 0.0002 expected: 0.0002
- %PASS Flash readback error of internal measurement record 18 Measured: 2.005E-04 expected: 2.005E-04
- %PASS Flash readback error of voltage flag record 18 Measured: 0 expected: 0
- %PASS Flash readback error of force DAC record 19 Measured: 9999 expected: 9999
- %PASS Flash readback error of external measurement record 19 Measured: 0 expected: 0 $\,$
- %PASS Flash readback error of internal measurement record 19 Measured: 7.033E-08 expected: 7.033E-08
- %PASS Flash readback error of voltage flag record 19 Measured: 0 expected: 0
- %PASS Flash readback error of force DAC record 20 Measured: 3.955 expected: 3.955
- %PASS Flash readback error of external measurement record 20 Measured: 0.00002 expected: 0.00002
- %PASS Flash readback error of internal measurement record 20 Measured: 2.005E-05 expected: 2.005E-05
- %PASS Flash readback error of voltage flag record 20 Measured: 0 expected: 0
- %PASS Flash readback error of force DAC record 21 Measured: 9999 expected: 9999
- %PASS Flash readback error of external measurement record 21 Measured: 0 expected: 0 $\,$

- %PASS Flash readback error of internal measurement record 21 Measured: 7.177E-09 expected: 7.177E-09
- %PASS Flash readback error of voltage flag record 21 Measured: 0 expected: 0
- %PASS Flash readback error of force DAC record 22 Measured: 3.953 expected: 3.953
- %PASS Flash readback error of external measurement record 22 Measured: 0.000002 expected: 0.000002
- %PASS Flash readback error of internal measurement record 22 Measured: 2.005E-06 expected: 2.005E-06
- %PASS Flash readback error of voltage flag record 22 Measured: 0 expected: 0
- %PASS Flash readback error of force DAC record 23 Measured: 9999 expected: 9999
- %PASS Flash readback error of external measurement record 23 Measured: 0 expected: 0
- %PASS Flash readback error of internal measurement record 23 Measured: 6.829E-10 expected: 6.829E-10
- %PASS Flash readback error of voltage flag record 23 Measured: 0 expected: 0
- %PASS Flash readback error of force DAC record 24 Measured: 3.946 expected: 3.946
- %PASS Flash readback error of external measurement record 24 Measured: 0.0000002 expected: 0.0000002
- %PASS Flash readback error of internal measurement record 24

- Measured: 2.005E-07 expected: 2.005E-07
- %PASS Flash readback error of voltage flag record 24 Measured: 0 expected: 0
- %PASS Flash readback error of force DAC record 25 Measured: 3.000 expected: 3.000
- %PASS Flash readback error of external measurement record 25 Measured: 3.000 expected: 3.000
- %PASS Flash readback error of internal measurement record 25 Measured: 2.998 expected: 2.998
- %PASS Flash readback error of voltage flag record 25 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 26 Measured: -0.9991 expected: -0.9991
- %PASS Flash readback error of external measurement record 26 Measured: -0.9996 expected: -0.9996
- %PASS Flash readback error of internal measurement record 26 Measured: -0.9968 expected: -0.9968
- %PASS Flash readback error of voltage flag record 26 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 27 Measured: 9.000 expected: 9.000
- %PASS Flash readback error of external measurement record 27 Measured: 9.000 expected: 9.000
- %PASS Flash readback error of internal measurement record 27 Measured: 8.993 expected: 8.993

- %PASS Flash readback error of voltage flag record 27 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 28 Measured: 9999 expected: 9999
- %PASS Flash readback error of external measurement record 28 Measured: 0.0003072 expected: 0.0003072
- %PASS Flash readback error of internal measurement record 28 Measured: 4.184E-03 expected: 4.184E-03
- %PASS Flash readback error of voltage flag record 28 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 29 Measured: -8.998 expected: -8.998
- %PASS Flash readback error of external measurement record 29 Measured: -8.999 expected: -8.999
- %PASS Flash readback error of internal measurement record 29 Measured: -8.989 expected: -8.989
- %PASS Flash readback error of voltage flag record 29 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 30 Measured: 1.000 expected: 1.000
- %PASS Flash readback error of external measurement record 30 Measured: 1.000 expected: 1.000
- %PASS Flash readback error of internal measurement record 30 Measured: 1.000 expected: 1.000
- %PASS Flash readback error of voltage flag record 30 Measured: 1 expected: 1

- %PASS Flash readback error of force DAC record 31 Measured: 18.996 expected: 18.996
- %PASS Flash readback error of external measurement record 31 Measured: 19.000 expected: 19.000
- %PASS Flash readback error of internal measurement record 31 Measured: 18.986 expected: 18.986
- %PASS Flash readback error of voltage flag record 31 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 32 Measured: 20.995 expected: 20.995
- %PASS Flash readback error of external measurement record 32 Measured: 21.000 expected: 21.000
- %PASS Flash readback error of internal measurement record 32 Measured: 20.984 expected: 20.984
- %PASS Flash readback error of voltage flag record 32 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 33 Measured: 4.000 expected: 4.000
- %PASS Flash readback error of external measurement record 33 Measured: 4.000 expected: 4.000
- %PASS Flash readback error of internal measurement record 33 Measured: 3.997 expected: 3.997
- %PASS Flash readback error of voltage flag record 33 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 34

- Measured: 6.000 expected: 6.000
- %PASS Flash readback error of external measurement record 34 Measured: 6.000 expected: 6.000
- %PASS Flash readback error of internal measurement record 34 Measured: 5.996 expected: 5.996
- %PASS Flash readback error of voltage flag record 34 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 35 Measured: 19.995 expected: 19.995
- %PASS Flash readback error of external measurement record 35 Measured: 20.000 expected: 20.000
- %PASS Flash readback error of internal measurement record 35 Measured: 19.984 expected: 19.984
- %PASS Flash readback error of voltage flag record 35 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 36 Measured: -18.996 expected: -18.996
- %PASS Flash readback error of external measurement record 36 Measured: -18.999 expected: -18.999
- %PASS Flash readback error of internal measurement record 36 Measured: -18.974 expected: -18.974
- %PASS Flash readback error of voltage flag record 36 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 37 Measured: -19.996 expected: -19.996

- %PASS Flash readback error of external measurement record 37 Measured: -19.999 expected: -19.999
- %PASS Flash readback error of internal measurement record 37 Measured: -19.973 expected: -19.973
- %PASS Flash readback error of voltage flag record 37 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 38 Measured: -20.996 expected: -20.996
- %PASS Flash readback error of external measurement record 38 Measured: -20.999 expected: -20.999
- %PASS Flash readback error of internal measurement record 38 Measured: -20.973 expected: -20.973
- %PASS Flash readback error of voltage flag record 38 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 39 Measured: 22.995 expected: 22.995
- %PASS Flash readback error of external measurement record 39 Measured: 23.000 expected: 23.000
- %PASS Flash readback error of internal measurement record 39 Measured: 22.982 expected: 22.982
- %PASS Flash readback error of voltage flag record 39 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 40 Measured: 21.995 expected: 21.995
- %PASS Flash readback error of external measurement record 40 Measured: 22.000 expected: 22.000

- %PASS Flash readback error of internal measurement record 40 Measured: 21.983 expected: 21.983
- %PASS Flash readback error of voltage flag record 40 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 41 Measured: 17.996 expected: 17.996
- %PASS Flash readback error of external measurement record 41 Measured: 18.000 expected: 18.000
- %PASS Flash readback error of internal measurement record 41 Measured: 17.986 expected: 17.986
- %PASS Flash readback error of voltage flag record 41 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 42 Measured: 16.996 expected: 16.996
- %PASS Flash readback error of external measurement record 42 Measured: 17.000 expected: 17.000
- %PASS Flash readback error of internal measurement record 42 Measured: 16.987 expected: 16.987
- %PASS Flash readback error of voltage flag record 42 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 43 Measured: 15.997 expected: 15.997
- $\mbox{\%PASS}$ Flash readback error of external measurement record 43 Measured: 16.000 expected: 16.000
- %PASS Flash readback error of internal measurement record 43

- Measured: 15.988 expected: 15.988
- %PASS Flash readback error of voltage flag record 43 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 44 Measured: 14.997 expected: 14.997
- %PASS Flash readback error of external measurement record 44 Measured: 15.000 expected: 15.000
- %PASS Flash readback error of internal measurement record 44 Measured: 14.989 expected: 14.989
- %PASS Flash readback error of voltage flag record 44 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 45 Measured: 13.997 expected: 13.997
- %PASS Flash readback error of external measurement record 45 Measured: 14.000 expected: 14.000
- %PASS Flash readback error of internal measurement record 45 Measured: 13.990 expected: 13.990
- %PASS Flash readback error of voltage flag record 45 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 46 Measured: 12.997 expected: 12.997
- %PASS Flash readback error of external measurement record 46 Measured: 13.000 expected: 13.000
- %PASS Flash readback error of internal measurement record 46 Measured: 12.991 expected: 12.991

- %PASS Flash readback error of voltage flag record 46 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 47 Measured: 11.997 expected: 11.997
- %PASS Flash readback error of external measurement record 47 Measured: 12.000 expected: 12.000
- %PASS Flash readback error of internal measurement record 47 Measured: 11.992 expected: 11.992
- %PASS Flash readback error of voltage flag record 47 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 48 Measured: 10.997 expected: 10.997
- %PASS Flash readback error of external measurement record 48 Measured: 11.000 expected: 11.000
- %PASS Flash readback error of internal measurement record 48 Measured: 10.993 expected: 10.993
- %PASS Flash readback error of voltage flag record 48 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 49 Measured: 8.000 expected: 8.000
- %PASS Flash readback error of external measurement record 49 Measured: 8.000 expected: 8.000
- %PASS Flash readback error of internal measurement record 49 Measured: 7.994 expected: 7.994
- %PASS Flash readback error of voltage flag record 49 Measured: 1 expected: 1

- %PASS Flash readback error of force DAC record 50 Measured: -2.998 expected: -2.998
- %PASS Flash readback error of external measurement record 50 Measured: -2.999 expected: -2.999
- %PASS Flash readback error of internal measurement record 50 Measured: -2.995 expected: -2.995
- %PASS Flash readback error of voltage flag record 50 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 51 Measured: -3.998 expected: -3.998
- %PASS Flash readback error of external measurement record 51 Measured: -3.999 expected: -3.999
- %PASS Flash readback error of internal measurement record 51 Measured: -3.993 expected: -3.993
- %PASS Flash readback error of voltage flag record 51 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 52 Measured: -5.998 expected: -5.998
- %PASS Flash readback error of external measurement record 52 Measured: -5.999 expected: -5.999
- %PASS Flash readback error of internal measurement record 52 Measured: -5.992 expected: -5.992
- %PASS Flash readback error of voltage flag record 52 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 53

- Measured: -7.998 expected: -7.998
- %PASS Flash readback error of external measurement record 53 Measured: -7.999 expected: -7.999
- %PASS Flash readback error of internal measurement record 53 Measured: -7.990 expected: -7.990
- %PASS Flash readback error of voltage flag record 53 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 54 Measured: -10.997 expected: -10.997
- %PASS Flash readback error of external measurement record 54 Measured: -10.999 expected: -10.999
- %PASS Flash readback error of internal measurement record 54 Measured: -10.982 expected: -10.982
- %PASS Flash readback error of voltage flag record 54 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 55 Measured: -11.997 expected: -11.997
- %PASS Flash readback error of external measurement record 55 Measured: -11.999 expected: -11.999
- %PASS Flash readback error of internal measurement record 55 Measured: -11.981 expected: -11.981
- %PASS Flash readback error of voltage flag record 55 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 56 Measured: -12.997 expected: -12.997

- %PASS Flash readback error of external measurement record 56 Measured: -12.999 expected: -12.999
- %PASS Flash readback error of internal measurement record 56 Measured: -12.980 expected: -12.980
- %PASS Flash readback error of voltage flag record 56 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 57 Measured: -13.997 expected: -13.997
- %PASS Flash readback error of external measurement record 57 Measured: -13.999 expected: -13.999
- %PASS Flash readback error of internal measurement record 57 Measured: -13.979 expected: -13.979
- %PASS Flash readback error of voltage flag record 57 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 58 Measured: -14.997 expected: -14.997
- %PASS Flash readback error of external measurement record 58 Measured: -14.999 expected: -14.999
- %PASS Flash readback error of internal measurement record 58 Measured: -14.978 expected: -14.978
- %PASS Flash readback error of voltage flag record 58 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 59 Measured: -15.997 expected: -15.997
- %PASS Flash readback error of external measurement record 59 Measured: -15.999 expected: -15.999

- %PASS Flash readback error of internal measurement record 59 Measured: -15.976 expected: -15.976
- %PASS Flash readback error of voltage flag record 59 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 60 Measured: -16.996 expected: -16.996
- %PASS Flash readback error of external measurement record 60 Measured: -16.999 expected: -16.999
- %PASS Flash readback error of internal measurement record 60 Measured: -16.976 expected: -16.976
- %PASS Flash readback error of voltage flag record 60 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 61 Measured: -17.996 expected: -17.996
- %PASS Flash readback error of external measurement record 61 Measured: -17.999 expected: -17.999
- %PASS Flash readback error of internal measurement record 61 Measured: -17.975 expected: -17.975
- %PASS Flash readback error of voltage flag record 61 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 62 Measured: -21.995 expected: -21.995
- %PASS Flash readback error of external measurement record 62 Measured: -21.999 expected: -21.999
- %PASS Flash readback error of internal measurement record 62

- Measured: -21.971 expected: -21.971
- %PASS Flash readback error of voltage flag record 62 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 63 Measured: -22.995 expected: -22.995
- %PASS Flash readback error of external measurement record 63 Measured: -22.999 expected: -22.999
- %PASS Flash readback error of internal measurement record 63 Measured: -22.971 expected: -22.971
- %PASS Flash readback error of voltage flag record 63 Measured: 1 expected: 1
- %PASS Flash readback error of force DAC record 64 Measured: 9999 expected: 9999
- %PASS Flash readback error of external measurement record 64 Measured: 9999 expected: 9999
- %PASS Flash readback error of internal measurement record 64 Measured: 9999 expected: 9999
- %PASS Flash readback error of voltage flag record 64 Measured: 9999 expected: 9999
- %PASS Flash readback error of force DAC record 65 Measured: 9999 expected: 9999
- %PASS Flash readback error of external measurement record 65 Measured: 9999 expected: 9999
- %PASS Flash readback error of internal measurement record 65 Measured: 9999 expected: 9999

- %PASS Flash readback error of voltage flag record 65 Measured: 9999 expected: 9999
- %PASS Flash readback error of force DAC record 66 Measured: 9999 expected: 9999
- %PASS Flash readback error of external measurement record 66 Measured: 9999 expected: 9999
- %PASS Flash readback error of internal measurement record 66 Measured: 9999 expected: 9999
- %PASS Flash readback error of voltage flag record 66 Measured: 9999 expected: 9999
- %PASS Flash readback error of force DAC record 67 Measured: 9999 expected: 9999
- %PASS Flash readback error of external measurement record 67 Measured: 9999 expected: 9999
- %PASS Flash readback error of internal measurement record 67 Measured: 9999 expected: 9999
- %PASS Flash readback error of voltage flag record 67 Measured: 9999 expected: 9999
- %PASS Flash readback error of force DAC record 68 Measured: 9999 expected: 9999
- %PASS Flash readback error of external measurement record 68 Measured: 9999 expected: 9999
- %PASS Flash readback error of internal measurement record 68 Measured: 9999 expected: 9999
- %PASS Flash readback error of voltage flag record 68 Measured: 9999 expected: 9999

- %PASS Flash readback error of force DAC record 69 Measured: 9999 expected: 9999
- %PASS Flash readback error of external measurement record 69 Measured: 9999 expected: 9999
- %PASS Flash readback error of internal measurement record 69 Measured: 9999 expected: 9999
- %PASS Flash readback error of voltage flag record 69 Measured: 9999 expected: 9999
- %PASS Flash readback error of force DAC record 70 Measured: 9999 expected: 9999
- %PASS Flash readback error of external measurement record 70 Measured: 9999 expected: 9999
- %PASS Flash readback error of internal measurement record 70 Measured: 9999 expected: 9999
- %PASS Flash readback error of voltage flag record 70 Measured: 9999 expected: 9999
- %PASS Flash readback error of force DAC record 71 Measured: 9999 expected: 9999
- %PASS Flash readback error of external measurement record 71 Measured: 9999 expected: 9999
- %PASS Flash readback error of internal measurement record 71 Measured: 9999 expected: 9999
- %PASS Flash readback error of voltage flag record 71 Measured: 9999 expected: 9999
- $\ensuremath{\mathrm{\%PASS}}$ Flash readback error of force DAC record 72

- Measured: 9999 expected: 9999
- %PASS Flash readback error of external measurement record 72 Measured: 9999 expected: 9999
- %PASS Flash readback error of internal measurement record 72 Measured: 9999 expected: 9999
- %PASS Flash readback error of voltage flag record 72 Measured: 9999 expected: 9999
- %PASS Flash readback error of force DAC record 73 Measured: 9999 expected: 9999
- %PASS Flash readback error of external measurement record 73 Measured: 9999 expected: 9999
- %PASS Flash readback error of internal measurement record 73 Measured: 9999 expected: 9999
- %PASS Flash readback error of voltage flag record 73 Measured: 9999 expected: 9999
- %PASS Flash readback error of force DAC record 74 Measured: 9999 expected: 9999
- %PASS Flash readback error of external measurement record 74 Measured: 9999 expected: 9999
- %PASS Flash readback error of internal measurement record 74 Measured: 9999 expected: 9999
- %PASS Flash readback error of voltage flag record 74 Measured: 9999 expected: 9999
- %JOB_END ****PASSED**** CUB External Calibration of slot 18 (26DA50) at 2:39:13 PM

Slot18_CALCUB_ExternalPV

- %JOB_START Beginning CUB External Verification test on slot 18 at 2:55:53

 PM on 2/29/2020

 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB

 # 30562A2 Rev 1137A
 - Performing source and measure voltage verification...
 - %PASS CALCUB test of source voltage at -24V Measured: -23.999395 low limit: -24.005417 high limit: -23.994582
 - %PASS CALCUB test of voltage measure at -24V Measured: -23.999987 low limit: -24.002446 high limit: -23.996343
 - %PASS CALCUB test of source voltage at -23V Measured: -22.999517 low limit: -23.005217 high limit: -22.994782
 - %PASS CALCUB test of voltage measure at -23V Measured: -22.999757 low limit: -23.002569 high limit: -22.996465
 - %PASS CALCUB test of source voltage at -22V Measured: -21.999556 low limit: -22.005017 high limit: -21.994982
 - %PASS CALCUB test of voltage measure at -22V Measured: -21.999826 low limit: -22.002607 high limit: -21.996504
 - %PASS CALCUB test of source voltage at -21V Measured: -20.999612 low limit: -21.004817 high limit: -20.995182
 - %PASS CALCUB test of voltage measure at -21V Measured: -20.999745 low limit: -21.002664 high limit: -20.996560
 - %PASS CALCUB test of source voltage at -20V Measured: -20.000008 low limit: -20.004617 high limit: -19.995382
 - %PASS CALCUB test of voltage measure at -20V Measured: -20.000244 low limit: -20.003060 high limit: -19.996956

- %PASS CALCUB test of source voltage at -19V Measured: -18.999549 low limit: -19.004417 high limit: -18.995582
- %PASS CALCUB test of voltage measure at -19V Measured: -18.999787 low limit: -19.002601 high limit: -18.996498
- %PASS CALCUB test of source voltage at -18V Measured: -17.999533 low limit: -18.004217 high limit: -17.995782
- %PASS CALCUB test of voltage measure at -18V Measured: -17.999733 low limit: -18.002585 high limit: -17.996481
- %PASS CALCUB test of source voltage at -17V Measured: -16.999554 low limit: -17.004017 high limit: -16.995982
- %PASS CALCUB test of voltage measure at -17V Measured: -16.999595 low limit: -17.002605 high limit: -16.996502
- %PASS CALCUB test of source voltage at -16V Measured: -15.999867 low limit: -16.003817 high limit: -15.996182
- %PASS CALCUB test of voltage measure at -16V Measured: -15.999926 low limit: -16.002919 high limit: -15.996815
- %PASS CALCUB test of source voltage at -15V Measured: -14.999908 low limit: -15.003617 high limit: -14.996382
- %PASS CALCUB test of voltage measure at -15V Measured: -14.999902 low limit: -15.002960 high limit: -14.996856
- %PASS CALCUB test of source voltage at -14V Measured: -13.999854 low limit: -14.003417 high limit: -13.996582
- %PASS CALCUB test of voltage measure at -14V Measured: -13.999781 low limit: -14.002906 high limit: -13.996802
- %PASS CALCUB test of source voltage at -13V Measured: -12.999782 low limit: -13.003217 high limit: -12.996782

- %PASS CALCUB test of voltage measure at -13V Measured: -12.999853 low limit: -13.002834 high limit: -12.996730
- %PASS CALCUB test of source voltage at -12V Measured: -11.999761 low limit: -12.003017 high limit: -11.996982
- %PASS CALCUB test of voltage measure at -12V

 Measured: -11.999640 low limit: -12.002813 high limit: -11.996709
- %PASS CALCUB test of source voltage at -11V Measured: -11.000154 low limit: -11.002817 high limit: -10.997182
- %PASS CALCUB test of voltage measure at -11V Measured: -11.000128 low limit: -11.003206 high limit: -10.997103
- %PASS CALCUB test of source voltage at -10V Measured: -10.000093 low limit: -10.002617 high limit: -9.997382
- %PASS CALCUB test of voltage measure at -10V Measured: -10.000213 low limit: -10.003145 high limit: -9.997042
- %PASS CALCUB test of source voltage at -9V Measured: -9.000021 low limit: -9.002417 high limit: -8.997582
- %PASS CALCUB test of voltage measure at -9V Measured: -9.000031 low limit: -9.000784 high limit: -8.999258
- %PASS CALCUB test of source voltage at -8V

 Measured: -8.000031 low limit: -8.002217 high limit: -7.997782
- %PASS CALCUB test of voltage measure at -8V Measured: -8.000048 low limit: -8.000794 high limit: -7.999268
- %PASS CALCUB test of source voltage at -7V

 Measured: -7.000012 low limit: -7.002017 high limit: -6.997982
- %PASS CALCUB test of voltage measure at -7V

- Measured: -7.000015 low limit: -7.000775 high limit: -6.999250
- %PASS CALCUB test of source voltage at -6V
 Measured: -6.000004 low limit: -6.001817 high limit: -5.998182
- %PASS CALCUB test of voltage measure at -6V
 Measured: -6.000041 low limit: -6.000767 high limit: -5.999241
- %PASS CALCUB test of source voltage at -5V

 Measured: -5.000044 low limit: -5.001617 high limit: -4.998382
- %PASS CALCUB test of voltage measure at -5V

 Measured: -5.000064 low limit: -5.000807 high limit: -4.999281
- %PASS CALCUB test of source voltage at -4V
 Measured: -4.000028 low limit: -4.001417 high limit: -3.998582
- %PASS CALCUB test of voltage measure at -4V

 Measured: -4.000066 low limit: -4.000791 high limit: -3.999265
- %PASS CALCUB test of source voltage at -3V Measured: -2.999654 low limit: -3.001217 high limit: -2.998782
- %PASS CALCUB test of voltage measure at -3V Measured: -2.999677 low limit: -3.000417 high limit: -2.998891
- %PASS CALCUB test of source voltage at -2V

 Measured: -2.000020 low limit: -2.001017 high limit: -1.998982
- %PASS CALCUB test of voltage measure at -2V Measured: -2.000021 low limit: -2.000783 high limit: -1.999258
- %PASS CALCUB test of source voltage at -2V at 200mA

 Measured: -1.999571 low limit: -2.001235 high limit: -1.998764
- %PASS CALCUB test of source voltage at -1V
 Measured: -1.000082 low limit: -1.000817 high limit: -0.9991820

- %PASS CALCUB test of voltage measure at -1V

 Measured: -1.000103 low limit: -1.000845 high limit: -0.9993195
- %PASS CALCUB test of source voltage at 0V
 Measured: -0.0001104743 low limit: -6.179903E-04 high limit: 6.179903E-04
- %PASS CALCUB test of voltage measure at 0V Measured: -3.420062E-05 low limit: -8.734370E-04 high limit: 6.524883E-04
- %PASS CALCUB test of source voltage at 1V

 Measured: 1.000341 low limit: 0.9991820 high limit: 1.000817
- %PASS CALCUB test of voltage measure at 1V

 Measured: 1.000338 low limit: 0.9995780 high limit: 1.001104
- %PASS CALCUB test of source voltage at 2V Measured: 1.999879 low limit: 1.998982 high limit: 2.001017
- %PASS CALCUB test of voltage measure at 2V Measured: 1.999929 low limit: 1.999116 high limit: 2.000642
- %PASS CALCUB test of source voltage at 2V at 200mA Measured: 1.999470 low limit: 1.998764 high limit: 2.001235
- %PASS CALCUB test of source voltage at 3V Measured: 2.999928 low limit: 2.998782 high limit: 3.001217
- %PASS CALCUB test of voltage measure at 3V Measured: 2.999966 low limit: 2.999165 high limit: 3.000691
- %PASS CALCUB test of source voltage at 3V with DGS perturbed high Measured: 3.177591 low limit: 3.1 high limit: 3.3
- %PASS CALCUB test of voltage measure at 3V with DGS perturbed high Measured: 2.999967 low limit: 2.999165 high limit: 3.000691
- %PASS CALCUB test of source voltage at 3V with DGS perturbed low Measured: 2.776746 low limit: 2.7 high limit: 2.9

- %PASS CALCUB test of voltage measure at 3V with DGS perturbed low Measured: 2.999960 low limit: 2.999165 high limit: 3.000691
- %PASS CALCUB test of source voltage at 4V $\label{eq:pass} \mbox{Measured: 3.998899 low limit: 3.998582 high limit: 4.001417}$
- %PASS CALCUB test of voltage measure at 4V Measured: 3.999881 low limit: 3.999136 high limit: 4.000662
- %PASS CALCUB test of source voltage at 5V Measured: 4.999916 low limit: 4.998382 high limit: 5.001617
- %PASS CALCUB test of voltage measure at 5V Measured: 4.999934 low limit: 4.999154 high limit: 5.000679
- %PASS CALCUB test of source voltage at 6V Measured: 6.000309 low limit: 5.998182 high limit: 6.001817
- %PASS CALCUB test of voltage measure at 6V Measured: 6.000314 low limit: 5.999546 high limit: 6.001072
- %PASS CALCUB test of source voltage at 7V Measured: 6.999910 low limit: 6.997982 high limit: 7.002017
- %PASS CALCUB test of voltage measure at 7V Measured: 6.999975 low limit: 6.999147 high limit: 7.000673
- %PASS CALCUB test of source voltage at 8V Measured: 7.999949 low limit: 7.997782 high limit: 8.002217
- %PASS CALCUB test of voltage measure at 8V Measured: 7.999984 low limit: 7.999187 high limit: 8.000712
- %PASS CALCUB test of source voltage at 9V Measured: 8.999945 low limit: 8.997582 high limit: 9.002417
- %PASS CALCUB test of voltage measure at 9V

- Measured: 8.999947 low limit: 8.999182 high limit: 9.000708
- %PASS CALCUB test of source voltage at 10V Measured: 9.999954 low limit: 9.997382 high limit: 10.002617
- %PASS CALCUB test of voltage measure at 10V Measured: 9.999957 low limit: 9.996902 high limit: 10.003006
- %PASS CALCUB test of source voltage at 11V Measured: 10.999697 low limit: 10.997182 high limit: 11.002817
- %PASS CALCUB test of voltage measure at 11V Measured: 10.999601 low limit: 10.996645 high limit: 11.002749
- %PASS CALCUB test of source voltage at 12V Measured: 11.999809 low limit: 11.996982 high limit: 12.003017
- %PASS CALCUB test of voltage measure at 12V Measured: 11.999667 low limit: 11.996757 high limit: 12.002861
- %PASS CALCUB test of source voltage at 13V Measured: 12.999846 low limit: 12.996782 high limit: 13.003217
- %PASS CALCUB test of voltage measure at 13V Measured: 13.000052 low limit: 12.996794 high limit: 13.002898
- %PASS CALCUB test of source voltage at 14V
 Measured: 14.000040 low limit: 13.996582 high limit: 14.003417
- %PASS CALCUB test of voltage measure at 14V

 Measured: 13.999931 low limit: 13.996988 high limit: 14.003092
- %PASS CALCUB test of source voltage at 15V Measured: 15.000189 low limit: 14.996382 high limit: 15.003617
- %PASS CALCUB test of voltage measure at 15V Measured: 15.000216 low limit: 14.997137 high limit: 15.003241

- %PASS CALCUB test of source voltage at 16V Measured: 15.999872 low limit: 15.996182 high limit: 16.003817
- %PASS CALCUB test of voltage measure at 16V
 Measured: 15.999909 low limit: 15.996820 high limit: 16.002924
- %PASS CALCUB test of source voltage at 17V Measured: 17.000006 low limit: 16.995982 high limit: 17.004017
- %PASS CALCUB test of voltage measure at 17V Measured: 17.000062 low limit: 16.996954 high limit: 17.003058
- %PASS CALCUB test of source voltage at 18V

 Measured: 18.000117 low limit: 17.995782 high limit: 18.004217
- %PASS CALCUB test of voltage measure at 18V Measured: 18.000079 low limit: 17.997065 high limit: 18.003169
- %PASS CALCUB test of source voltage at 19V Measured: 19.000278 low limit: 18.995582 high limit: 19.004417
- %PASS CALCUB test of voltage measure at 19V Measured: 19.000258 low limit: 18.997227 high limit: 19.003330
- %PASS CALCUB test of source voltage at 20V Measured: 19.999605 low limit: 19.995382 high limit: 20.004617
- %PASS CALCUB test of voltage measure at 20V Measured: 19.999665 low limit: 19.996553 high limit: 20.002657
- %PASS CALCUB test of source voltage at 21V Measured: 20.999745 low limit: 20.995182 high limit: 21.004817
- %PASS CALCUB test of voltage measure at 21V Measured: 20.999720 low limit: 20.996693 high limit: 21.002797

- %PASS CALCUB test of voltage measure at 22V Measured: 21.999863 low limit: 21.996784 high limit: 22.002887
- %PASS CALCUB test of source voltage at 23V Measured: 22.999924 low limit: 22.994782 high limit: 23.005217
- %PASS CALCUB test of voltage measure at 23V Measured: 22.999957 low limit: 22.996872 high limit: 23.002976
- %PASS CALCUB test of source voltage at 24V Measured: 24.000057 low limit: 23.994582 high limit: 24.005417
- %PASS CALCUB test of voltage measure at 24V
 Measured: 24.000060 low limit: 23.997005 high limit: 24.003109
- Performing current measure verification...
- $\mbox{\%PASS}$ CALCUB test of current measure at -0.00002494145682mA with 2Mohm source impedance

Measured: -2.5174E-05 low limit: -2.6162E-05 high limit: -2.3720E-05

 $\mbox{\%PASS}$ - CALCUB test of current measure at 0.00002438111725mA with 2Mohm source impedance

Measured: 2.4220E-05 low limit: 2.3160E-05 high limit: 2.5601E-05

 $\mbox{\%PASS}$ - CALCUB test of current measure at -0.00005005260419mA with 2Mohm source impedance

Measured: -4.9807E-05 low limit: -5.1273E-05 high limit: -4.8831E-05

 $\mbox{\%PASS}$ - CALCUB test of current measure at 0.000049174607mA with 2Mohm source impedance

Measured: 4.9030E-05 low limit: 4.7953E-05 high limit: 5.0395E-05

 $\mbox{\it \%PASS}$ - CALCUB test of current measure at -0.00009985292737mA with 2Mohm source impedance

Measured: -9.9583E-05 low limit: -1.0107E-04 high limit: -9.8632E-05

 $\ensuremath{\mathrm{\%PASS}}$ - CALCUB test of current measure at $0.00009890999166 \mathrm{mA}$ with 2Mohm source

Measured: 9.8787E-05 low limit: 9.7689E-05 high limit: 1.0013E-04

 $\mbox{\it \%PASS}$ - CALCUB test of current measure at -0.0001991514421mA with 2Mohm source impedance

Measured: -1.9915E-04 low limit: -2.0037E-04 high limit: -1.9793E-04

 $\mbox{\%PASS}$ - CALCUB test of current measure at 0.0001983984902mA with 2Mohm source impedance

Measured: 1.9812E-04 low limit: 1.9717E-04 high limit: 1.9961E-04

 $\mbox{\%PASS}$ - CALCUB test of current measure at -0.0004975206896mA with 2Mohm source impedance

Measured: -4.9732E-04 low limit: -4.9874E-04 high limit: -4.9629E-04

 $\mbox{\%PASS}$ - CALCUB test of current measure at 0.000496794512mA with 2Mohm source impedance

Measured: 4.9651E-04 low limit: 4.9557E-04 high limit: 4.9801E-04

 $\mbox{\%PASS}$ - CALCUB test of current measure at -0.001014289516mA with 2Mohm source impedance

Measured: -1.0141E-03 low limit: -1.0155E-03 high limit: -1.0130E-03

 $\mbox{\%PASS}$ - CALCUB test of current measure at $0.001013373767\mbox{mA}$ with 2Mohm source impedance

Measured: 1.0131E-03 low limit: 1.0121E-03 high limit: 1.0145E-03

 $\mbox{\%PASS}$ - CALCUB test of current measure at -0.002028152693mA with 2Mohm source impedance

Measured: -2.0279E-03 low limit: -2.0293E-03 high limit: -2.0269E-03

 $\mbox{\%PASS}$ - CALCUB test of current measure at 0.00202707307mA with 2Mohm source impedance

Measured: 2.0269E-03 low limit: 2.0258E-03 high limit: 2.0282E-03

 $\mbox{\%PASS}$ - CALCUB test of current measure at -0.0002104708839mA with 200Kohm source impedance

Measured: -2.1057E-04 low limit: -2.2267E-04 high limit: -1.9826E-04

 $\mbox{\%PASS}$ - CALCUB test of current measure at 0.0002037760442mA with 200Kohm source impedance

Measured: 2.0122E-04 low limit: 1.9156E-04 high limit: 2.1598E-04

 $\mbox{\%PASS}$ - CALCUB test of current measure at -0.0004169748744mA with 200Kohm source impedance

Measured: -4.1499E-04 low limit: -4.2918E-04 high limit: -4.0476E-04

 $\mbox{\%PASS}$ - CALCUB test of current measure at $0.0004102883968\mbox{mA}$ with 200Kohm source impedance

Measured: 4.0807E-04 low limit: 3.9808E-04 high limit: 4.2249E-04

 $\mbox{\%PASS}$ - CALCUB test of current measure at -0.0009936999927mA with 200Kohm source impedance

Measured: -9.9187E-04 low limit: -1.0059E-03 high limit: -9.8149E-04

 $\mbox{\%PASS}$ - CALCUB test of current measure at 0.0009857152951mA with 200Kohm source impedance

Measured: 9.8328E-04 low limit: 9.7350E-04 high limit: 9.9792E-04

 $\mbox{\%PASS}$ - CALCUB test of current measure at -0.001984728535mA with 200Kohm source impedance

Measured: -1.9835E-03 low limit: -1.9969E-03 high limit: -1.9725E-03

 $\mbox{\%PASS}$ - CALCUB test of current measure at 0.001974616569mA with 200Kohm source impedance

Measured: 1.9726E-03 low limit: 1.9624E-03 high limit: 1.9868E-03

 $\mbox{\%PASS}$ - CALCUB test of current measure at -0.004953460812mA with 200Kohm source impedance

Measured: -4.9525E-03 low limit: -4.9656E-03 high limit: -4.9412E-03

 $\mbox{\%PASS}$ - CALCUB test of current measure at $0.004945625051\mbox{mA}$ with 200Kohm source impedance

Measured: 4.9439E-03 low limit: 4.9334E-03 high limit: 4.9578E-03

%PASS - CALCUB test of current measure at -0.01012451488mA with 200Kohm source

Measured: -1.0124E-02 low limit: -1.0136E-02 high limit: -1.0112E-02

 $\mbox{\%PASS}$ - CALCUB test of current measure at 0.01011444128mA with 200Kohm source impedance

Measured: 1.0113E-02 low limit: 1.0102E-02 high limit: 1.0126E-02

 $\mbox{\it \%PASS}$ - CALCUB test of current measure at -0.02024489969mA with 200Kohm source impedance

Measured: -2.0245E-02 low limit: -2.0257E-02 high limit: -2.0232E-02

 $\mbox{\it \%PASS}$ - CALCUB test of current measure at 0.02023479925mA with 200Kohm source impedance

Measured: 2.0234E-02 low limit: 2.0222E-02 high limit: 2.0247E-02

 $\mbox{\it \%PASS}$ - CALCUB test of current measure at -0.002030909898mA with 20Kohm source impedance

Measured: -2.0276E-03 low limit: -2.1529E-03 high limit: -1.9088E-03

 $\mbox{\%PASS}$ - CALCUB test of current measure at 0.00196634451mA with 20Kohm source impedance

Measured: 1.9443E-03 low limit: 1.8442E-03 high limit: 2.0884E-03

 $\mbox{\%PASS}$ - CALCUB test of current measure at -0.004023020303mA with 20Kohm source impedance

Measured: -4.0081E-03 low limit: -4.1450E-03 high limit: -3.9009E-03

 $\mbox{\%PASS}$ - CALCUB test of current measure at $0.003958648554 \mbox{mA}$ with 20Kohm source impedance

Measured: 3.9373E-03 low limit: 3.8365E-03 high limit: 4.0807E-03

 $\mbox{\%PASS}$ - CALCUB test of current measure at -0.009759518857mA with 20Kohm source impedance

Measured: -9.7372E-03 low limit: -9.8815E-03 high limit: -9.6374E-03

 $\mbox{\%PASS}$ - CALCUB test of current measure at 0.009681057922mA with 20Kohm source impedance

Measured: 9.6562E-03 low limit: 9.5589E-03 high limit: 9.8031E-03

 $\mbox{\%PASS}$ - CALCUB test of current measure at -0.01949308048mA with 20Kohm source impedance

Measured: -1.9470E-02 low limit: -0.019615 high limit: -0.019371

 $\mbox{\%PASS}$ - CALCUB test of current measure at 0.01939366435mA with 20Kohm source impedance

Measured: 1.9371E-02 low limit: 0.019271 high limit: 0.019515

 $\mbox{\%PASS}$ - CALCUB test of current measure at -0.04865093004mA with 20Kohm source impedance

Measured: -4.8638E-02 low limit: -0.048773 high limit: -0.048528

 $\mbox{\%PASS}$ - CALCUB test of current measure at 0.04857195887mA with 20Kohm source impedance

Measured: 4.8557E-02 low limit: 0.048449 high limit: 0.048694

 $\mbox{\%PASS}$ - CALCUB test of current measure at -0.1011821869mA with 20Kohm source impedance

Measured: -0.10117 low limit: -0.10130 high limit: -0.10106

 $\mbox{\%PASS}$ - CALCUB test of current measure at 0.101081832mA with 20Kohm source impedance

Measured: 0.10107 low limit: 0.10095 high limit: 0.10120

 $\mbox{\%PASS}$ - CALCUB test of current measure at -0.2023283405mA with 20Kohm source impedance

Measured: -0.20232 low limit: -0.20245 high limit: -0.20220

 $\mbox{\%PASS}$ - CALCUB test of current measure at 0.2022283994mA with 20Kohm source impedance

Measured: 0.20222 low limit: 0.20210 high limit: 0.20235

 $\mbox{\%PASS}$ - CALCUB test of current measure at -0.01771432722mA with 2Kohm source impedance

Measured: -1.7636E-02 low limit: -1.8935E-02 high limit: -1.6493E-02

 $\mbox{\%PASS}$ - CALCUB test of current measure at 0.01715266389mA with 2Kohm source

Measured: 1.6864E-02 low limit: 1.5931E-02 high limit: 1.8373E-02

 $\mbox{\%PASS}$ - CALCUB test of current measure at -0.03509828413mA with 2Kohm source impedance

Measured: -3.5033E-02 low limit: -3.6319E-02 high limit: -3.3877E-02

 $\mbox{\%PASS}$ - CALCUB test of current measure at $0.03453274743\mbox{mA}$ with 2Kohm source impedance

Measured: 3.4274E-02 low limit: 3.3312E-02 high limit: 3.5753E-02

 $\mbox{\%PASS}$ - CALCUB test of current measure at -0.09708607475mA with 2Kohm source impedance

Measured: -9.6862E-02 low limit: -9.8306E-02 high limit: -9.5865E-02

 $\mbox{\%PASS}$ - CALCUB test of current measure at 0.096306943mA with 2Kohm source impedance

Measured: 9.6059E-02 low limit: 9.5086E-02 high limit: 9.7527E-02

 $\mbox{\%PASS}$ - CALCUB test of current measure at -0.1939044373mA with 2Kohm source impedance

Measured: -0.19369 low limit: -0.19512 high limit: -0.19268

 $\mbox{\%PASS}$ - CALCUB test of current measure at 0.1929235118mA with 2Kohm source impedance

Measured: 0.19268 low limit: 0.19170 high limit: 0.19414

 $\mbox{\%PASS}$ - CALCUB test of current measure at -0.4839595682mA with 2Kohm source impedance

Measured: -0.48385 low limit: -0.48518 high limit: -0.48273

 $\mbox{\%PASS}$ - CALCUB test of current measure at 0.4831964863mA with 2Kohm source impedance

Measured: 0.48307 low limit: 0.48197 high limit: 0.48441

 $\mbox{\it \%PASS}$ - CALCUB test of current measure at -1.010734901mA with 2Kohm source impedance

Measured: -1.0106 low limit: -1.0119 high limit: -1.0095

 $\mbox{\%PASS}$ - CALCUB test of current measure at 1.009739097mA with 2Kohm source impedance

Measured: 1.0096 low limit: 1.0085 high limit: 1.0109

 $\mbox{\%PASS}$ - CALCUB test of current measure at -2.021001307mA with 2Kohm source impedance

Measured: -2.0209 low limit: -2.0222 high limit: -2.0197

 $\mbox{\%PASS}$ - CALCUB test of current measure at 2.020030586mA with 2Kohm source impedance

Measured: 2.0200 low limit: 2.0188 high limit: 2.0212

 $\mbox{\%PASS}$ - CALCUB test of current measure at -0.1707105582mA with 200ohm source impedance

Measured: -0.16971 low limit: -0.18291 high limit: -0.15850

 $\mbox{\%PASS}$ - CALCUB test of current measure at $0.165275011\mbox{mA}$ with 200ohm source impedance

Measured: 0.16271 low limit: 0.15306 high limit: 0.17748

 $\mbox{\%PASS}$ - CALCUB test of current measure at -0.3381665052mA with 200ohm source impedance

Measured: -0.33737 low limit: -0.35037 high limit: -0.32595

 $\mbox{\%PASS}$ - CALCUB test of current measure at 0.3327592811mA with 200ohm source impedance

Measured: 0.33043 low limit: 0.32055 high limit: 0.34496

 $\mbox{\%PASS}$ - CALCUB test of current measure at -0.9607391423mA with 200ohm source impedance

Measured: -0.95844 low limit: -0.97294 high limit: -0.94853

 $\mbox{\%PASS}$ - CALCUB test of current measure at 0.9529138069mA with 200ohm source impedance

Measured: 0.95040 low limit: 0.94070 high limit: 0.96512

%PASS - CALCUB test of current measure at -1.918814177mA with 200ohm source

Measured: -1.9166 low limit: -1.9310 high limit: -1.9066

 $\mbox{\%PASS}$ - CALCUB test of current measure at 1.909159319mA with 200ohm source impedance

Measured: 1.9071 low limit: 1.8969 high limit: 1.9213

 $\mbox{\it \%PASS}$ - CALCUB test of current measure at -4.788975226mA with 200ohm source impedance

Measured: -4.7875 low limit: -4.8011 high limit: -4.7767

 $\mbox{\%PASS}$ - CALCUB test of current measure at $4.781890603\mbox{mA}$ with 200ohm source impedance

Measured: 4.7810 low limit: 4.7696 high limit: 4.7940

 $\mbox{\it \%PASS}$ - CALCUB test of current measure at -9.995232305mA with 200ohm source impedance

Measured: -9.9947 low limit: -10.0074 high limit: -9.9830

 $\mbox{\%PASS}$ - CALCUB test of current measure at 9.987065974mA with 200ohm source impedance

Measured: 9.9866 low limit: 9.9748 high limit: 9.9992

 $\mbox{\%PASS}$ - CALCUB test of current measure at -19.98592717mA with 200ohm source impedance

Measured: -19.9855 low limit: -19.9981 high limit: -19.9737

 $\mbox{\%PASS}$ - CALCUB test of current measure at 19.97949824mA with 200ohm source impedance

Measured: 19.9798 low limit: 19.9672 high limit: 19.9917

 $\mbox{\%PASS}$ - CALCUB test of current measure at -1.592937708mA with 20ohm source impedance

Measured: -1.5889 low limit: -1.7150 high limit: -1.4708

 $\mbox{\%PASS}$ - CALCUB test of current measure at 1.541981492mA with 20ohm source impedance

Measured: 1.5133 low limit: 1.4199 high limit: 1.6640

 $\mbox{\%PASS}$ - CALCUB test of current measure at -3.155685028mA with 20ohm source impedance

Measured: -3.1543 low limit: -3.2777 high limit: -3.0336

 $\mbox{\%PASS}$ - CALCUB test of current measure at 3.106549858mA with 20ohm source impedance

Measured: 3.0817 low limit: 2.9844 high limit: 3.2286

 $\mbox{\%PASS}$ - CALCUB test of current measure at -8.703809787mA with 20ohm source impedance

Measured: -8.6842 low limit: -8.8258 high limit: -8.5817

 $\mbox{\%PASS}$ - CALCUB test of current measure at 8.647187418mA with 20ohm source impedance

Measured: 8.6207 low limit: 8.5251 high limit: 8.7692

 $\mbox{\%PASS}$ - CALCUB test of current measure at -17.38516615mA with 20ohm source impedance

Measured: -17.3676 low limit: -17.5072 high limit: -17.2630

 $\mbox{\%PASS}$ - CALCUB test of current measure at 17.32404885mA with 20ohm source impedance

Measured: 17.2985 low limit: 17.2019 high limit: 17.4461

 $\mbox{\%PASS}$ - CALCUB test of current measure at -43.38854705mA with 20ohm source impedance

Measured: -43.3834 low limit: -43.5106 high limit: -43.2664

 $\mbox{\%PASS}$ - CALCUB test of current measure at 43.39172432mA with 20ohm source impedance

Measured: 43.3796 low limit: 43.2696 high limit: 43.5137

 $\mbox{\it \%PASS}$ - CALCUB test of current measure at -90.78619028mA with 20ohm source impedance

Measured: -90.7858 low limit: -90.9082 high limit: -90.6641

%PASS - CALCUB test of current measure at 90.84941135mA with 20ohm source

Measured: 90.8479 low limit: 90.7273 high limit: 90.9714

 $\mbox{\%PASS}$ - CALCUB test of current measure at -9.674014458mA with 20hm source impedance

Measured: -9.6058 low limit: -10.8947 high limit: -8.4532

- %PASS CALCUB test of current measure at 9.427122437mA with 20hm source impedance Measured: 9.2418 low limit: 8.2063 high limit: 10.6478
- %PASS CALCUB test of current measure at -19.16909755mA with 20hm source impedance

 Measured: -19.0930 low limit: -20.3898 high limit: -17.9483
- %PASS CALCUB test of current measure at 18.99671732mA with 20hm source impedance Measured: 18.7809 low limit: 17.7759 high limit: 20.2174
- %PASS CALCUB test of current measure at -47.51000873mA with 20hm source impedance

 Measured: -47.3075 low limit: -48.7307 high limit: -46.2892
- %PASS CALCUB test of current measure at 47.54826621mA with 20hm source impedance Measured: 47.3559 low limit: 46.3275 high limit: 48.7690
- %PASS CALCUB test of current measure at -94.80292434mA with 20hm source impedance

 Measured: -94.6421 low limit: -96.0236 high limit: -93.5821
- %PASS CALCUB test of current measure at 95.17797993mA with 20hm source impedance Measured: 95.0461 low limit: 93.9572 high limit: 96.3987
- %PASS CALCUB test of current measure at -237.1768528mA with 20hm source impedance

 Measured: -237.2198 low limit: -238.3975 high limit: -235.9561
- % PASS CALCUB test of current measure at 238.6905495mA with 20hm source impedance Measured: 238.7764 low limit: 237.4698 high limit: 239.9112

- Performing 100Mhz clock accuracy verification...

%PASS - CALCUB Clock 100 External Performance Verification test.

Measured: 99999945.2 low limit: 99996000 high limit: 100004000

%JOB_END - ****PASSED**** CUB External Verification of slot 18 (26DA50) at 3:05:31 PM