基板配列/ Board configuration

29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	12	11	10	9	8	7	6	5	4	3	2	1	
						PS		PS	PSL		PS		PS	PSL							DC	DC	DCL	DC	DC	DCL	IFL	IFL									TP4	TP3	TP2	TP1	
						17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	
						LOG	ANA	DRVIO		ANA	IOVIO	IOVIO			ANA	DRVIO		ANA	IOVIO	IOVIO			CBOX	FTUIF	CHVD	CHVD			FCDST	TG	FC	ALPG	FMCNT	SC						TERM	