

//Sun Apr 03 05:19:42 2022

```
CpuIfBusIf:: IMask(0x04)= 0x3ff Status= 0x7000250
// Configuring Credence_TestHeadIf in slot[0]
// Configuring NPTest_6AmpDPS in slot[2]
// Configuring NPTest_QBIX in slot[3]
// Configuring NPTest_D4032PS in slot[4]
// Configuring NPTest_D4032PS in slot[5]
// Configuring NPTest_D4032PS in slot[6]
// Configuring NPTest_D4032PS in slot[7]
// Configuring NPTest_D4032PS in slot[8]
// Configuring NPTest_D3208PSB in slot[9]
// Configuring NPTest_D4032PS in slot[11]
// Configuring NPTest_D4032PS in slot[12]
// Configuring NPTest_D4032PS in slot[13]
// Configuring NPTest_D4032PS in slot[26]
// Configuring NPTest_D4032PS in slot[27]
// Configuring NPTest_D4032PS in slot[28]
// Configuring NPTest_D4032PS in slot[31]
// Configuring NPTest_D4032PS in slot[32]
// Configuring NPTest_D4032PS in slot[33]
// Configuring NPTest_D4032PS in slot[34]
// Configuring NPTest_QBIX in slot[36]
// Configuring Credence_GenericLoadBoard in LoadBoard
```

//Sun Apr 03 05:19:48 2022

//Sun Apr 03 05:19:49 2022

```
Slot[0]:NPTest_TestHeadIf, PN# 96152014, SN# 96152014-0730041R, Rev 12
  BusIf:: Addr/Rsrc 0x00000/0x0100, BusCal 0x061, GCN 0  ++IsAlive++
  Mod:SMPwr::  *Unknown*,          PN# ,          SN# ,          Rev *
++IsAlive++
  Mod:Main::  NPtTHIF_Main,  PN# 97152014, SN# 97152014-0721008BA,Rev 04
Configured
  Fpga:Thif::  DesignRev 0x03120700, UserCode 0xffffffff, EepUCode 0xffffffff,
```

ImageRev 0x03120700, EepId[0,1] 0xf5036093, Compared against Thif, BuildId N/A,
ImageFile D:\XTOS_release\XTOS_4.1.P02\Fpga\Credence_TestHeadIf\thif_fpga.svf

Fpga:Sm:: DesignRev 0x0000000e, DevId 0x01038093, EepId[0] 0x05036093,
EepId[1] 0xf5036093

Slot[2]:NPTest_6AmpDPS, PN# 96152005, SN# 96152005-869843, Rev 10, UniqId
013a1e410a0000c4

BusIf:: Addr/Rsrc 0x00400/0x0180, BusCal 0x057, GCN 188 ++IsAlive++

Mod:SMPwr:: NPT6ADPS_POWER, PN# 97152043, SN# 97152043-866139,Rev 0B
++IsAlive++

Mod:Main:: NPT6ADPS_Main, PN# 97152005, SN# 97152005-869169,Rev 02P
Configured

Mod:Dps:: NPT6ADPS_DPS, PN# 97152204, SN# 97152204-869153,Rev 07
++IsAlive++

Mod:Pmu:: NPTest_PMU, PN# 97152202, SN# 97152202-868619,Rev 01
++IsAlive++

Fpga:Fike:: DesignRev 0x03120800

Fpga:Sm:: DesignRev 0x00000010, UserCode 0xffffffff, EepUCode 0xffffffff,
ImageRev 0x00000010, Compared against Sm , BuildId N/A, ImageFile
D:\XTOS_release\XTOS_4.1.P02\Fpga\Credence_6AmpDPS\Rev1\sm_fpga.svf

Fpga:Dps:: DesignRev 0x000000f4, UserCode 0x000000f0, DevId 0x01038093

Fpga:Pmu:: DesignRev 0x00000091, DevId 0x01020093

Slot[3]:NPTest_QBIX, PN# 96152007, SN# 96152007-0613075R, Rev 66, UniqId
0165cd8d0e00006b

BusIf:: Addr/Rsrc 0x00f00/0x01c0, BusCal 0x066, GCN 188 ++IsAlive++

Mod:SMPwr:: NPtQBIX_Power, PN# 97152066, SN# 97152066-0610109BH,Rev 02
++IsAlive++

Mod:Main:: NPtQBIX_Main, PN# 97152007, SN# 97152007-0605277BH,Rev 04
++IsAlive++

Mod:APe0:: NPtQBIX_APE, PN# 97152205, SN# 97152205-874172,Rev 04 ,
QV ++IsAlive++

Mod:APe1:: NPtQBIX_APE, PN# 97152205, SN# 97152205-874149,Rev 04 ,
QV ++IsAlive++

Mod:APe2:: NPtQBIX_APE, PN# 97152205, SN# 97152205-0835042F,Rev 30 ,
QV_090929 ++IsAlive++

Mod:APe3:: NPtQBIX_APE, PN# 97152205, SN# 97152205-881440,Rev 04 ,
QV_090309 ++IsAlive++
Mod:Pmu:: NPTTest_PMU, PN# 97152202, SN# 97152202-0608206BH,Rev 01
++IsAlive++
Mod:Tmu:: NPtQBIX_TMU, PN# 97152203, SN# 97152203-0551124BH,Rev
02P ++IsAlive++
Mod:AnaClk:: NPtQBIX_AClk, PN# 97152206, SN# 97152206-885400,Rev 01
++IsAlive++
Fpga:Fike:: DesignRev 0x04220800
Fpga:Sm:: DesignRev 0x0000001f, UserCode 0x00000000, EepUCode 0x00000002,
ImageRev 0x0000001d, EepId 0x00000002, Compared against Sm, BuildId N/A, ImageFile
D:¥XTOS_release¥XTOS_4.1.P02¥fpga¥Credence_QBIX¥sm_fpga.svf
Fpga:DC/DC:: DesignRev 0x05040400
Fpga:AnaSeq0:: DesignRev 0x06080600, DevId 0x21040093
Fpga:AnaSeq1:: DesignRev 0x06080600, DevId 0x21040093
Fpga:APe0:: DesignRev 0x05010801, DevId 0x31028093
Fpga:APe1:: DesignRev 0x05010801, DevId 0x31028093
Fpga:APe2:: DesignRev 0x05010801, DevId 0x31028093
Fpga:APe3:: DesignRev 0x05010801, DevId 0x31028093
Fpga:Pmu:: DesignRev 0x00000091, DevId 0x01020093
Fpga:Tmu:: DesignRev 0x00000109, DevId 0x01020093

Slot[4]:NPTTest_D4032PS, PN# 96152003, SN# 870798, Rev 8, UniqId 01288ef50900003d
Config:: Isa MemSize: 0x3ffffff, Jua0 MemSize: 0x7ffffff, Jua1 MemSize: 0x0
BusIf:: Addr/Rsrc 0x02800/0x0240, BusCal 0x01c, GCN 188 ++IsAlive++
Mod:SMPwr:: NPtD4032_Power, PN# 97152048, SN# 869445, Rev C
++IsAlive++
Mod:Main:: NPtD4032_Main, PN# 97152003, SN# 870420, Rev 8
++IsAlive++
Mod:Pe0:: NPtD4032_PE, PN# 97152200, SN# 869905, Rev 4
++IsAlive++
Mod:Pe1:: NPtD4032_PE, PN# 97152200, SN# 869869, Rev 4
++IsAlive++
Mod:Pe2:: NPtD4032_PE, PN# 97152200, SN# 869951, Rev 4
++IsAlive++
Mod:Pe3:: NPtD4032_PE, PN# 97152200, SN# 869989, Rev 4

++IsAlive++

Mod:Pmu:: NPTest_PMU, PN# 97152202, SN# 869396, Rev 1

++IsAlive++

Mod:Tmu:: NPTest_TMU, PN# 97152203, SN# 866094, Rev 2

++IsAlive++

Fpga:Fike:: DesignRev 0x03120800, UserCode 0x03120800, DevId 0x21040093

Fpga:Sm:: DesignRev 0x00000024, UserCode 0xffffffff, EepUCode 0xffffffff,
ImageRev 0x00000024, DevId 0x80614093, EepId 0x05034093, Compared against Sm ,
BuildId N/A, ImageFile

D:\XTOS_release\XTOS_4.1.P02\Fpga\Credence_D4032PS\sm_fpga.svf

Fpga:Isabela:: DesignRev 0xf900071d, UserCode 0x0000071d, DevId 0x21040093

Fpga:Juana0:: DesignRev 0xcfc00071d, DevId 0x21040093

Fpga:Juana1:: DesignRev 0x0000200a, UserCode 0x0000200a, DevId 0x21040093

Fpga:Pe0:: DesignRev 0xcad002a4, DevId 0x01020093

Fpga:Pe1:: DesignRev 0xcad002a4, DevId 0x01020093

Fpga:Pe2:: DesignRev 0xcad002a4, DevId 0x01020093

Fpga:Pe3:: DesignRev 0xcad002a4, DevId 0x01020093

Fpga:Pmu:: DesignRev 0x00000091, DevId 0x01020093

Fpga:Tmu:: DesignRev 0x00000109, DevId 0x01020093

Slot[5]:NPTest_D4032PS, PN# 96152140, SN# 96152140-0614116R, Rev 13, UniqId
01499fbd0e0000fe

Config:: Isa MemSize: 0x3ffffff, Jua0 MemSize: 0x7ffffff, Jua1 MemSize: 0x0

BusIf:: Addr/Rsrc 0x02800/0x0280, BusCal 0x023, GCN 188 ++IsAlive++

Mod:SMPwr:: NPtD4032_Power, PN# 97152048, SN# 97152048-0526119BH,Rev 0C

++IsAlive++

Mod:Main:: NPtD4032_Main, PN# 97152003, SN# 97152003-0613486BH,Rev 11

++IsAlive++

Mod:Pe0:: NPtD4032_PE, PN# 97152200C,SN# 889395, Rev 02

++IsAlive++

Mod:Pe1:: NPtD4032_PE, PN# 97152200C,SN# 97152200C-890332,Rev 02

++IsAlive++

Mod:Pe2:: NPtD4032_PE, PN# 97152200C,SN# 890307, Rev 02

++IsAlive++

Mod:Pe3:: NPtD4032_PE, PN# 97152200C,SN# 889404, Rev 02

++IsAlive++

Mod:Pmu:: NPTest_PMU, PN# 97152202, SN# 97152202-0547275BH,Rev 01
++IsAlive++
Mod:Tmu:: NPTest_TMU, PN# 97152203, SN# 97152203-0611416BH,Rev 02
++IsAlive++
Fpga:Fike:: DesignRev 0x03120800, UserCode 0x03120800, DevId 0x21040093
Fpga:Sm:: DesignRev 0x00000024, UserCode 0xffffffff, EepUCode 0xffffffff,
ImageRev 0x00000024, DevId 0x80614093, EepId 0xf5034093, Compared against Sm ,
BuildId N/A, ImageFile
D:¥XTOS_release¥XTOS_4.1.P02¥fpga¥Credence_D4032PS¥sm_fpga.svf
Fpga:Isabela:: DesignRev 0xf900071d, UserCode 0x0000071d, DevId 0x21040093
Fpga:Juana0:: DesignRev 0xcf00071d, DevId 0x21040093
Fpga:Juana1:: DesignRev 0x0000200a, UserCode 0x0000200a, DevId 0x21040093
Fpga:Pe0:: DesignRev 0xcad002a4, DevId 0x01020093
Fpga:Pe1:: DesignRev 0xcad002a4, DevId 0x01020093
Fpga:Pe2:: DesignRev 0xcad002a4, DevId 0x01020093
Fpga:Pe3:: DesignRev 0xcad002a4, DevId 0x01020093
Fpga:Pmu:: DesignRev 0x00000091, DevId 0x01020093
Fpga:Tmu:: DesignRev 0x00000109, DevId 0x01020093

Slot[6]:NPTest_D4032PS, PN# 96152140, SN# 96152140-0614118R, Rev 13, UniqId
017e40ef0a0000e6

Config:: Isa MemSize: 0x3ffffff, Jua0 MemSize: 0x7ffffff, Jua1 MemSize: 0x0
BusIf:: Addr/Rsrc 0x02800/0x02c0, BusCal 0x024, GCN 188 ++IsAlive++
Mod:SMPwr:: NPtD4032_Power, PN# 97152048, SN# 97152048-878648,Rev 0C
++IsAlive++
Mod:Main:: NPtD4032_Main, PN# 97152003, SN# 97152003-0613504BH,Rev 11
++IsAlive++
Mod:Pe0:: NPtD4032_PE, PN# 97152200F,SN# 880259, Rev 1
++IsAlive++
Mod:Pe1:: NPtD4032_PE, PN# 97152200F,SN# 882068, Rev 1
++IsAlive++
Mod:Pe2:: NPtD4032_PE, PN# 97152200F,SN# 880634, Rev 1
++IsAlive++
Mod:Pe3:: NPtD4032_PE, PN# 97152200F,SN# 880313, Rev 1
++IsAlive++
Mod:Pmu:: NPTest_PMU, PN# 97152202, SN# 880088, Rev 1

++IsAlive++

Mod:Tmu:: NPTest_TMU, PN# 97152203, SN# 878286, Rev 2

++IsAlive++

Fpga:Fike:: DesignRev 0x03120800, UserCode 0x03120800, DevId 0x21040093

Fpga:Sm:: DesignRev 0x00000024, UserCode 0xffffffff, EepUCode 0xffffffff,
ImageRev 0x00000024, DevId 0x80614093, EepId 0x05034093, Compared against Sm ,
BuildId N/A, ImageFile

D:¥XTOS_release¥XTOS_4.1.P02¥fpga¥Credence_D4032PS¥sm_fpga.svf

Fpga:Isabela:: DesignRev 0xf900071d, UserCode 0x0000071d, DevId 0x21040093

Fpga:Juana0:: DesignRev 0xcf00071d, DevId 0x21040093

Fpga:Juana1:: DesignRev 0x0000200a, UserCode 0x0000200a, DevId 0x21040093

Fpga:Pe0:: DesignRev 0xcad002a4, DevId 0x01020093

Fpga:Pe1:: DesignRev 0xcad002a4, DevId 0x01020093

Fpga:Pe2:: DesignRev 0xcad002a4, DevId 0x01020093

Fpga:Pe3:: DesignRev 0xcad002a4, DevId 0x01020093

Fpga:Pmu:: DesignRev 0x00000091, DevId 0x01020093

Fpga:Tmu:: DesignRev 0x00000109, DevId 0x01020093

Slot[7]:NPTest_D4032PS, PN# 96152003, SN# 875917, Rev 11A, UniqId
01bf1d690a0000e7

Config:: Isa MemSize: 0x3ffffff, Jua0 MemSize: 0x7ffffff, Jua1 MemSize: 0x0

BusIf:: Addr/Rsrc 0x02800/0x0300, BusCal 0x01c, GCN 188 ++IsAlive++

Mod:SMPwr:: NPtD4032_Power, PN# 97152048, SN# 875071, Rev C

++IsAlive++

Mod:Main:: NPtD4032_Main, PN# 97152003, SN# 875071, Rev 9

++IsAlive++

Mod:Pe0:: NPtD4032_PE, PN# 97152200A,SN# 875513, Rev 1

++IsAlive++

Mod:Pe1:: NPtD4032_PE, PN# 97152200A,SN# 876558, Rev 1

++IsAlive++

Mod:Pe2:: NPtD4032_PE, PN# 97152200A,SN# 876433, Rev 1

++IsAlive++

Mod:Pe3:: NPtD4032_PE, PN# 97152200A,SN# 876439, Rev 1

++IsAlive++

Mod:Pmu:: NPTest_PMU, PN# 97152202, SN# 872095, Rev 1

++IsAlive++

Mod:Tmu:: NPTest_TMU, PN# 97152203, SN# 876297, Rev 2
++IsAlive++
Fpga:Fike:: DesignRev 0x03120800, UserCode 0x03120800, DevId 0x31040093
Fpga:Sm:: DesignRev 0x00000024, UserCode 0xffffffff, EepUCode 0xffffffff,
ImageRev 0x00000024, DevId 0x80614093, EepId 0x05034093, Compared against Sm ,
BuildId N/A, ImageFile
D:¥XTOS_release¥XTOS_4.1.P02¥fpga¥Credence_D4032PS¥sm_fpga.svf
Fpga:Isabela:: DesignRev 0xf900071d, UserCode 0x0000071d, DevId 0x31040093
Fpga:Juana0:: DesignRev 0xcf00071d, DevId 0x31040093
Fpga:Juana1:: DesignRev 0x0000200a, UserCode 0x0000200a, DevId 0x31040093
Fpga:Pe0:: DesignRev 0xcad002a4, DevId 0x01020093
Fpga:Pe1:: DesignRev 0xcad002a4, DevId 0x01020093
Fpga:Pe2:: DesignRev 0xcad002a4, DevId 0x01020093
Fpga:Pe3:: DesignRev 0xcad002a4, DevId 0x01020093
Fpga:Pmu:: DesignRev 0x00000091, DevId 0x01020093
Fpga:Tmu:: DesignRev 0x00000109, DevId 0x01020093

Slot[8]:NPTest_D4032PS, PN# 96152140, SN# 96152003-875796, Rev 18, UniqId
01958f610a0000b6

Config:: Isa MemSize: 0x3ffffff, Jua0 MemSize: 0x7ffffff, Jua1 MemSize: 0x0
BusIf:: Addr/Rsrc 0x02800/0x0340, BusCal 0x01f, GCN 188 ++IsAlive++
Mod:SMPwr:: NPtD4032_Power, PN# 97152048, SN# 97152048-863402,Rev 0D
++IsAlive++
Mod:Main:: NPtD4032_Main, PN# 97152003, SN# 97152003-873892,Rev 09
++IsAlive++
Mod:Pe0:: NPtD4032_PE, PN# 97152200, SN# 97152200-875103,Rev 05
++IsAlive++
Mod:Pe1:: NPtD4032_PE, PN# 97152200, SN# 97152200-875120,Rev 05
++IsAlive++
Mod:Pe2:: NPtD4032_PE, PN# 97152200, SN# 97152200-875153,Rev 05
++IsAlive++
Mod:Pe3:: NPtD4032_PE, PN# 97152200, SN# 97152200-875154,Rev 05
++IsAlive++
Mod:Pmu:: NPtD4032_PMU, PN# 97152202, SN# 97152202-873799,Rev 01
++IsAlive++
Mod:Tmu:: NPtD4032_TMU, PN# 97152203, SN# 97152203-0551127BH,Rev 02

++IsAlive++

Fpga:Fike:: DesignRev 0x03120800, UserCode 0x03120800, DevId 0x31040093

Fpga:Sm:: DesignRev 0x00000024, UserCode 0xffffffff, EepUCode 0xffffffff,
ImageRev 0x00000024, DevId 0x80614093, EepId 0x05034093, Compared against Sm ,
BuildId N/A, ImageFile

D:¥XTOS_release¥XTOS_4.1.P02¥fpga¥Credence_D4032PS¥sm_fpga.svf

Fpga:Isabela:: DesignRev 0xf900071d, UserCode 0x0000071d, DevId 0x31040093

Fpga:Juana0:: DesignRev 0xcf00071d, DevId 0x31040093

Fpga:Juana1:: DesignRev 0x0000200a, UserCode 0x0000200a, DevId 0x31040093

Fpga:Pe0:: DesignRev 0xcad002a4, DevId 0x01020093

Fpga:Pe1:: DesignRev 0xcad002a4, DevId 0x01020093

Fpga:Pe2:: DesignRev 0xcad002a4, DevId 0x01020093

Fpga:Pe3:: DesignRev 0xcad002a4, DevId 0x01020093

Fpga:Pmu:: DesignRev 0x00000091, DevId 0x01020093

Fpga:Tmu:: DesignRev 0x00000109, DevId 0x01020093

Slot[9]:NPTTest_D3208PSB, PN# 96152176, SN# 96152176-0530012R, Rev 07, UniqId
01adede80a000022

Config:: Isa MemSize: 0x3ffffff, Jua0 MemSize: 0x7ffffff, Jua1 MemSize: 0x7ffffff

BusIf:: Addr/Rsrc 0x04100/0x0600, BusCal 0x024, GCN 188 ++IsAlive++

Mod:SMPwr:: NPtD3208_Power, PN# 97152069, SN# 97152069-0525077P,Rev F3

++IsAlive++

Mod:Main:: NPtD3208_Main, PN# 97152116, SN# 97152116-0526043BH,Rev 04

++IsAlive++

Mod:Pe0:: NPtD3208_PBIPE, PN# 97152266, SN# 97152266-0527146BH,Rev 04

++IsAlive++

Mod:Pe1:: NPtD3208_PBIPE, PN# 97152266, SN# 97152266-0527146BH,Rev 04

++IsAlive++

Mod:Pe2:: NPtD3208_PBIPE, PN# 97152266, SN# 97152266-0526495BH,Rev 04

++IsAlive++

Mod:Pe3:: NPtD3208_PBIPE, PN# 97152266, SN# 97152266-0526495BH,Rev 04

++IsAlive++

Mod:Pmu:: NPTTest_PMU, PN# 97152202, SN# 22-0518606BH,Rev 01

++IsAlive++

Mod:Tmu:: NPtD3208_TMU, PN# 97152203, SN# 884003, Rev 02

++IsAlive++

Fpga:Fike:: DesignRev 0x03120800
Fpga:Sm:: DesignRev 0x0000003c, UserCode 0xffffffff, EepUCode 0xffffffff,
ImageRev 0x0000003c, Compared against Sm, BuildId N/A, ImageFile
D:¥XTOS_release¥XTOS_4.1.P02¥fpga¥Credence_D3208PSB¥sm_fpga.svf
Fpga:Isabela:: DesignRev 0xf980101b, UserCode 0x0080101b, DevId 0x21040093
Fpga:Juana0:: DesignRev 0xff80101b, UserCode 0x0080101b, DevId 0x21040093
Fpga:Juana1:: DesignRev 0xff80101b, UserCode 0x0080101b, DevId 0x21040093
Fpga:Pe0:: DesignRev 0xfb10001a, UserCode 0xfb100019, DevId 0x01030093
Fpga:Pe1:: DesignRev 0xfb10001a, UserCode 0xfb100019, DevId 0x01030093
Fpga:Pe2:: DesignRev 0xfb10001a, UserCode 0xfb100019, DevId 0x01030093
Fpga:Pe3:: DesignRev 0xfb10001a, UserCode 0xfb100019, DevId 0x01030093
Fpga:Pmu:: DesignRev 0x00000091, DevId 0x01020093
Fpga:Tmu:: DesignRev 0x00000109, DevId 0x01020093

Slot[11]:NPTTest_D4032PS, PN# 96152140, SN# 96152141-879824, Rev 11A, UniqId
01e656dd090000bd

Config:: Isa MemSize: 0x3ffffff, Jua0 MemSize: 0x7ffffff, Jua1 MemSize: 0x0
BusIf:: Addr/Rsrc 0x02800/0x0380, BusCal 0x01e, GCN 188 ++IsAlive++
Mod:SMPwr:: NPtD4032_Power, PN# 97152048, SN# 876175, Rev C
++IsAlive++
Mod:Main:: NPtD4032_Main, PN# 97152003, SN# 015727, Rev 9
++IsAlive++
Mod:Pe0:: NPtD4032_PE, PN# 97152200, SN# 879188, Rev 4
++IsAlive++
Mod:Pe1:: NPtD4032_PE, PN# 97152200, SN# 879185, Rev 4
++IsAlive++
Mod:Pe2:: NPtD4032_PE, PN# 97152200, SN# 879180, Rev 4
++IsAlive++
Mod:Pe3:: NPtD4032_PE, PN# 97152200, SN# 879197, Rev 4
++IsAlive++
Mod:Pmu:: NPTTest_PMU, PN# 97152202, SN# 876931, Rev 1
++IsAlive++
Mod:Tmu:: NPTTest_TMU, PN# 97152203, SN# 878265, Rev 2
++IsAlive++
Fpga:Fike:: DesignRev 0x03120800, UserCode 0x03120800, DevId 0x31040093
Fpga:Sm:: DesignRev 0x00000024, UserCode 0xffffffff, EepUCode 0xffffffff,

ImageRev 0x00000024, DevId 0x80614093, EepId 0x05034093, Compared against Sm ,
BuildId N/A, ImageFile

D:¥XTOS_release¥XTOS_4.1.P02¥fpga¥Credence_D4032PS¥sm_fpga.svf

Fpga:Isabela:: DesignRev 0xf900071d, UserCode 0x0000071d, DevId 0x31040093
Fpga:Juana0:: DesignRev 0xcf00071d, DevId 0x31040093
Fpga:Juana1:: DesignRev 0x0000200a, UserCode 0x0000200a, DevId 0x31040093
Fpga:Pe0:: DesignRev 0xcad002a4, DevId 0x01020093
Fpga:Pe1:: DesignRev 0xcad002a4, DevId 0x01020093
Fpga:Pe2:: DesignRev 0xcad002a4, DevId 0x01020093
Fpga:Pe3:: DesignRev 0xcad002a4, DevId 0x01020093
Fpga:Pmu:: DesignRev 0x00000091, DevId 0x01020093
Fpga:Tmu:: DesignRev 0x00000109, DevId 0x01020093

Slot[12]:NPTTest_D4032PS, PN# 96152140, SN# 96152140-882854, Rev 18, UniqId
018245690a0000eb

Config:: Isa MemSize: 0x3ffffff, Jua0 MemSize: 0x7ffffff, Jua1 MemSize: 0x0

BusIf:: Addr/Rsrc 0x02800/0x03c0, BusCal 0x021, GCN 188 ++IsAlive++

Mod:SMPwr:: *Unknown*, PN# , SN# , Rev *

++IsAlive++

Mod:Main:: NPtD4032_Main, PN# 97152003, SN# 97152003-886215,Rev 09

++IsAlive++

Mod:Pe0:: NPtD4032_PE, PN# 97152200F,SN# 97152200F-884134,Rev 02

++IsAlive++

Mod:Pe1:: NPtD4032_PE, PN# 97152200F,SN# 97152200F-884112,Rev 02

++IsAlive++

Mod:Pe2:: NPtD4032_PE, PN# 97152200F,SN# 97152200F-884142,Rev 02

++IsAlive++

Mod:Pe3:: NPtD4032_PE, PN# 97152200F,SN# 97152200F-884215,Rev 02

++IsAlive++

Mod:Pmu:: NPTTest_PMU, PN# 97152202, SN# 97152202-0610752BH,Rev 01

++IsAlive++

Mod:Tmu:: NPtD4032_TMU, PN# 97152203, SN# 97152203-888063,Rev 02P

++IsAlive++

Fpga:Fike:: DesignRev 0x03120800, UserCode 0x03120800, DevId 0x21040093

Fpga:Sm:: DesignRev 0x00000024, UserCode 0xffffffff, EepUCode 0xffffffff,

ImageRev 0x00000024, DevId 0x80614093, EepId 0x05034093, Compared against Sm ,

BuildId N/A, ImageFile
D:\XTOS_release\XTOS_4.1.P02\fpga\Credence_D4032PS\sm_fpga.svf
Fpga:Isabela:: DesignRev 0xf900071d, UserCode 0x0000071d, DevId 0x21040093
Fpga:Juana0:: DesignRev 0xcfc00071d, DevId 0x21040093
Fpga:Juana1:: DesignRev 0x0000200a, UserCode 0x0000200a, DevId 0x21040093
Fpga:Pe0:: DesignRev 0xcad002a4, DevId 0x01020093
Fpga:Pe1:: DesignRev 0xcad002a4, DevId 0x01020093
Fpga:Pe2:: DesignRev 0xcad002a4, DevId 0x01020093
Fpga:Pe3:: DesignRev 0xcad002a4, DevId 0x01020093
Fpga:Pmu:: DesignRev 0x00000091, DevId 0x01020093
Fpga:Tmu:: DesignRev 0x00000109, DevId 0x01020093

Slot[13]:NPTTest_D4032PS, PN# 96152140, SN# 96152140-0623012R, Rev 18, UniqId 013425bd0e0000de

Config:: Isa MemSize: 0x3ffffff, Jua0 MemSize: 0x7ffffff, Jua1 MemSize: 0x0
BusIf:: Addr/Rsrc 0x02800/0x0400, BusCal 0x029, GCN 188 ++IsAlive++
Mod:SMPwr:: NPtD4032_Power, PN# 97152048, SN# 97152048-0624066BH,Rev 0D
++IsAlive++
Mod:Main:: NPtD4032_Main, PN# 97152003, SN# 97152003-0620063BH,Rev 11
++IsAlive++
Mod:Pe0:: NPtD4032_PE, PN# 97152200F,SN# 97152200F-890512,Rev 02
++IsAlive++
Mod:Pe1:: NPtD4032_PE, PN# 97152200F,SN# 97152200F-882065,Rev 02
++IsAlive++
Mod:Pe2:: NPtD4032_PE, PN# 97152200F,SN# 97152200F-881872,Rev 02
++IsAlive++
Mod:Pe3:: NPtD4032_PE, PN# 97152200F,SN# 97152200F-882308,Rev 02
++IsAlive++
Mod:Pmu:: NPTTest_PMU, PN# 97152202, SN# 97152202-0612348BH,Rev 01
++IsAlive++
Mod:Tmu:: NPTTest_TMU, PN# 97152203, SN# 97152203-0620633BH,Rev 02
++IsAlive++
Fpga:Fike:: DesignRev 0x03120800, UserCode 0x03120800, DevId 0x21040093
Fpga:Sm:: DesignRev 0x00000024, UserCode 0xffffffff, EepUCode 0xffffffff,
ImageRev 0x00000024, DevId 0x80614093, EepId 0xf5034093, Compared against Sm ,
BuildId N/A, ImageFile

D:\XTOS_release\XTOS_4.1.P02\Fpga\Credence_D4032PS\sm_fpga.svf

Fpga:Isabela:: DesignRev 0xf900071d, UserCode 0x0000071d, DevId 0x21040093
Fpga:Juana0:: DesignRev 0xcf00071d, DevId 0x21040093
Fpga:Juana1:: DesignRev 0x0000200a, UserCode 0x0000200a, DevId 0x21040093
Fpga:Pe0:: DesignRev 0xcad002a4, DevId 0x01020093
Fpga:Pe1:: DesignRev 0xcad002a4, DevId 0x01020093
Fpga:Pe2:: DesignRev 0xcad002a4, DevId 0x01020093
Fpga:Pe3:: DesignRev 0xcad002a4, DevId 0x01020093
Fpga:Pmu:: DesignRev 0x00000091, DevId 0x01020093
Fpga:Tmu:: DesignRev 0x00000109, DevId 0x01020093

Slot[26]:NPTTest_D4032PS, PN# 96152003, SN# 875819, Rev 11, UniqId 016f7f690a0000b1

Config:: Isa MemSize: 0x3ffffff, Jua0 MemSize: 0x7ffffff, Jua1 MemSize: 0x0

BusIf:: Addr/Rsrc 0x02800/0x0440, BusCal 0x018, GCN 188 ++IsAlive++

Mod:SMPwr:: NPtD4032_Power, PN# 97152048, SN# 874610, Rev C

++IsAlive++

Mod:Main:: NPtD4032_Main, PN# 97152003, SN# 874292, Rev 8

++IsAlive++

Mod:Pe0:: NPtD4032_PE, PN# 97152200a, SN# 875564, Rev 1

++IsAlive++

Mod:Pe1:: NPtD4032_PE, PN# 97152200, SN# 875212, Rev 4

++IsAlive++

Mod:Pe2:: NPtD4032_PE, PN# 97152200, SN# 875205, Rev 4

++IsAlive++

Mod:Pe3:: NPtD4032_PE, PN# 97152200, SN# 875193, Rev 4

++IsAlive++

Mod:Pmu:: NPTTest_PMU, PN# 97152202, SN# 872842, Rev 1

++IsAlive++

Mod:Tmu:: NPTTest_TMU, PN# 97152203, SN# 872428, Rev 2

++IsAlive++

Fpga:Fike:: DesignRev 0x03120800, UserCode 0x03120800, DevId 0x21040093

Fpga:Sm:: DesignRev 0x00000024, UserCode 0xffffffff, EepUCode 0xffffffff,

ImageRev 0x00000024, DevId 0x80614093, EepId 0x05034093, Compared against Sm ,

BuildId N/A, ImageFile

D:\XTOS_release\XTOS_4.1.P02\Fpga\Credence_D4032PS\sm_fpga.svf

Fpga:Isabela:: DesignRev 0xf900071d, UserCode 0x0000071d, DevId 0x21040093

Fpga:Juana0:: DesignRev 0xcf00071d, DevId 0x21040093
Fpga:Juana1:: DesignRev 0x0000200a, UserCode 0x0000200a, DevId 0x21040093
Fpga:Pe0:: DesignRev 0xcad002a4, DevId 0x01020093
Fpga:Pe1:: DesignRev 0xcad002a4, DevId 0x01020093
Fpga:Pe2:: DesignRev 0xcad002a4, DevId 0x01020093
Fpga:Pe3:: DesignRev 0xcad002a4, DevId 0x01020093
Fpga:Pmu:: DesignRev 0x00000091, DevId 0x01020093
Fpga:Tmu:: DesignRev 0x00000109, DevId 0x01020093

Slot[27]:NPTest_D4032PS, PN# 96152140, SN# 96152141-868084, Rev 11A, UniqId
01a25b430a0000e8

Config:: Isa MemSize: 0x3ffffff, Jua0 MemSize: 0x7ffffff, Jua1 MemSize: 0x0

BusIf:: Addr/Rsrc 0x02800/0x0480, BusCal 0x01a, GCN 188 ++IsAlive++

Mod:SMPwr:: NPtD4032_Power, PN# 97152048, SN# 97152048-867320,Rev 0c
++IsAlive++

Mod:Main:: NPtD4032_Main, PN# 97152003, SN# 97152003-868627,Rev 09
++IsAlive++

Mod:Pe0:: NPtD4032_PE, PN# 97152200, SN# 867800, Rev 4
++IsAlive++

Mod:Pe1:: NPtD4032_PE, PN# 97152200, SN# 867751, Rev 4
++IsAlive++

Mod:Pe2:: NPtD4032_PE, PN# 97152200, SN# 867801, Rev 4
++IsAlive++

Mod:Pe3:: NPtD4032_PE, PN# 97152200, SN# 867730, Rev 4
++IsAlive++

Mod:Pmu:: NPTest_PMU, PN# 97152202, SN# 865899, Rev 1
++IsAlive++

Mod:Tmu:: NPTest_TMU, PN# 97152203, SN# 876611, Rev 2
++IsAlive++

Fpga:Fike:: DesignRev 0x03120800, UserCode 0x03120800, DevId 0x21040093

Fpga:Sm:: DesignRev 0x00000024, UserCode 0xffffffff, EepUCode 0xffffffff,
ImageRev 0x00000024, DevId 0x80614093, EepId 0x05034093, Compared against Sm ,
BuildId N/A, ImageFile
D:¥XTOS_release¥XTOS_4.1.P02¥fpga¥Credence_D4032PS¥sm_fpga.svf

Fpga:Isabela:: DesignRev 0xf900071d, UserCode 0x0000071d, DevId 0x21040093

Fpga:Juana0:: DesignRev 0xcf00071d, DevId 0x21040093

Fpga:Juana1:: DesignRev 0x0000200a, UserCode 0x0000200a, DevId 0x21040093
Fpga:Pe0:: DesignRev 0xcad002a4, DevId 0x01020093
Fpga:Pe1:: DesignRev 0xcad002a4, DevId 0x01020093
Fpga:Pe2:: DesignRev 0xcad002a4, DevId 0xc1020093
Fpga:Pe3:: DesignRev 0xcad002a4, DevId 0x01020093
Fpga:Pmu:: DesignRev 0x00000091, DevId 0x01020093
Fpga:Tmu:: DesignRev 0x00000109, DevId 0x01020093

Slot[28]:NPTTest_D4032PS, PN# 96152140, SN# 96152003-875856, Rev 17, UniqId
013ae1c30a0000d3

Config:: Isa MemSize: 0x3ffffff, Jua0 MemSize: 0x7ffffff, Jua1 MemSize: 0x0

BusIf:: Addr/Rsrc 0x02800/0x04c0, BusCal 0x026, GCN 188 ++IsAlive++

Mod:SMPwr:: NPtD4032_Power, PN# 97152048, SN# 97152048-874631,Rev 0D
++IsAlive++

Mod:Main:: NPtD4032_Main, PN# 97152003, SN# 886891, Rev 9

++IsAlive++

Mod:Pe0:: NPtD4032_PE, PN# 97152200A,SN# 875568, Rev 1

++IsAlive++

Mod:Pe1:: NPtD4032_PE, PN# 97152200A,SN# 875435, Rev 1

++IsAlive++

Mod:Pe2:: NPtD4032_PE, PN# 97152200A,SN# 875534, Rev 1

++IsAlive++

Mod:Pe3:: NPtD4032_PE, PN# 97152200A,SN# 875551, Rev 1

++IsAlive++

Mod:Pmu:: NPTTest_PMU, PN# 97152202, SN# 874750, Rev 1

++IsAlive++

Mod:Tmu:: NPTTest_TMU, PN# 97152203, SN# 876290, Rev 2

++IsAlive++

Fpga:Fike:: DesignRev 0x03120800, UserCode 0x03120800, DevId 0x21040093

Fpga:Sm:: DesignRev 0x00000024, UserCode 0xffffffff, EepUCode 0xffffffff,

ImageRev 0x00000024, DevId 0x80614093, EepId 0x05034093, Compared against Sm ,
BuildId N/A, ImageFile

D:\XTOS_release\XTOS_4.1.P02\Fpga\Credence_D4032PS\sm_fpga.svf

Fpga:Isabela:: DesignRev 0xf900071d, UserCode 0x0000071d, DevId 0x21040093

Fpga:Juana0:: DesignRev 0xcfc00071d, DevId 0x21040093

Fpga:Juana1:: DesignRev 0x0000200a, UserCode 0x0000200a, DevId 0x21040093

Fpga:Pe0:: DesignRev 0xcad002a4, DevId 0x01020093
Fpga:Pe1:: DesignRev 0xcad002a4, DevId 0x01020093
Fpga:Pe2:: DesignRev 0xcad002a4, DevId 0x01020093
Fpga:Pe3:: DesignRev 0xcad002a4, DevId 0x01020093
Fpga:Pmu:: DesignRev 0x00000091, DevId 0x01020093
Fpga:Tmu:: DesignRev 0x00000109, DevId 0x01020093

Slot[31]:NPTTest_D4032PS, PN# 96152003, SN# 870794, Rev 8, UniqId 011262430a000098

Config:: Isa MemSize: 0x3ffffff, Jua0 MemSize: 0x7ffffff, Jua1 MemSize: 0x0

BusIf:: Addr/Rsrc 0x02800/0x0500, BusCal 0x01a, GCN 188 ++IsAlive++

Mod:SMPwr:: NPtD4032_Power, PN# 97152048, SN# 870922, Rev c

++IsAlive++

Mod:Main:: NPtD4032_Main, PN# 97152003, SN# 870331, Rev 8

++IsAlive++

Mod:Pe0:: NPtD4032_PE, PN# 97152200, SN# 872631, Rev 4

++IsAlive++

Mod:Pe1:: NPtD4032_PE, PN# 97152200, SN# 872657, Rev 4

++IsAlive++

Mod:Pe2:: NPtD4032_PE, PN# 97152200, SN# 872671, Rev 4

++IsAlive++

Mod:Pe3:: NPtD4032_PE, PN# 97152200, SN# 872672, Rev 4

++IsAlive++

Mod:Pmu:: NPTTest_PMU, PN# 97152202, SN# 871066, Rev 1

++IsAlive++

Mod:Tmu:: NPTTest_TMU, PN# 97152203, SN# 871299, Rev 2

++IsAlive++

Fpga:Fike:: DesignRev 0x03120800, UserCode 0x03120800, DevId 0x31040093

Fpga:Sm:: DesignRev 0x00000024, UserCode 0xffffffff, EepUCode 0xffffffff,

ImageRev 0x00000024, DevId 0x80614093, EepId 0x05034093, Compared against Sm ,

BuildId N/A, ImageFile

D:¥XTOS_release¥XTOS_4.1.P02¥fpga¥Credence_D4032PS¥sm_fpga.svf

Fpga:Isabela:: DesignRev 0xf900071d, UserCode 0x0000071d, DevId 0x31040093

Fpga:Juana0:: DesignRev 0xcf00071d, DevId 0x31040093

Fpga:Juana1:: DesignRev 0x0000200a, UserCode 0x0000200a, DevId 0x31040093

Fpga:Pe0:: DesignRev 0xcad002a4, DevId 0x01020093

Fpga:Pe1:: DesignRev 0xcad002a4, DevId 0x01020093

Fpga:Pe2:: DesignRev 0xcad002a4, DevId 0x01020093
Fpga:Pe3:: DesignRev 0xcad002a4, DevId 0x01020093
Fpga:Pmu:: DesignRev 0x00000091, DevId 0x01020093
Fpga:Tmu:: DesignRev 0x00000109, DevId 0x01020093

Slot[32]:NPTest_D4032PS, PN# 96152140, SN# 96152140-882835, Rev 12A, UniqId
010035690a000002

Config:: Isa MemSize: 0x3ffffff, Jua0 MemSize: 0x7ffffff, Jua1 MemSize: 0x0

BusIf:: Addr/Rsrc 0x02800/0x0540, BusCal 0x01b, GCN 188 ++IsAlive++

Mod:SMPwr:: NPtD4032_Power, PN# 96152048, SN# 883783, Rev C
++IsAlive++

Mod:Main:: NPtD4032_Main, PN# 97152003, SN# 886005, Rev 11
++IsAlive++

Mod:Pe0:: NPtD4032_PE, PN# 97152200F,SN# 882063, Rev 2
++IsAlive++

Mod:Pe1:: *Unknown*, PN# , SN# , Rev *
++IsAlive++

Mod:Pe2:: NPtD4032_PE, PN# 97152200F,SN# 882342, Rev 2
++IsAlive++

Mod:Pe3:: NPtD4032_PE, PN# 97152200F,SN# 882300, Rev 2
++IsAlive++

Mod:Pmu:: NPTest_PMU, PN# 97152202, SN# 885292, Rev 1
++IsAlive++

Mod:Tmu:: NPTest_TMU, PN# 97152203, SN# 886099, Rev 2
++IsAlive++

Fpga:Fike:: DesignRev 0x03120800, UserCode 0x03120800, DevId 0x21040093

Fpga:Sm:: DesignRev 0x00000024, UserCode 0xffffffff, EepUCode 0xffffffff,
ImageRev 0x00000024, DevId 0x80614093, EepId 0x05034093, Compared against Sm ,
BuildId N/A, ImageFile

D:¥XTOS_release¥XTOS_4.1.P02¥fpga¥Credence_D4032PS¥sm_fpga.svf

Fpga:Isabela:: DesignRev 0xf900071d, UserCode 0x0000071d, DevId 0x21040093

Fpga:Juana0:: DesignRev 0xcf00071d, DevId 0x21040093

Fpga:Juana1:: DesignRev 0x0000200a, UserCode 0x0000200a, DevId 0x21040093

Fpga:Pe0:: DesignRev 0xcad002a4, DevId 0x01020093

Fpga:Pe1:: DesignRev 0xcad002a4, DevId 0x01020093

Fpga:Pe2:: DesignRev 0xcad002a4, DevId 0x01020093

Fpga:Pe3:: DesignRev 0xcad002a4, DevId 0x01020093
Fpga:Pmu:: DesignRev 0x00000091, DevId 0x01020093
Fpga:Tmu:: DesignRev 0x00000109, DevId 0x01020093

Slot[33]:NPTest_D4032PS, PN# 96152140, SN# 96152140-0624006R, Rev 18, UniqId
01cf1dbd0e0000ee

Config:: Isa MemSize: 0x3ffffff, Jua0 MemSize: 0x7ffffff, Jua1 MemSize: 0x0

BusIf:: Addr/Rsrc 0x02800/0x0580, BusCal 0x02a, GCN 188 ++IsAlive++

Mod:SMPwr:: NPtD4032_Power, PN# 97152048, SN# 97152048-0622123BH,Rev 0D
++IsAlive++

Mod:Main:: NPtD4032_Main, PN# 97152003, SN# 97152003-0620019BH,Rev 11
++IsAlive++

Mod:Pe0:: NPtD4032_PE, PN# 97152200, SN# 97152200-012908,Rev 05
++IsAlive++

Mod:Pe1:: NPtD4032_PE, PN# 97152200, SN# 97152200-869714,Rev 05
++IsAlive++

Mod:Pe2:: NPtD4032_PE, PN# 97152200, SN# 97152200-864104,Rev 05
++IsAlive++

Mod:Pe3:: NPtD4032_PE, PN# 97152200, SN# 97152200-866734,Rev 05
++IsAlive++

Mod:Pmu:: NPTest_PMU, PN# 97152202, SN# 97152202-0610736BH,Rev 01
++IsAlive++

Mod:Tmu:: NPTest_TMU, PN# 97152203, SN# 97152203-0620638BH,Rev 02
++IsAlive++

Fpga:Fike:: DesignRev 0x03120800, UserCode 0x03120800, DevId 0x21040093

Fpga:Sm:: DesignRev 0x00000024, UserCode 0xffffffff, EepUCode 0xffffffff,
ImageRev 0x00000024, DevId 0x80614093, EepId 0x05034093, Compared against Sm ,
BuildId N/A, ImageFile

D:¥XTOS_release¥XTOS_4.1.P02¥fpga¥Credence_D4032PS¥sm_fpga.svf

Fpga:Isabela:: DesignRev 0xf900071d, UserCode 0x0000071d, DevId 0x21040093

Fpga:Juana0:: DesignRev 0xcf00071d, DevId 0x21040093

Fpga:Juana1:: DesignRev 0x0000200a, UserCode 0x0000200a, DevId 0x21040093

Fpga:Pe0:: DesignRev 0xcad002a4, DevId 0x01020093

Fpga:Pe1:: DesignRev 0xcad002a4, DevId 0x01020093

Fpga:Pe2:: DesignRev 0xcad002a4, DevId 0x01020093

Fpga:Pe3:: DesignRev 0xcad002a4, DevId 0x01020093

Fpga:Pmu:: DesignRev 0x00000091, DevId 0x01020093
Fpga:Tmu:: DesignRev 0x00000109, DevId 0x01020093

Slot[34]:NPTTest_D4032PS, PN# 96152140, SN# 96152140-870814, Rev 18, UniqId
014f92610a00002d

Config:: Isa MemSize: 0x3ffffff, Jua0 MemSize: 0x7ffffff, Jua1 MemSize: 0x0

BusIf:: Addr/Rsrc 0x02800/0x05c0, BusCal 0x05f, GCN 188 ++IsAlive++

Mod:SMPwr:: NPtD4032_Power, PN# 97152048, SN# 97152048-876177,Rev 0D
++IsAlive++

Mod:Main:: NPtD4032_Main, PN# 97152003, SN# 97152003-886875,Rev 09
++IsAlive++

Mod:Pe0:: NPtD4032_PE, PN# 97152200, SN# 97152200-879173,Rev 05
++IsAlive++

Mod:Pe1:: NPtD4032_PE, PN# 97152200, SN# 97152200-877227,Rev 05
++IsAlive++

Mod:Pe2:: NPtD4032_PE, PN# 97152200, SN# 97152200-878539,Rev 05
++IsAlive++

Mod:Pe3:: NPtD4032_PE, PN# 97152200, SN# 97152200-877282,Rev 05
++IsAlive++

Mod:Pmu:: NPTTest_PMU, PN# 97152202, SN# 97152202-876935,Rev 01
++IsAlive++

Mod:Tmu:: NPTTest_TMU, PN# 97152203, SN# 97152203-877892,Rev 02
++IsAlive++

Fpga:Fike:: DesignRev 0x03120800, UserCode 0x03120800, DevId 0x31040093

Fpga:Sm:: DesignRev 0x00000024, UserCode 0xffffffff, EepUCode 0xffffffff,
ImageRev 0x00000024, DevId 0x80614093, EepId 0x05034093, Compared against Sm ,
BuildId N/A, ImageFile

D:¥XTOS_release¥XTOS_4.1.P02¥fpga¥Credence_D4032PS¥sm_fpga.svf

Fpga:Isabela:: DesignRev 0xf900071d, UserCode 0x0000071d, DevId 0x31040093

Fpga:Juana0:: DesignRev 0xcf00071d, DevId 0x31040093

Fpga:Juana1:: DesignRev 0x0000200a, UserCode 0x0000200a, DevId 0x31040093

Fpga:Pe0:: DesignRev 0xcad002a4, DevId 0x01020093

Fpga:Pe1:: DesignRev 0xcad002a4, DevId 0x01020093

Fpga:Pe2:: DesignRev 0xcad002a4, DevId 0x01020093

Fpga:Pe3:: DesignRev 0xcad002a4, DevId 0x01020093

Fpga:Pmu:: DesignRev 0x00000091, DevId 0x01020093

Fpga:Tmu:: DesignRev 0x00000109, DevId 0x01020093

Slot[36]:NPTest_QBIX, PN# 96152007, SN# 96152007-879858, Rev 66, UniqId 01b579610a00003a

BusIf:: Addr/Rsrc 0x00f00/0x0200, BusCal 0x040, GCN 188 ++IsAlive++

Mod:SMPwr:: NPtQBIX_Power, PN# 97152066, SN# 97152066-0819014F,Rev 16
++IsAlive++

Mod:Main:: NPtQBIX_Main, PN# 97152007, SN# 97152007-878170,Rev 04
++IsAlive++

Mod:APe0:: NPtQBIX_APE, PN# 97152205, SN# 97152205-0612220BH,Rev 04 , QV ++IsAlive++

Mod:APe1:: NPtQBIX_APE, PN# 97152205, SN# 97152205-877981,Rev 04 , QV_090929 ++IsAlive++

Mod:APe2:: NPtQBIX_APE, PN# 97152205, SN# 97152205-0835011F,Rev 30 , QV_090622 10p ++IsAlive++

Mod:APe3:: NPtQBIX_APE, PN# 97152205, SN# 97152205-878464,Rev 04 , QV_091013 ++IsAlive++

Mod:Pmu:: NPTest_PMU, PN# 97152202, SN# 97152202-0804094BA,Rev 10
++IsAlive++

Mod:Tmu:: NPTest_TMU, PN# 97152203, SN# 97152203-0804039BA,Rev 02
++IsAlive++

Mod:AnaClk:: NPtQBIX_AClk, PN# 97152206, SN# 97152206-0639152BH,Rev 01
++IsAlive++

Fpga:Fike:: DesignRev 0x04220800

Fpga:Sm:: DesignRev 0x0000001f, UserCode 0x00000000, EepUCode 0x00000002, ImageRev 0x0000001d, EepId 0x00000002, Compared against Sm, BuildId N/A, ImageFile D:¥XTOS_release¥XTOS_4.1.P02¥fpga¥Credence_QBIX¥sm_fpga.svf

Fpga:DC/DC:: DesignRev 0x03170600

Fpga:AnaSeq0:: DesignRev 0x06080600, DevId 0x31040093

Fpga:AnaSeq1:: DesignRev 0x06080600, DevId 0x31040093

Fpga:APe0:: DesignRev 0x05010801, DevId 0x31028093

Fpga:APe1:: DesignRev 0x05010801, DevId 0x31028093

Fpga:APe2:: DesignRev 0x05010801, DevId 0x31028093

Fpga:APe3:: DesignRev 0x05010801, DevId 0x31028093

Fpga:Pmu:: DesignRev 0x00000091, DevId 0x01020093

Fpga:Tmu:: DesignRev 0x00000109, DevId 0x01020093

LoadBoard(Generic):Default_LoadBoard, PN# , SN# , Rev *

TesterBus Config: RealMode, Orion, NumInstruments=22, NumTypes=7,
NumRingInstruments=20, NumRingTypes=5

RingClockDelay=0, MasterClockFreq=400Mhz

SystemRsrcs=0, 2-9, 11-13, 26-28, 31-34, 36, 254-355, 360-365, 368, 372, 384-392, 448-454, 456-462, 464-470, 472-478, 480, 488-491, 512-518, 520-526, 528-534, 536-542, 544, 552-555, 576-608, 616-620, 640-672, 680-684, 704-736, 744-748, 768-800, 808-812, 832-864, 872-876, 896-928, 936-940, 960-992, 1000-1004, 1024-1056, 1064-1068, 1088-1120, 1128-1132, 1152-1184, 1192-1196, 1216-1248, 1256-1260, 1280-1312, 1320-1324, 1344-1376, 1384-1388, 1408-1440, 1448-1452, 1472-1504, 1512-1516, 1536-1568, 1576-1580

LoadedRsrcs=0, 2-9, 11-13, 26-28, 31-34, 36, 254-355, 360-365, 368, 372, 384-392, 448-454, 456-462, 464-470, 472-478, 480, 488-491, 512-518, 520-526, 528-534, 536-542, 544, 552-555, 576-608, 616-620, 640-672, 680-684, 704-736, 744-748, 768-800, 808-812, 832-864, 872-876, 896-928, 936-940, 960-992, 1000-1004, 1024-1056, 1064-1068, 1088-1120, 1128-1132, 1152-1184, 1192-1196, 1216-1248, 1256-1260, 1280-1312, 1320-1324, 1344-1376, 1384-1388, 1408-1440, 1448-1452, 1472-1504, 1512-1516, 1536-1568, 1576-1580

ProcessingMode= ThreadDisabled, totalSites=0, loadedSites[0]=0

// NumThreadsActive: Hw=1, Os=2, threadSwapCount=0, freeInfoCount=0,
freeHwThreads=0, Tracing not activated

PCI64/66:Credence_CpuIface, PN# , SN# , Rev 1

CompanyName: Credence Systems Corp.

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InternalName: Sapphire_cpuif.sys

FileVersion: 2.44.0.0

ProductName: XTOS

ProductVersion: 4.1

NPTestConfigSpec: element * CHECKEDOUTelement * .../xtos_4.1.x/LATEST
element * .../xtos_main/LATEST -time 11-Oct-2008.19:00UTC -mkbranch
xtos_4.1.x element * .../win_sj/LATEST -time 08-Aug-2006.09:00UTC -mkbranch
xtos_4.1.x element * MAIN_WIN_STATIC -mkbranch xtos_4.1.xelement *
/main/LATEST -mkbranch xtos_4.1.x #Snapshot View Load Rule load

¥sapphire

NPTestBuildID: SJ090126.190913

++IsAlive++

CanBus:: ++IsAlive++

Fpga:CpuIf:: DesignRev 231 (0x070100e7), UserCode 0xffffffff, EepUCode 0xffffffff,
ImageRev 0x000000e7, DevId 0x01038093, EepId[0,1] 0x05036093, Compared against
Cpuif, BuildId N/A, ImageFile
D:¥XTOS_release¥XTOS_4.1.P02¥fpga¥Credence_CpuIface¥cpuif_fpga.svf
//Sun Apr 03 05:19:49 2022

TestConfig:: Completed successfully.