



Trust Technology Corporation

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TTC ID	
Maker	TERADYNE
Model	J750
Description	Tester
Vintage	
QTY	1

[NOTES]

There are more tools listed on the following URL.
We hope you will also view it.
(Used Tools Information)

<http://www.trust-t.com/ttcc/en/equipment/>

CurrentConfig.

#slot[.subslot]	Type	idprom (type serial rev company)
-1	sli	239-624-00 2031ae 9846-A 5445
0	channel	239-026-31 c0035de 0737-E 5445
1	channel	239-026-31 c00533c 0737-5 5445
2	channel	239-026-05 c1287a1 1042-5 5445
3	channel	239-026-05 c00a4bd 0746-5 5445
4	channel	239-026-05 c00d321 0746-5 5445
5	channel	239-026-05 c003c8e 0746-5 5445
6	channel	239-026-05 c0dc273 0923-5 5445
7	channel	239-026-03 5000f42 0951-B 5445
17	cto	239-029-02 c320e91 1332-D 5445
18	cub	239-020-06 1fc1ca 0621-D 5445
21	dps	239-016-06 c0681e7 0702-F 5445
22	dps	239-016-06 c06862a 0702-F 5445
23	dps	239-016-06 c0681e9 0702-F 5445
24	dps	239-016-06 c068630 0702-F 5445

Quick_Module_Calibration_Performance_PASS

%JOB_START - Beginning PCIT Quick Check test on slot 0 at 4:51:20 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- PCIT CARD INFORMATION:

Part Number: 939-360-00
Serial Number: 0
Revision Date: A0422

%JOB_END - ****PASSED**** PCIT Quick Check of slot 0 at 4:51:21 PM

%JOB_START - Beginning CUB Quick Check test on slot 18 at 4:51:31 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Starting DIB Power Tests
- Completed DIB Power Tests
- Starting System Fan Checks
- Completed System Fan Checks
- Starting PG_History_Ram
- Completed PG_History_Ram
- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Skipping PG_LVM_BIST_Ram
- Beginning Qck_Register Test
- Completed Qck_Register Test
- Starting CalCub_TG_Register Tests
- LRS Off
- LRS On
- Completed CalCub_TG_Register Tests
- Started IdProm Test
- Completed IdProm Test

- Beginning Force Voltage Test
- Completed Force Voltage Test
- Beginning TestRefToDac
- Completed TestRefToDac
- Starting the CalCubSLITest
- Completed the CalCubSLITest

%JOB_END - ****PASSED**** CUB Quick Check of slot 18 (1FC1CA) at 4:51:36 PM

%JOB_START - Beginning Channel_Board Quick Check test on slot 0 at 4:51:42 PM on
2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting PG_History_Ram
- Completed PG_History_Ram
- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Starting PG_LVM_BIST_Ram (up to 32 sec)
- Completed PG_LVM_BIST_Ram in 21.6 sec
- LRS Off
- Starting TG Register Tests
- Completed TG Register Tests
- LRS On
- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG_Period_Ram
- Completed TG_Period_Ram
- Starting TG_Period_Map_Ram
- Completed TG_Period_Map_Ram

- Starting TG_LVM_BIST_Ram (up to 32 sec)
- Completed TG_LVM_BIST_Ram in 10.8 sec
- Starting TG_History_Ram
- Completed TG_History_Ram
- Starting TG_SVM_Ram
- Completed TG_SVM_Ram
- Starting TG_ADSS_Ram
- Completed TG_ADSS_Ram
- Starting TG_KeepAlive_Ram
- Completed TG_KeepAlive_Ram
- Starting TG_Tset_LkDwn_Ram
- Completed TG_Tset_LkDwn_Ram
- Starting TG_Edge_Ram
- Completed TG_Edge_Ram
- Starting TG_Format_Ram
- Completed TG_Format_Ram
- Starting TG_FormatLkDwn_Ram
- Completed TG_FormatLkDwn_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB_END - ****PASSED**** Channel_Board Quick Check of slot 0 (C0035DE) at
4:52:31 PM

%JOB_START - Beginning Channel_Board Quick Check test on slot 1 at 4:52:37 PM on
2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting PG_History_Ram
- Completed PG_History_Ram

- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Starting PG_LVM_BIST_Ram (up to 32 sec)
- Completed PG_LVM_BIST_Ram in 21.6 sec
- LRS Off
- Starting TG Register Tests
- Completed TG Register Tests
- LRS On
- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG_Period_Ram
- Completed TG_Period_Ram
- Starting TG_Period_Map_Ram
- Completed TG_Period_Map_Ram
- Starting TG_LVM_BIST_Ram (up to 32 sec)
- Completed TG_LVM_BIST_Ram in 10.8 sec
- Starting TG_History_Ram
- Completed TG_History_Ram
- Starting TG_SVM_Ram
- Completed TG_SVM_Ram
- Starting TG_ADSS_Ram
- Completed TG_ADSS_Ram
- Starting TG_KeepAlive_Ram
- Completed TG_KeepAlive_Ram
- Starting TG_Tset_LkDwn_Ram
- Completed TG_Tset_LkDwn_Ram
- Starting TG_Edge_Ram
- Completed TG_Edge_Ram
- Starting TG_Format_Ram
- Completed TG_Format_Ram
- Starting TG_FormatLkDwn_Ram
- Completed TG_FormatLkDwn_Ram

- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB_END - ****PASSED**** Channel_Board Quick Check of slot 1 (C00533C) at
4:53:26 PM

%JOB_START - Beginning Channel_Board Quick Check test on slot 2 at 4:53:32 PM on
2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting PG_History_Ram
- Completed PG_History_Ram
- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Starting PG_LVM_BIST_Ram (up to 32 sec)
- Completed PG_LVM_BIST_Ram in 21.6 sec
- LRS Off
- Starting TG Register Tests
- Completed TG Register Tests
- LRS On
- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG_Period_Ram
- Completed TG_Period_Ram

- Starting TG_Period_Map_Ram
- Completed TG_Period_Map_Ram
- Starting TG_LVM_BIST_Ram (up to 32 sec)
- Completed TG_LVM_BIST_Ram in 10.8 sec
- Starting TG_History_Ram
- Completed TG_History_Ram
- Starting TG_SVM_Ram
- Completed TG_SVM_Ram
- Starting TG_ADSS_Ram
- Completed TG_ADSS_Ram
- Starting TG_KeepAlive_Ram
- Completed TG_KeepAlive_Ram
- Starting TG_Tset_LkDwn_Ram
- Completed TG_Tset_LkDwn_Ram
- Starting TG_Edge_Ram
- Completed TG_Edge_Ram
- Starting TG_Format_Ram
- Completed TG_Format_Ram
- Starting TG_FormatLkDwn_Ram
- Completed TG_FormatLkDwn_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB_END - ****PASSED**** Channel_Board Quick Check of slot 2 (C1287A1) at
4:54:22 PM

%JOB_START - Beginning Channel_Board Quick Check test on slot 3 at 4:54:27 PM on
2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Started IdProm Test
- Completed IdProm Test

- Starting PG_History_Ram
- Completed PG_History_Ram
- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Starting PG_LVM_BIST_Ram (up to 32 sec)
- Completed PG_LVM_BIST_Ram in 21.6 sec
- LRS Off
- Starting TG Register Tests
- Completed TG Register Tests
- LRS On
- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG_Period_Ram
- Completed TG_Period_Ram
- Starting TG_Period_Map_Ram
- Completed TG_Period_Map_Ram
- Starting TG_LVM_BIST_Ram (up to 32 sec)
- Completed TG_LVM_BIST_Ram in 10.8 sec
- Starting TG_History_Ram
- Completed TG_History_Ram
- Starting TG_SVM_Ram
- Completed TG_SVM_Ram
- Starting TG_ADSS_Ram
- Completed TG_ADSS_Ram
- Starting TG_KeepAlive_Ram
- Completed TG_KeepAlive_Ram
- Starting TG_Tset_LkDwn_Ram
- Completed TG_Tset_LkDwn_Ram
- Starting TG_Edge_Ram
- Completed TG_Edge_Ram
- Starting TG_Format_Ram
- Completed TG_Format_Ram

- Starting TG_FormatLkDwn_Ram
- Completed TG_FormatLkDwn_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB_END - ****PASSED**** Channel_Board Quick Check of slot 3 (C00A4BD) at
4:55:17 PM

%JOB_START - Beginning Channel_Board Quick Check test on slot 4 at 4:55:22 PM on
2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting PG_History_Ram
- Completed PG_History_Ram
- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Starting PG_LVM_BIST_Ram (up to 32 sec)
- Completed PG_LVM_BIST_Ram in 21.6 sec
- LRS Off
- Starting TG Register Tests
- Completed TG Register Tests
- LRS On
- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register

- Starting TG_Period_Ram
- Completed TG_Period_Ram
- Starting TG_Period_Map_Ram
- Completed TG_Period_Map_Ram
- Starting TG_LVM_BIST_Ram (up to 32 sec)
- Completed TG_LVM_BIST_Ram in 10.8 sec
- Starting TG_History_Ram
- Completed TG_History_Ram
- Starting TG_SVM_Ram
- Completed TG_SVM_Ram
- Starting TG_ADSS_Ram
- Completed TG_ADSS_Ram
- Starting TG_KeepAlive_Ram
- Completed TG_KeepAlive_Ram
- Starting TG_Tset_LkDwn_Ram
- Completed TG_Tset_LkDwn_Ram
- Starting TG_Edge_Ram
- Completed TG_Edge_Ram
- Starting TG_Format_Ram
- Completed TG_Format_Ram
- Starting TG_FormatLkDwn_Ram
- Completed TG_FormatLkDwn_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB_END - ****PASSED**** Channel_Board Quick Check of slot 4 (C00D321) at
4:56:12 PM

%JOB_START - Beginning Channel_Board Quick Check test on slot 5 at 4:56:17 PM on
2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting PG_History_Ram
- Completed PG_History_Ram
- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Starting PG_LVM_BIST_Ram (up to 32 sec)
- Completed PG_LVM_BIST_Ram in 21.6 sec
- LRS Off
- Starting TG Register Tests
- Completed TG Register Tests
- LRS On
- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG_Period_Ram
- Completed TG_Period_Ram
- Starting TG_Period_Map_Ram
- Completed TG_Period_Map_Ram
- Starting TG_LVM_BIST_Ram (up to 32 sec)
- Completed TG_LVM_BIST_Ram in 10.8 sec
- Starting TG_History_Ram
- Completed TG_History_Ram
- Starting TG_SVM_Ram
- Completed TG_SVM_Ram
- Starting TG_ADSS_Ram
- Completed TG_ADSS_Ram
- Starting TG_KeepAlive_Ram
- Completed TG_KeepAlive_Ram
- Starting TG_Tset_LkDwn_Ram
- Completed TG_Tset_LkDwn_Ram
- Starting TG_Edge_Ram
- Completed TG_Edge_Ram

- Starting TG_Format_Ram
- Completed TG_Format_Ram
- Starting TG_FormatLkDwn_Ram
- Completed TG_FormatLkDwn_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB_END - ****PASSED**** Channel_Board Quick Check of slot 5 (C003C8E) at
4:57:07 PM

%JOB_START - Beginning Channel_Board Quick Check test on slot 6 at 4:57:12 PM on
2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting PG_History_Ram
- Completed PG_History_Ram
- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Starting PG_LVM_BIST_Ram (up to 32 sec)
- Completed PG_LVM_BIST_Ram in 21.6 sec
- LRS Off
- Starting TG Register Tests
- Completed TG Register Tests
- LRS On
- Starting TG Register Tests
- Completed TG Register Tests

- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG_Period_Ram
- Completed TG_Period_Ram
- Starting TG_Period_Map_Ram
- Completed TG_Period_Map_Ram
- Starting TG_LVM_BIST_Ram (up to 32 sec)
- Completed TG_LVM_BIST_Ram in 10.8 sec
- Starting TG_History_Ram
- Completed TG_History_Ram
- Starting TG_SVM_Ram
- Completed TG_SVM_Ram
- Starting TG_ADSS_Ram
- Completed TG_ADSS_Ram
- Starting TG_KeepAlive_Ram
- Completed TG_KeepAlive_Ram
- Starting TG_Tset_LkDwn_Ram
- Completed TG_Tset_LkDwn_Ram
- Starting TG_Edge_Ram
- Completed TG_Edge_Ram
- Starting TG_Format_Ram
- Completed TG_Format_Ram
- Starting TG_FormatLkDwn_Ram
- Completed TG_FormatLkDwn_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB_END - ****PASSED**** Channel_Board Quick Check of slot 6 (C0DC273) at
4:58:02 PM

%JOB_START - Beginning Channel_Board Quick Check test on slot 7 at 4:58:08 PM on
2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #

23BA64 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting PG_History_Ram
- Completed PG_History_Ram
- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Starting PG_LVM_BIST_Ram (up to 32 sec)
- Completed PG_LVM_BIST_Ram in 21.6 sec
- LRS Off
- Starting TG Register Tests
- Completed TG Register Tests
- LRS On
- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG_Period_Ram
- Completed TG_Period_Ram
- Starting TG_Period_Map_Ram
- Completed TG_Period_Map_Ram
- Starting TG_LVM_BIST_Ram (up to 32 sec)
- Completed TG_LVM_BIST_Ram in 10.8 sec
- Starting TG_History_Ram
- Completed TG_History_Ram
- Starting TG_SVM_Ram
- Completed TG_SVM_Ram
- Starting TG_ADSS_Ram
- Completed TG_ADSS_Ram
- Starting TG_KeepAlive_Ram
- Completed TG_KeepAlive_Ram
- Starting TG_Tset_LkDwn_Ram
- Completed TG_Tset_LkDwn_Ram

- Starting TG_Edge_Ram
- Completed TG_Edge_Ram
- Starting TG_Format_Ram
- Completed TG_Format_Ram
- Starting TG_FormatLkDwn_Ram
- Completed TG_FormatLkDwn_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB_END - ****PASSED**** Channel_Board Quick Check of slot 7 (5000F42) at
4:58:57 PM

%JOB_START - Beginning Relay_Board_Lower Quick Check test on slot 0 at 4:59:03 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 0 in Quick Mode (Cal Relay DIB Not Required)

%JOB_END - ****PASSED**** Relay_Board_Lower Quick Check of slot 0 at 4:59:03 PM

%JOB_START - Beginning Relay_Board_Lower Quick Check test on slot 1 at 4:59:09 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 1 in Quick Mode (Cal Relay DIB Not Required)

%JOB_END - ****PASSED**** Relay_Board_Lower Quick Check of slot 1 at 4:59:09 PM

%JOB_START - Beginning Relay_Board_Lower Quick Check test on slot 2 at 4:59:15 PM

on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 2 in Quick Mode (Cal Relay DIB Not
Required)

%JOB_END - ****PASSED**** Relay_Board_Lower Quick Check of slot 2 at 4:59:15 PM

%JOB_START - Beginning Relay_Board_Lower Quick Check test on slot 3 at 4:59:21 PM
on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 3 in Quick Mode (Cal Relay DIB Not
Required)

%JOB_END - ****PASSED**** Relay_Board_Lower Quick Check of slot 3 at 4:59:21 PM

%JOB_START - Beginning Relay_Board_Lower Quick Check test on slot 4 at 4:59:27 PM
on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 4 in Quick Mode (Cal Relay DIB Not
Required)

%JOB_END - ****PASSED**** Relay_Board_Lower Quick Check of slot 4 at 4:59:27 PM

%JOB_START - Beginning Relay_Board_Lower Quick Check test on slot 5 at 4:59:33 PM
on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 5 in Quick Mode (Cal Relay DIB Not
Required)

%JOB_END - ****PASSED**** Relay_Board_Lower Quick Check of slot 5 at 4:59:34 PM

%JOB_START - Beginning Relay_Board_Lower Quick Check test on slot 6 at 4:59:39 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 6 in Quick Mode (Cal Relay DIB Not
Required)

%JOB_END - ****PASSED**** Relay_Board_Lower Quick Check of slot 6 at 4:59:40 PM

%JOB_START - Beginning Relay_Board_Lower Quick Check test on slot 7 at 4:59:45 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 7 in Quick Mode (Cal Relay DIB Not
Required)

%JOB_END - ****PASSED**** Relay_Board_Lower Quick Check of slot 7 at 4:59:46 PM

%JOB_START - Beginning Relay_Board_Upper Quick Check test on slot 0 at 4:59:51 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 0 in Quick Mode (Cal Relay DIB Not
Required)

%JOB_END - ****PASSED**** Relay_Board_Upper Quick Check of slot 0 at 4:59:52 PM

%JOB_START - Beginning Relay_Board_Upper Quick Check test on slot 1 at 4:59:57 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 1 in Quick Mode (Cal Relay DIB Not
Required)

%JOB_END - ****PASSED**** Relay_Board_Upper Quick Check of slot 1 at 4:59:58 PM

%JOB_START - Beginning Relay_Board_Upper Quick Check test on slot 2 at 5:00:03 PM
on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 2 in Quick Mode (Cal Relay DIB Not
Required)

%JOB_END - ****PASSED**** Relay_Board_Upper Quick Check of slot 2 at 5:00:04 PM

%JOB_START - Beginning Relay_Board_Upper Quick Check test on slot 3 at 5:00:09 PM
on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 3 in Quick Mode (Cal Relay DIB Not
Required)

%JOB_END - ****PASSED**** Relay_Board_Upper Quick Check of slot 3 at 5:00:10 PM

%JOB_START - Beginning Relay_Board_Upper Quick Check test on slot 4 at 5:00:15 PM
on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 4 in Quick Mode (Cal Relay DIB Not
Required)

%JOB_END - ****PASSED**** Relay_Board_Upper Quick Check of slot 4 at 5:00:16 PM

%JOB_START - Beginning Relay_Board_Upper Quick Check test on slot 5 at 5:00:22 PM
on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 5 in Quick Mode (Cal Relay DIB Not Required)

%JOB_END - ****PASSED**** Relay_Board_Upper Quick Check of slot 5 at 5:00:22 PM

%JOB_START - Beginning Relay_Board_Upper Quick Check test on slot 6 at 5:00:28 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 6 in Quick Mode (Cal Relay DIB Not Required)

%JOB_END - ****PASSED**** Relay_Board_Upper Quick Check of slot 6 at 5:00:28 PM

%JOB_START - Beginning Relay_Board_Upper Quick Check test on slot 7 at 5:00:34 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 7 in Quick Mode (Cal Relay DIB Not Required)

%JOB_END - ****PASSED**** Relay_Board_Upper Quick Check of slot 7 at 5:00:34 PM

%JOB_START - Beginning CTO Quick Check test on slot 17 at 5:00:40 PM on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Performing register test...
- Completed register test.
- Performing idprom and temperature test...
- Completed idprom and temperature test.
- Performing PG test...
- Starting PG_History_Ram
- Completed PG_History_Ram
- Starting PG_Svm_Ram
- Completed PG_Svm_Ram

- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Starting PG_LVM_BIST_Ram (up to 32 sec)
- Completed PG_LVM_BIST_Ram in 21.6 sec
- Completed PG test.
- Performing internal loopback test...
- Completed internal loopback test.
- Performing local reference test...
- Completed local reference test.
- Performing internal Capture burst test...
- Completed internal Capture burst test.
- Performing internal loopback burst test...
- Completed internal loopback burst test.

%JOB_END - ****PASSED**** CTO Quick Check of slot 17 (C320E91) at 5:01:04 PM

%JOB_START - Beginning CTO_DIB Quick Check test on slot 17 at 5:01:09 PM on
2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Performing relay test...

%JOB_END - ****PASSED**** CTO_DIB Quick Check of slot 17 (C320E91) at 5:01:10 PM

%JOB_START - Beginning DPS Quick Check test on slot 21 at 5:01:16 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Verifying DPS Current Leakage
- Channel 0
- Channel 1
- Channel 2
- Channel 3
- Channel 4
- Channel 5

- Channel 6
- Channel 7
- Finished Verifying DPS Current Leakage

%JOB_END - ****PASSED**** DPS Quick Check of slot 21 (C0681E7) at 5:01:18 PM

%JOB_START - Beginning DPS Quick Check test on slot 22 at 5:01:23 PM on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Verifying DPS Current Leakage
- Channel 0
- Channel 1
- Channel 2
- Channel 3
- Channel 4
- Channel 5
- Channel 6
- Channel 7
- Finished Verifying DPS Current Leakage

%JOB_END - ****PASSED**** DPS Quick Check of slot 22 (C06862A) at 5:01:26 PM

%JOB_START - Beginning DPS Quick Check test on slot 23 at 5:01:31 PM on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Verifying DPS Current Leakage
- Channel 0
- Channel 1
- Channel 2
- Channel 3
- Channel 4
- Channel 5
- Channel 6
- Channel 7

- Finished Verifying DPS Current Leakage

%JOB_END - ****PASSED**** DPS Quick Check of slot 23 (C0681E9) at 5:01:34 PM

%JOB_START - Beginning DPS Quick Check test on slot 24 at 5:01:39 PM on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Verifying DPS Current Leakage
 - Channel 0
 - Channel 1
 - Channel 2
 - Channel 3
 - Channel 4
 - Channel 5
 - Channel 6
 - Channel 7
- Finished Verifying DPS Current Leakage

%JOB_END - ****PASSED**** DPS Quick Check of slot 24 (C068630) at 5:01:41 PM

%JOB_START - Beginning systemwide tests at 5:01:47 PM on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Systemwide functionality and continuity to slot 0
- Systemwide functionality and continuity to slot 1
- Systemwide functionality and continuity to slot 2
- Systemwide functionality and continuity to slot 3
- Systemwide functionality and continuity to slot 4
- Systemwide functionality and continuity to slot 5
- Systemwide functionality and continuity to slot 6
- Systemwide functionality and continuity to slot 7
- Starting BackPlane Fail Bus test
- Completed BackPlane Fail Bus test
- Completed Extra

%JOB_END - ****PASSED**** Systemwide tests at 5:02:55 PM

%JOB_START - Beginning CUB Module Check test on slot 18 at 5:03:00 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Testing CalCUB and CalDIB Leakage
- Completed Cal Dib Leakage Test
- Testing CalCUB Voltage Sources on CalDIB
- Beginning Cal DIB RawV Test on Slot18
- Completed Cal DIB RawV Test on Slot 18
- Beginning Cal Dib to BPMU Test on Slot 0
- Completed Cal Dib to BPMU Test on Slot 0
- Beginning Cal Dib to EXTERN Test on Slot 0
- Completed Cal Dib to EXTERN Test on Slot 0
- Testing CalDIB Utility Bit Relays using Channel Board Utility Bits for Slot 0
- Completed Cal Dib to UTIL Test on Slot 0
- Beginning Cal Dib RF Tree & Pogo Test

- Beginning Cal Dib to BPMU Test on Slot 1
- Completed Cal Dib to BPMU Test on Slot 1
- Beginning Cal Dib to EXTERN Test on Slot 1
- Completed Cal Dib to EXTERN Test on Slot 1
- Testing CalDIB Utility Bit Relays using Channel Board Utility Bits for Slot 1
- Completed Cal Dib to UTIL Test on Slot 1
- Beginning Cal Dib RF Tree & Pogo Test

- Beginning Cal Dib to BPMU Test on Slot 2
- Completed Cal Dib to BPMU Test on Slot 2
- Beginning Cal Dib to EXTERN Test on Slot 2
- Completed Cal Dib to EXTERN Test on Slot 2
- Testing CalDIB Utility Bit Relays using Channel Board Utility Bits for Slot 2
- Completed Cal Dib to UTIL Test on Slot 2
- Beginning Cal Dib RF Tree & Pogo Test

- Beginning Cal Dib to BPMU Test on Slot 3
- Completed Cal Dib to BPMU Test on Slot 3

- Beginning Cal Dib to EXTERN Test on Slot 3
- Completed Cal Dib to EXTERN Test on Slot 3
- Testing CalDIB Utility Bit Relays using Channel Board Utility Bits for Slot 3
- Completed Cal Dib to UTIL Test on Slot 3
- Beginning Cal Dib RF Tree & Pogo Test

- Beginning Cal Dib to BPMU Test on Slot 4
- Completed Cal Dib to BPMU Test on Slot 4
- Beginning Cal Dib to EXTERN Test on Slot 4
- Completed Cal Dib to EXTERN Test on Slot 4
- Testing CalDIB Utility Bit Relays using Channel Board Utility Bits for Slot 4
- Completed Cal Dib to UTIL Test on Slot 4
- Beginning Cal Dib RF Tree & Pogo Test

- Beginning Cal Dib to BPMU Test on Slot 5
- Completed Cal Dib to BPMU Test on Slot 5
- Beginning Cal Dib to EXTERN Test on Slot 5
- Completed Cal Dib to EXTERN Test on Slot 5
- Testing CalDIB Utility Bit Relays using Channel Board Utility Bits for Slot 5
- Completed Cal Dib to UTIL Test on Slot 5
- Beginning Cal Dib RF Tree & Pogo Test

- Beginning Cal Dib to BPMU Test on Slot 6
- Completed Cal Dib to BPMU Test on Slot 6
- Beginning Cal Dib to EXTERN Test on Slot 6
- Completed Cal Dib to EXTERN Test on Slot 6
- Testing CalDIB Utility Bit Relays using Channel Board Utility Bits for Slot 6
- Completed Cal Dib to UTIL Test on Slot 6
- Beginning Cal Dib RF Tree & Pogo Test

- Beginning Cal Dib to BPMU Test on Slot 7
- Completed Cal Dib to BPMU Test on Slot 7
- Beginning Cal Dib to EXTERN Test on Slot 7
- Completed Cal Dib to EXTERN Test on Slot 7
- Testing CalDIB Utility Bit Relays using Channel Board Utility Bits for Slot 7
- Completed Cal Dib to UTIL Test on Slot 7
- Beginning Cal Dib RF Tree & Pogo Test

- Beginning Cal DIB to DPS_FSG Test on Slot 21
- Completed Cal DIB to DPS_FSG Test on Slot 21
- Beginning Cal DIB to DPS_FSG Test on Slot 22
- Completed Cal DIB to DPS_FSG Test on Slot 22
- Beginning Cal DIB to DPS_FSG Test on Slot 23
- Completed Cal DIB to DPS_FSG Test on Slot 23
- Beginning Cal DIB to DPS_FSG Test on Slot 24
- Completed Cal DIB to DPS_FSG Test on Slot 24
- Testing Device Ground Sense on CalDIB
- Completed DGS Test
- ****Completed CalDib Test****
- Start Compare Level Vol test
- Completed Compare Level Vol test
- Start Compare Level Voh test
- Completed Compare Level Voh test
- Start Drive_Level_test VIL
- Completed Drive_Level_test VIL
- Start Drive_Level_test VIH
- Completed Drive_Level_test VIH
- Beginning CalCub_Measure_Current using Bpmu in Slot 0
- Completed CalCub_Measure_Current

%JOB_END - ****PASSED**** CUB Module Check of slot 18 (1FC1CA) at 5:04:35 PM

%JOB_START - Beginning Channel_Board_DIB Module Check test on slot 0 at 5:04:41 PM
on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test
- Starting Pin PMU Checker
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests...

- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
- Completed Compare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
 - Continuing KeepAlive test
 - Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode
 - Continuing KeepAlive test
 - Continuing KeepAlive test
- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz
 - Continuing Random Pattern test.
 - Continuing Random Pattern test.

- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
 - Statebus : Checking STB lines : Normal mode, 30MHz
 - Statebus : Checking STB lines : Normal mode, 50MHz
 - Statebus : Checking STB lines : Normal mode, 80MHz
 - Statebus : Checking STB lines : Normal mode, 100MHz
 - Statebus : Checking State number lines : Extended mode, 25MHz
 - Statebus : Checking State number lines : Extended mode, 30MHz
 - Statebus : Checking State number lines : Extended mode, 50MHz
 - Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 25MHz
 - Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 30MHz
 - Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 35MHz
 - Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 50MHz
 - Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 35MHz
 - Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 50MHz

- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 80MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
- Started Scan Load test
- Completed Scan Load test
- Started Scan ADB test
- Completed Scan ADB test
- Completed Scan test
- Starting MultiClock test
- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel_Board_DIB test on slot 0

%JOB_END - ****PASSED**** Channel_Board_DIB Module Check of slot 0 (C0035DE) at
5:09:05 PM

%JOB_START - Beginning Channel_Board_DIB Module Check test on slot 1 at 5:09:11 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test
- Starting Pin PMU Checker
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests...

- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
- Completed Compare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
 - Continuing KeepAlive test
 - Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode
 - Continuing KeepAlive test
 - Continuing KeepAlive test
- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz
 - Continuing Random Pattern test.
 - Continuing Random Pattern test.

- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
 - Statebus : Checking STB lines : Normal mode, 30MHz
 - Statebus : Checking STB lines : Normal mode, 50MHz
 - Statebus : Checking STB lines : Normal mode, 80MHz
 - Statebus : Checking STB lines : Normal mode, 100MHz
 - Statebus : Checking State number lines : Extended mode, 25MHz
 - Statebus : Checking State number lines : Extended mode, 30MHz
 - Statebus : Checking State number lines : Extended mode, 50MHz
 - Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 25MHz
 - Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 30MHz
 - Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 35MHz
 - Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 50MHz
 - Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 35MHz
 - Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 50MHz

- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 80MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
- Started Scan Load test
- Completed Scan Load test
- Started Scan ADB test
- Completed Scan ADB test
- Completed Scan test
- Starting MultiClock test
- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel_Board_DIB test on slot 1

%JOB_END - ****PASSED**** Channel_Board_DIB Module Check of slot 1 (C00533C) at
5:13:35 PM

%JOB_START - Beginning Channel_Board_DIB Module Check test on slot 2 at 5:13:41 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test
- Starting Pin PMU Checker
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests...

- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
- Completed Compare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
 - Continuing KeepAlive test
 - Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode
 - Continuing KeepAlive test
 - Continuing KeepAlive test
- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz

- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
 - Statebus : Checking STB lines : Normal mode, 30MHz
 - Statebus : Checking STB lines : Normal mode, 50MHz
 - Statebus : Checking STB lines : Normal mode, 80MHz
 - Statebus : Checking STB lines : Normal mode, 100MHz
 - Statebus : Checking State number lines : Extended mode, 25MHz
 - Statebus : Checking State number lines : Extended mode, 30MHz
 - Statebus : Checking State number lines : Extended mode, 50MHz
 - Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 25MHz
 - Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 30MHz
 - Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 35MHz
 - Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 50MHz

- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 80MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
- Started Scan Load test
- Completed Scan Load test
- Started Scan ADB test
- Completed Scan ADB test
- Completed Scan test
- Starting MultiClock test
- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel_Board_DIB test on slot 2

%JOB_END - ****PASSED**** Channel_Board_DIB Module Check of slot 2 (C1287A1) at
5:18:05 PM

%JOB_START - Beginning Channel_Board_DIB Module Check test on slot 3 at 5:18:11 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test
- Starting Pin PMU Checker
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...

- Performing PPMU force current tests...
- Performing PPMU measure current tests...
- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
- Completed Compare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
 - Continuing KeepAlive test
 - Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode
 - Continuing KeepAlive test
 - Continuing KeepAlive test
- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz

- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
 - Statebus : Checking STB lines : Normal mode, 30MHz
 - Statebus : Checking STB lines : Normal mode, 50MHz
 - Statebus : Checking STB lines : Normal mode, 80MHz
 - Statebus : Checking STB lines : Normal mode, 100MHz
 - Statebus : Checking State number lines : Extended mode, 25MHz
 - Statebus : Checking State number lines : Extended mode, 30MHz
 - Statebus : Checking State number lines : Extended mode, 50MHz
 - Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 25MHz
 - Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 30MHz
 - Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 35MHz
 - Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 50MHz

- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 80MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
- Started Scan Load test
- Completed Scan Load test
- Started Scan ADB test
- Completed Scan ADB test
- Completed Scan test
- Starting MultiClock test
- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel_Board_DIB test on slot 3

%JOB_END - ****PASSED**** Channel_Board_DIB Module Check of slot 3 (C00A4BD) at
5:22:35 PM

%JOB_START - Beginning Channel_Board_DIB Module Check test on slot 4 at 5:22:41 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test
- Starting Pin PMU Checker
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...

- Performing PPMU force current tests...
- Performing PPMU measure current tests...
- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
- Completed Compare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
 - Continuing KeepAlive test
 - Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode
 - Continuing KeepAlive test
 - Continuing KeepAlive test
- Completed KeepAlive test.

- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
 - Statebus : Checking STB lines : Normal mode, 30MHz
 - Statebus : Checking STB lines : Normal mode, 50MHz
 - Statebus : Checking STB lines : Normal mode, 80MHz
 - Statebus : Checking STB lines : Normal mode, 100MHz
 - Statebus : Checking State number lines : Extended mode, 25MHz
 - Statebus : Checking State number lines : Extended mode, 30MHz
 - Statebus : Checking State number lines : Extended mode, 50MHz
 - Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 25MHz
 - Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 30MHz
 - Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 35MHz

- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 80MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
 - Started Scan Load test
 - Completed Scan Load test
 - Started Scan ADB test
 - Completed Scan ADB test
- Completed Scan test
- Starting MultiClock test
- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel_Board_DIB test on slot 4

%JOB_END - ****PASSED**** Channel_Board_DIB Module Check of slot 4 (C00D321) at
5:27:05 PM

%JOB_START - Beginning Channel_Board_DIB Module Check test on slot 5 at 5:27:11 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
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- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test
- Starting Pin PMU Checker
 - Performing PPMU force voltage tests...

- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests...
- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
- Completed Compare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
 - Continuing KeepAlive test
 - Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode
 - Continuing KeepAlive test
 - Continuing KeepAlive test

- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
- Statebus : Checking STB lines : Normal mode, 30MHz
- Statebus : Checking STB lines : Normal mode, 50MHz
- Statebus : Checking STB lines : Normal mode, 80MHz
- Statebus : Checking STB lines : Normal mode, 100MHz
- Statebus : Checking State number lines : Extended mode, 25MHz
- Statebus : Checking State number lines : Extended mode, 30MHz
- Statebus : Checking State number lines : Extended mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 25MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 30MHz

- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 80MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
 - Started Scan Load test
 - Completed Scan Load test
 - Started Scan ADB test
 - Completed Scan ADB test
- Completed Scan test
- Starting MultiClock test
- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel_Board_DIB test on slot 5

%JOB_END - ****PASSED**** Channel_Board_DIB Module Check of slot 5 (C003C8E) at
5:31:35 PM

%JOB_START - Beginning Channel_Board_DIB Module Check test on slot 6 at 5:31:41 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
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- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test
- Starting Pin PMU Checker

- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests...
- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
- Completed Compare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
 - Continuing KeepAlive test
 - Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode
 - Continuing KeepAlive test
 - Continuing KeepAlive test

- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
- Statebus : Checking STB lines : Normal mode, 30MHz
- Statebus : Checking STB lines : Normal mode, 50MHz
- Statebus : Checking STB lines : Normal mode, 80MHz
- Statebus : Checking STB lines : Normal mode, 100MHz
- Statebus : Checking State number lines : Extended mode, 25MHz
- Statebus : Checking State number lines : Extended mode, 30MHz
- Statebus : Checking State number lines : Extended mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 25MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 30MHz

- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 80MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
- Started Scan Load test
- Completed Scan Load test
- Started Scan ADB test
- Completed Scan ADB test
- Completed Scan test
- Starting MultiClock test
- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel_Board_DIB test on slot 6

%JOB_END - ****PASSED**** Channel_Board_DIB Module Check of slot 6 (C0DC273) at
5:36:05 PM

%JOB_START - Beginning Channel_Board_DIB Module Check test on slot 7 at 5:36:11 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
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- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test
- Starting Pin PMU Checker

- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests...
- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
- Completed Compare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
 - Continuing KeepAlive test
 - Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode
 - Continuing KeepAlive test

- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
 - Statebus : Checking STB lines : Normal mode, 30MHz
 - Statebus : Checking STB lines : Normal mode, 50MHz
 - Statebus : Checking STB lines : Normal mode, 80MHz
 - Statebus : Checking STB lines : Normal mode, 100MHz
 - Statebus : Checking State number lines : Extended mode, 25MHz
 - Statebus : Checking State number lines : Extended mode, 30MHz
 - Statebus : Checking State number lines : Extended mode, 50MHz
 - Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 25MHz

- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 30MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 80MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
 - Started Scan Load test
 - Completed Scan Load test
 - Started Scan ADB test
 - Completed Scan ADB test
- Completed Scan test
- Starting MultiClock test
- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel_Board_DIB test on slot 7

%JOB_END - ****PASSED**** Channel_Board_DIB Module Check of slot 7 (5000F42) at
5:40:35 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 0 at 5:40:41 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 0 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 0 at 5:40:41 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 1 at 5:40:47 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 1 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 1 at 5:40:47 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 2 at 5:40:53 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 2 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 2 at 5:40:53 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 3 at 5:40:59 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 3 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 3 at 5:40:59 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 4 at 5:41:05 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 4 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 4 at 5:41:05 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 5 at 5:41:11 PM
on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 5 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 5 at 5:41:12 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 6 at 5:41:17 PM
on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 6 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 6 at 5:41:18 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 7 at 5:41:23 PM
on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 7 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 7 at 5:41:24 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 0 at 5:41:29 PM
on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 0 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 0 at 5:41:30 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 1 at 5:41:35 PM
on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 1 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 1 at 5:41:36 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 2 at 5:41:41 PM
on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 2 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 2 at 5:41:42 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 3 at 5:41:47 PM
on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 3 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 3 at 5:41:48 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 4 at 5:41:54 PM
on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 4 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 4 at 5:41:54 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 5 at 5:42:00 PM
on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 5 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 5 at 5:42:00 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 6 at 5:42:06 PM
on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 6 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 6 at 5:42:06 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 7 at 5:42:12 PM
on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 7 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 7 at 5:42:12 PM

%JOB_START - Beginning DPS_DIB Module Check test on slot 21 at 5:42:18 PM on
2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Verifying DPS DIB MOUT Output Impedance Test

- Verifying DPS DIB Current Measure Output Test

%JOB_END - ****PASSED**** DPS_DIB Module Check of slot 21 (C0681E7) at 5:42:37 PM

%JOB_START - Beginning DPS_DIB Module Check test on slot 22 at 5:42:42 PM on
2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Verifying DPS DIB MOUT Output Impedance Test

- Verifying DPS DIB Current Measure Output Test

%JOB_END - ****PASSED**** DPS_DIB Module Check of slot 22 (C06862A) at 5:43:02 PM

%JOB_START - Beginning DPS_DIB Module Check test on slot 23 at 5:43:07 PM on
2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Verifying DPS DIB MOUT Output Impedance Test
- Verifying DPS DIB Current Measure Output Test

%JOB_END - ****PASSED**** DPS_DIB Module Check of slot 23 (C0681E9) at 5:43:26 PM

%JOB_START - Beginning DPS_DIB Module Check test on slot 24 at 5:43:32 PM on
2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Verifying DPS DIB MOUT Output Impedance Test
- Verifying DPS DIB Current Measure Output Test

%JOB_END - ****PASSED**** DPS_DIB Module Check of slot 24 (C068630) at 5:43:51 PM

%JOB_START - Beginning CTO Module Check test on slot 17 at 5:43:56 PM on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Performing CTO calibration test...
- Performing RAM test...

%JOB_END - ****PASSED**** CTO Module Check of slot 17 (C320E91) at 5:44:00 PM

%JOB_START - Beginning CTO_DIB Module Check test on slot 17 at 5:44:05 PM on
2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Performing internal reference test...
- Performing VREF voltage test...
- Performing PPMU force V test...
- Performing PPMU force I test...
- Performing PPMU measure V test...
- Performing PPMU measure I test...
- Performing Source voltage test...
- Performing Capture voltage test...
- Performing Source/Capture loopback test...
- Performing Source burst test...
- Performing Capture burst test...
- Performing Source/Capture loopback burst test...

%JOB_END - ****PASSED**** CTO_DIB Module Check of slot 17 (C320E91) at 5:46:06 PM

%JOB_START - Beginning CUB Calibration test on slot 18 at 5:46:12 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Starting Channel Drive Levels: Vih Vil
- Starting Channel Compare Levels: Voh Vol

%JOB_END - ****PASSED**** CUB Calibration of slot 18 (1FC1CA) at 5:46:13 PM

%JOB_START - Beginning Channel_Board_DIB Calibration test on slot 0 at 5:46:19 PM
on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Starting dib_test
- Temperature at PE Ch00 is 58 deg C
- Temperature at PE Ch60 is 41 deg C
- Temperature at Incoming Air is 26 deg C
- Temperature at TG Ch00 is 47 deg C
- Starting BPMU Force Voltage

- Starting Bpmu Clamp Voltage
- Starting Bpmu Measure Voltage
- Starting Bpmu Measure Current
- Starting Bpmu Force current
- Starting Bpmu Limit current
- Starting Channel Drive Levels: Vih Vil
- Starting Channel Compare Levels: Voh Vol
- Starting Channel Clamps: Vch Vcl
- Starting Channel Loads: Ioh Iol
- Starting Channel Load Threshold: Vt
- Starting HV Channel Drive Levels
- Starting HV Channel Drive Current
- Ppmu Mi Warmup 5:48:33 PM
- Ppmu Mi Warmup 5:48:33 PM
- Starting Ppmu Force Voltage
- Starting Ppmu Measure Voltage
- Starting Ppmu Force Current Ppmu Force 200ua
- Starting Ppmu Force Current Ppmu Force 2ma
- Starting Ppmu Measure 2ma
- Continuing Ppmu Measure Current chan 15
- Continuing Ppmu Measure Current chan 31
- Continuing Ppmu Measure Current chan 47
- Continuing Ppmu Measure Current chan 63
- Starting Ppmu Measure 200ua
- Continuing Ppmu Measure Current chan 15
- Continuing Ppmu Measure Current chan 31
- Continuing Ppmu Measure Current chan 47
- Continuing Ppmu Measure Current chan 63
- Starting Ppmu Measure Int 20ua
- Continuing Ppmu Measure Current chan 15
- Continuing Ppmu Measure Current chan 31
- Continuing Ppmu Measure Current chan 47
- Continuing Ppmu Measure Current chan 63
- Starting Ppmu Measure Int 2ua
- Continuing Ppmu Measure Current chan 15
- Continuing Ppmu Measure Current chan 31
- Continuing Ppmu Measure Current chan 47
- Continuing Ppmu Measure Current chan 63

- Starting Ppmu Measure Int 200na
- Continuing Ppmu Measure Current chan 15
- Continuing Ppmu Measure Current chan 31
- Continuing Ppmu Measure Current chan 47
- Continuing Ppmu Measure Current chan 63
- Finished Channel Calibration

%JOB_END - ****PASSED**** Channel_Board_DIB Calibration of slot 0 (C0035DE) at
5:51:40 PM

%JOB_START - Beginning Channel_Board_DIB Calibration test on slot 1 at 5:51:45 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Starting dib_test
- Temperature at PE Ch00 is 59 deg C
- Temperature at PE Ch60 is 40 deg C
- Temperature at Incoming Air is 25 deg C
- Temperature at TG Ch00 is 46 deg C
- Starting BPMU Force Voltage
- Starting Bpmu Clamp Voltage
- Starting Bpmu Measure Voltage
- Starting Bpmu Measure Current
- Starting Bpmu Force current
- Starting Bpmu Limit current
- Starting Channel Drive Levels: Vih Vil
- Starting Channel Compare Levels: Voh Vol
- Starting Channel Clamps: Vch Vcl
- Starting Channel Loads: Ioh Iol
- Starting Channel Load Threshold: Vt
- Starting HV Channel Drive Levels
- Starting HV Channel Drive Current
- Ppmu Mi Warmup 5:53:59 PM
- Ppmu Mi Warmup 5:53:59 PM
- Starting Ppmu Force Voltage
- Starting Ppmu Measure Voltage
- Starting Ppmu Force Current Ppmu Force 200ua

- Starting Ppmu Force Current Ppmu Force 2ma
- Starting Ppmu Measure 2ma
- Continuing Ppmu Measure Current chan 79
- Continuing Ppmu Measure Current chan 95
- Continuing Ppmu Measure Current chan 111
- Continuing Ppmu Measure Current chan 127
- Starting Ppmu Measure 200ua
- Continuing Ppmu Measure Current chan 79
- Continuing Ppmu Measure Current chan 95
- Continuing Ppmu Measure Current chan 111
- Continuing Ppmu Measure Current chan 127
- Starting Ppmu Measure Int 20ua
- Continuing Ppmu Measure Current chan 79
- Continuing Ppmu Measure Current chan 95
- Continuing Ppmu Measure Current chan 111
- Continuing Ppmu Measure Current chan 127
- Starting Ppmu Measure Int 2ua
- Continuing Ppmu Measure Current chan 79
- Continuing Ppmu Measure Current chan 95
- Continuing Ppmu Measure Current chan 111
- Continuing Ppmu Measure Current chan 127
- Starting Ppmu Measure Int 200na
- Continuing Ppmu Measure Current chan 79
- Continuing Ppmu Measure Current chan 95
- Continuing Ppmu Measure Current chan 111
- Continuing Ppmu Measure Current chan 127
- Finished Channel Calibration

%JOB_END - ****PASSED**** Channel_Board_DIB Calibration of slot 1 (C00533C) at
5:57:06 PM

%JOB_START - Beginning Channel_Board_DIB Calibration test on slot 2 at 5:57:11 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Starting dib_test
- Temperature at PE Ch00 is 59 deg C

- Temperature at PE Ch60 is 41 deg C
- Temperature at Incoming Air is 25 deg C
- Temperature at TG Ch00 is 46 deg C
- Starting BPMU Force Voltage
- Starting Bpmu Clamp Voltage
- Starting Bpmu Measure Voltage
- Starting Bpmu Measure Current
- Starting Bpmu Force current
- Starting Bpmu Limit current
- Starting Channel Drive Levels: Vih Vil
- Starting Channel Compare Levels: Voh Vol
- Starting Channel Clamps: Vch Vcl
- Starting Channel Loads: Ioh Iol
- Starting Channel Load Threshold: Vt
- Starting HV Channel Drive Levels
- Starting HV Channel Drive Current
- Ppmu Mi Warmup 5:59:24 PM
- Ppmu Mi Warmup 5:59:24 PM
- Starting Ppmu Force Voltage
- Starting Ppmu Measure Voltage
- Starting Ppmu Force Current Ppmu Force 200ua
- Starting Ppmu Force Current Ppmu Force 2ma
- Starting Ppmu Measure 2ma
- Continuing Ppmu Measure Current chan 143
- Continuing Ppmu Measure Current chan 159
- Continuing Ppmu Measure Current chan 175
- Continuing Ppmu Measure Current chan 191
- Starting Ppmu Measure 200ua
- Continuing Ppmu Measure Current chan 143
- Continuing Ppmu Measure Current chan 159
- Continuing Ppmu Measure Current chan 175
- Continuing Ppmu Measure Current chan 191
- Starting Ppmu Measure Int 20ua
- Continuing Ppmu Measure Current chan 143
- Continuing Ppmu Measure Current chan 159
- Continuing Ppmu Measure Current chan 175
- Continuing Ppmu Measure Current chan 191
- Starting Ppmu Measure Int 2ua

- Continuing Ppmu Measure Current chan 143
- Continuing Ppmu Measure Current chan 159
- Continuing Ppmu Measure Current chan 175
- Continuing Ppmu Measure Current chan 191
- Starting Ppmu Measure Int 200na
- Continuing Ppmu Measure Current chan 143
- Continuing Ppmu Measure Current chan 159
- Continuing Ppmu Measure Current chan 175
- Continuing Ppmu Measure Current chan 191
- Finished Channel Calibration

%JOB_END - ****PASSED**** Channel_Board_DIB Calibration of slot 2 (C1287A1) at
6:02:31 PM

%JOB_START - Beginning Channel_Board_DIB Calibration test on slot 3 at 6:02:36 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Starting dib_test
- Temperature at PE Ch00 is 61 deg C
- Temperature at PE Ch60 is 41 deg C
- Temperature at Incoming Air is 25 deg C
- Temperature at TG Ch00 is 46 deg C
- Starting BPMU Force Voltage
- Starting Bpmu Clamp Voltage
- Starting Bpmu Measure Voltage
- Starting Bpmu Measure Current
- Starting Bpmu Force current
- Starting Bpmu Limit current
- Starting Channel Drive Levels: Vih Vil
- Starting Channel Compare Levels: Voh Vol
- Starting Channel Clamps: Vch Vcl
- Starting Channel Loads: Ioh Iol
- Starting Channel Load Threshold: Vt
- Starting HV Channel Drive Levels
- Starting HV Channel Drive Current
- Ppmu Mi Warmup 6:04:51 PM

- Ppmu Mi Warmup 6:04:51 PM
- Starting Ppmu Force Voltage
- Starting Ppmu Measure Voltage
- Starting Ppmu Force Current Ppmu Force 200ua
- Starting Ppmu Force Current Ppmu Force 2ma
- Starting Ppmu Measure 2ma

- Continuing Ppmu Measure Current chan 207
- Continuing Ppmu Measure Current chan 223
- Continuing Ppmu Measure Current chan 239
- Continuing Ppmu Measure Current chan 255
- Starting Ppmu Measure 200ua
- Continuing Ppmu Measure Current chan 207
- Continuing Ppmu Measure Current chan 223
- Continuing Ppmu Measure Current chan 239
- Continuing Ppmu Measure Current chan 255
- Starting Ppmu Measure Int 20ua
- Continuing Ppmu Measure Current chan 207
- Continuing Ppmu Measure Current chan 223
- Continuing Ppmu Measure Current chan 239
- Continuing Ppmu Measure Current chan 255
- Starting Ppmu Measure Int 2ua
- Continuing Ppmu Measure Current chan 207
- Continuing Ppmu Measure Current chan 223
- Continuing Ppmu Measure Current chan 239
- Continuing Ppmu Measure Current chan 255
- Starting Ppmu Measure Int 200na
- Continuing Ppmu Measure Current chan 207
- Continuing Ppmu Measure Current chan 223
- Continuing Ppmu Measure Current chan 239
- Continuing Ppmu Measure Current chan 255
- Finished Channel Calibration

%JOB_END - ****PASSED**** Channel_Board_DIB Calibration of slot 3 (C00A4BD) at
6:07:57 PM

%JOB_START - Beginning Channel_Board_DIB Calibration test on slot 4 at 6:08:03 PM
on 2/29/2020

- Starting dib_test
- Temperature at PE Ch00 is 60 deg C
- Temperature at PE Ch60 is 42 deg C
- Temperature at Incoming Air is 25 deg C
- Temperature at TG Ch00 is 47 deg C
- Starting BPMU Force Voltage
- Starting Bpmu Clamp Voltage
- Starting Bpmu Measure Voltage
- Starting Bpmu Measure Current
- Starting Bpmu Force current
- Starting Bpmu Limit current
- Starting Channel Drive Levels: Vih Vil
- Starting Channel Compare Levels: Voh Vol
- Starting Channel Clamps: Vch Vcl
- Starting Channel Loads: Ioh Iol
- Starting Channel Load Threshold: Vt
- Starting HV Channel Drive Levels
- Starting HV Channel Drive Current
- Ppmu Mi Warmup 6:10:14 PM
- Ppmu Mi Warmup 6:10:14 PM
- Starting Ppmu Force Voltage
- Starting Ppmu Measure Voltage
- Starting Ppmu Force Current Ppmu Force 200ua
- Starting Ppmu Force Current Ppmu Force 2ma
- Starting Ppmu Measure 2ma
- Continuing Ppmu Measure Current chan 271
- Continuing Ppmu Measure Current chan 287
- Continuing Ppmu Measure Current chan 303
- Continuing Ppmu Measure Current chan 319
- Starting Ppmu Measure 200ua
- Continuing Ppmu Measure Current chan 271
- Continuing Ppmu Measure Current chan 287
- Continuing Ppmu Measure Current chan 303
- Continuing Ppmu Measure Current chan 319
- Starting Ppmu Measure Int 20ua

- Continuing Ppmu Measure Current chan 271
- Continuing Ppmu Measure Current chan 287
- Continuing Ppmu Measure Current chan 303
- Continuing Ppmu Measure Current chan 319
- Starting Ppmu Measure Int 2ua
- Continuing Ppmu Measure Current chan 271
- Continuing Ppmu Measure Current chan 287
- Continuing Ppmu Measure Current chan 303
- Continuing Ppmu Measure Current chan 319
- Starting Ppmu Measure Int 200na
- Continuing Ppmu Measure Current chan 271
- Continuing Ppmu Measure Current chan 287
- Continuing Ppmu Measure Current chan 303
- Continuing Ppmu Measure Current chan 319
- Finished Channel Calibration

%JOB_END - ****PASSED**** Channel_Board_DIB Calibration of slot 4 (C00D321) at
6:13:20 PM

%JOB_START - Beginning Channel_Board_DIB Calibration test on slot 5 at 6:13:26 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Starting dib_test
- Temperature at PE Ch00 is 60 deg C
- Temperature at PE Ch60 is 43 deg C
- Temperature at Incoming Air is 25 deg C
- Temperature at TG Ch00 is 46 deg C
- Starting BPMU Force Voltage
- Starting Bpmu Clamp Voltage
- Starting Bpmu Measure Voltage
- Starting Bpmu Measure Current
- Starting Bpmu Force current
- Starting Bpmu Limit current
- Starting Channel Drive Levels: Vih Vil
- Starting Channel Compare Levels: Voh Vol
- Starting Channel Clamps: Vch Vcl

- Starting Channel Loads: Ioh Iol
- Starting Channel Load Threshold: Vt
- Starting HV Channel Drive Levels
- Starting HV Channel Drive Current
- Ppmu Mi Warmup 6:15:37 PM
- Ppmu Mi Warmup 6:15:37 PM
- Starting Ppmu Force Voltage
- Starting Ppmu Measure Voltage
- Starting Ppmu Force Current Ppmu Force 200ua
- Starting Ppmu Force Current Ppmu Force 2ma
- Starting Ppmu Measure 2ma
- Continuing Ppmu Measure Current chan 335
- Continuing Ppmu Measure Current chan 351
- Continuing Ppmu Measure Current chan 367
- Continuing Ppmu Measure Current chan 383
- Starting Ppmu Measure 200ua
- Continuing Ppmu Measure Current chan 335
- Continuing Ppmu Measure Current chan 351
- Continuing Ppmu Measure Current chan 367
- Continuing Ppmu Measure Current chan 383
- Starting Ppmu Measure Int 20ua
- Continuing Ppmu Measure Current chan 335
- Continuing Ppmu Measure Current chan 351
- Continuing Ppmu Measure Current chan 367
- Continuing Ppmu Measure Current chan 383
- Starting Ppmu Measure Int 2ua
- Continuing Ppmu Measure Current chan 335
- Continuing Ppmu Measure Current chan 351
- Continuing Ppmu Measure Current chan 367
- Continuing Ppmu Measure Current chan 383
- Starting Ppmu Measure Int 200na
- Continuing Ppmu Measure Current chan 335
- Continuing Ppmu Measure Current chan 351
- Continuing Ppmu Measure Current chan 367
- Continuing Ppmu Measure Current chan 383
- Finished Channel Calibration

%JOB_END - ****PASSED**** Channel_Board_DIB Calibration of slot 5 (C003C8E) at

6:18:44 PM

%JOB_START - Beginning Channel_Board_DIB Calibration test on slot 6 at 6:18:49 PM

on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Starting dib_test
- Temperature at PE Ch00 is 61 deg C
- Temperature at PE Ch60 is 47 deg C
- Temperature at Incoming Air is 25 deg C
- Temperature at TG Ch00 is 45 deg C
- Starting BPMU Force Voltage
- Starting Bpmu Clamp Voltage
- Starting Bpmu Measure Voltage
- Starting Bpmu Measure Current
- Starting Bpmu Force current
- Starting Bpmu Limit current
- Starting Channel Drive Levels: Vih Vil
- Starting Channel Compare Levels: Voh Vol
- Starting Channel Clamps: Vch Vcl
- Starting Channel Loads: Ioh Iol
- Starting Channel Load Threshold: Vt
- Starting HV Channel Drive Levels
- Starting HV Channel Drive Current
- Ppmu Mi Warmup 6:21:03 PM
- Ppmu Mi Warmup 6:21:03 PM
- Starting Ppmu Force Voltage
- Starting Ppmu Measure Voltage
- Starting Ppmu Force Current Ppmu Force 200ua
- Starting Ppmu Force Current Ppmu Force 2ma
- Starting Ppmu Measure 2ma
- Continuing Ppmu Measure Current chan 399
- Continuing Ppmu Measure Current chan 415
- Continuing Ppmu Measure Current chan 431
- Continuing Ppmu Measure Current chan 447
- Starting Ppmu Measure 200ua
- Continuing Ppmu Measure Current chan 399

- Continuing Ppmu Measure Current chan 415
- Continuing Ppmu Measure Current chan 431
- Continuing Ppmu Measure Current chan 447
- Starting Ppmu Measure Int 20ua
- Continuing Ppmu Measure Current chan 399
- Continuing Ppmu Measure Current chan 415
- Continuing Ppmu Measure Current chan 431
- Continuing Ppmu Measure Current chan 447
- Starting Ppmu Measure Int 2ua
- Continuing Ppmu Measure Current chan 399
- Continuing Ppmu Measure Current chan 415
- Continuing Ppmu Measure Current chan 431
- Continuing Ppmu Measure Current chan 447
- Starting Ppmu Measure Int 200na
- Continuing Ppmu Measure Current chan 399
- Continuing Ppmu Measure Current chan 415
- Continuing Ppmu Measure Current chan 431
- Continuing Ppmu Measure Current chan 447
- Finished Channel Calibration

%JOB_END - ****PASSED**** Channel_Board_DIB Calibration of slot 6 (C0DC273) at
6:24:10 PM

%JOB_START - Beginning Channel_Board_DIB Calibration test on slot 7 at 6:24:15 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Starting dib_test
- Temperature at PE Ch00 is 59 deg C
- Temperature at PE Ch60 is 46 deg C
- Temperature at Incoming Air is 26 deg C
- Temperature at TG Ch00 is 45 deg C
- Starting BPMU Force Voltage
- Starting Bpmu Clamp Voltage
- Starting Bpmu Measure Voltage
- Starting Bpmu Measure Current
- Starting Bpmu Force current

- Starting Bpmu Limit current
- Starting Channel Drive Levels: Vih Vil
- Starting Channel Compare Levels: Voh Vol
- Starting Channel Clamps: Vch Vcl
- Starting Channel Loads: Ioh Iol
- Starting Channel Load Threshold: Vt
- Starting HV Channel Drive Levels
- Starting HV Channel Drive Current
- Ppmu Mi Warmup 6:26:26 PM
- Ppmu Mi Warmup 6:26:26 PM
- Starting Ppmu Force Voltage
- Starting Ppmu Measure Voltage
- Starting Ppmu Force Current Ppmu Force 200ua
- Starting Ppmu Force Current Ppmu Force 2ma
- Starting Ppmu Measure 2ma
- Continuing Ppmu Measure Current chan 463
- Continuing Ppmu Measure Current chan 479
- Continuing Ppmu Measure Current chan 495
- Continuing Ppmu Measure Current chan 511
- Starting Ppmu Measure 200ua
- Continuing Ppmu Measure Current chan 463
- Continuing Ppmu Measure Current chan 479
- Continuing Ppmu Measure Current chan 495
- Continuing Ppmu Measure Current chan 511
- Starting Ppmu Measure Int 20ua
- Continuing Ppmu Measure Current chan 463
- Continuing Ppmu Measure Current chan 479
- Continuing Ppmu Measure Current chan 495
- Continuing Ppmu Measure Current chan 511
- Starting Ppmu Measure Int 2ua
- Continuing Ppmu Measure Current chan 463
- Continuing Ppmu Measure Current chan 479
- Continuing Ppmu Measure Current chan 495
- Continuing Ppmu Measure Current chan 511
- Starting Ppmu Measure Int 200na
- Continuing Ppmu Measure Current chan 463
- Continuing Ppmu Measure Current chan 479
- Continuing Ppmu Measure Current chan 495

- Continuing Ppmu Measure Current chan 511
- Finished Channel Calibration

%JOB_END - ****PASSED**** Channel_Board_DIB Calibration of slot 7 (5000F42) at
6:29:33 PM

%JOB_START - Beginning DPS_DIB Calibration test on slot 21 at 6:29:38 PM on
2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Starting DPS Calibration on slot 21
- Calibrating DPS Voltage on slot 21
- Calibrating DPS Current Limit on slot 21
- Calibrating DPS Current Measure (50uA Range) on slot 21
- Calibrating DPS Current Measure (500uA Range) on slot 21
- Calibrating DPS Current Measure (10mA Range) on slot 21
- Calibrating DPS Current Measure (100mA Range) on slot 21
- Calibrating DPS Current Measure (1A Range) on slot 21
- Finished DPS Calibration on slot 21

%JOB_END - ****PASSED**** DPS_DIB Calibration of slot 21 (C0681E7) at 6:29:53 PM

%JOB_START - Beginning DPS_DIB Calibration test on slot 22 at 6:29:59 PM on
2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Starting DPS Calibration on slot 22
- Calibrating DPS Voltage on slot 22
- Calibrating DPS Current Limit on slot 22
- Calibrating DPS Current Measure (50uA Range) on slot 22
- Calibrating DPS Current Measure (500uA Range) on slot 22
- Calibrating DPS Current Measure (10mA Range) on slot 22

- Calibrating DPS Current Measure (100mA Range) on slot 22
- Calibrating DPS Current Measure (1A Range) on slot 22

- Finished DPS Calibration on slot 22

%JOB_END - ****PASSED**** DPS_DIB Calibration of slot 22 (C06862A) at 6:30:13 PM

%JOB_START - Beginning DPS_DIB Calibration test on slot 23 at 6:30:19 PM on

2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Starting DPS Calibration on slot 23

- Calibrating DPS Voltage on slot 23
- Calibrating DPS Current Limit on slot 23
- Calibrating DPS Current Measure (50uA Range) on slot 23
- Calibrating DPS Current Measure (500uA Range) on slot 23
- Calibrating DPS Current Measure (10mA Range) on slot 23
- Calibrating DPS Current Measure (100mA Range) on slot 23
- Calibrating DPS Current Measure (1A Range) on slot 23

- Finished DPS Calibration on slot 23

%JOB_END - ****PASSED**** DPS_DIB Calibration of slot 23 (C0681E9) at 6:30:34 PM

%JOB_START - Beginning DPS_DIB Calibration test on slot 24 at 6:30:39 PM on

2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Starting DPS Calibration on slot 24

- Calibrating DPS Voltage on slot 24
- Calibrating DPS Current Limit on slot 24
- Calibrating DPS Current Measure (50uA Range) on slot 24

- Calibrating DPS Current Measure (500uA Range) on slot 24
- Calibrating DPS Current Measure (10mA Range) on slot 24
- Calibrating DPS Current Measure (100mA Range) on slot 24
- Calibrating DPS Current Measure (1A Range) on slot 24

- Finished DPS Calibration on slot 24

%JOB_END - ****PASSED**** DPS_DIB Calibration of slot 24 (C068630) at 6:30:54 PM

%JOB_START - Beginning CTO_DIB Calibration test on slot 17 at 6:30:59 PM on
2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Performing CTO Voltage Reference Calibration
- Performing CTO PPMU Force Voltage Calibration
- Performing CTO PPMU Measure Voltage Calibration
- Performing CTO PPMU Measure and Force Current Calibration on channel 0
- Performing CTO PPMU Measure and Force Current Calibration on channel 1
- Performing CTO PPMU Measure and Force Current Calibration on channel 2
- Performing CTO PPMU Measure and Force Current Calibration on channel 3
- Performing CTO PPMU Measure and Force Current Calibration on channel 4
- Performing CTO PPMU Measure and Force Current Calibration on channel 5
- Performing CTO PPMU Measure and Force Current Calibration on channel 6
- Performing CTO PPMU Measure and Force Current Calibration on channel 7

%JOB_END - ****PASSED**** CTO_DIB Calibration of slot 17 (C320E91) at 6:32:30 PM

%JOB_START - Beginning AC Calibration at 6:32:36 PM on 2/29/2020 in High Accuracy
Mode

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Beginning Digital Channel Timing Calibration
- Building List of Digital Channels
- Checking CalDIB AC Continuity for all Digital Channels
- Calibrating Super Linear Interpolator (SLI)

- Measuring Cub Round Trip Delay
- Measuring RF Matrix Delay

- Calibrating Drive Edges
- Calibrating Receive Edges
- Calibrating Receive Window
- Calibrating Mux Drive
- Calibrating Receive Mux
- Completed Digital Channel Timing Calibration

%JOB_END - ****PASSED**** AC Calibration at 7:08:31 PM

%JOB_START - Beginning Channel_Board Performance Verification test on slot 0 at
7:08:37 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Starting dib_test
- Temperature at PE Ch00 is 57 deg C
- Temperature at PE Ch60 is 40 deg C
- Temperature at Incoming Air is 25 deg C
- Temperature at TG Ch00 is 46 deg C
- Performing VIH/VIL level tests... at DGS=0mV
- Performing IOH/IOL level tests...
- Performing VT level tests...
- Starting Channel Comparator Performance Verification on slot 0
- Finished Channel Comparator Performance Verification on slot 0
- Performing Clamp level tests...
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests... DGS=0mV
- Starting BPMU Performance Verification on slot 0, DGS=-217.810 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy

- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 0, DGS= 5.200 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 0, DGS= 182.336 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Finished BPMU Performance Verification on slot 0
- Starting High Voltage DC Performance Verification on slot 0
- Finished High Voltage DC Performance Verification on slot 0

%JOB_END - ****PASSED**** Channel_Board Performance Verification of slot 0
(C0035DE) at 7:17:02 PM

%JOB_START - Beginning Channel_Board Performance Verification test on slot 1 at
7:17:08 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Starting dib_test
- Temperature at PE Ch00 is 53 deg C
- Temperature at PE Ch60 is 36 deg C
- Temperature at Incoming Air is 25 deg C
- Temperature at TG Ch00 is 45 deg C
- Performing VIH/VIL level tests... at DGS=0mV
- Performing IOH/IOL level tests...
- Performing VT level tests...

- Starting Channel Comparator Performance Verification on slot 1
- Finished Channel Comparator Performance Verification on slot 1
- Performing Clamp level tests...
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests... DGS=0mV
- Starting BPMU Performance Verification on slot 1, DGS=-221.388 mV
 - Verifying BPMU Forced Voltage Accuracy
 - Verifying BPMU Voltage Measure Accuracy
 - Verifying BPMU Forced Current Accuracy
 - Verifying BPMU Current Measure Accuracy
 - Verifying BPMU Voltage Clamping Accuracy
 - Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 1, DGS= 1.750 mV
 - Verifying BPMU Forced Voltage Accuracy
 - Verifying BPMU Voltage Measure Accuracy
 - Verifying BPMU Forced Current Accuracy
 - Verifying BPMU Current Measure Accuracy
 - Verifying BPMU Voltage Clamping Accuracy
 - Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 1, DGS= 178.710 mV
 - Verifying BPMU Forced Voltage Accuracy
 - Verifying BPMU Voltage Measure Accuracy
 - Verifying BPMU Forced Current Accuracy
 - Verifying BPMU Current Measure Accuracy
 - Verifying BPMU Voltage Clamping Accuracy
 - Verifying BPMU Current Clamping Accuracy
- Finished BPMU Performance Verification on slot 1
- Starting High Voltage DC Performance Verification on slot 1
- Finished High Voltage DC Performance Verification on slot 1

%JOB_END - ****PASSED**** Channel_Board Performance Verification of slot 1
(C00533C) at 7:25:33 PM

%JOB_START - Beginning Channel_Board Performance Verification test on slot 2 at

7:25:38 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Starting dib_test
- Temperature at PE Ch00 is 52 deg C
- Temperature at PE Ch60 is 37 deg C
- Temperature at Incoming Air is 25 deg C
- Temperature at TG Ch00 is 45 deg C
- Performing VIH/VIL level tests... at DGS=0mV
- Performing IOH/IOL level tests...
- Performing VT level tests...
- Starting Channel Comparator Performance Verification on slot 2
- Finished Channel Comparator Performance Verification on slot 2
- Performing Clamp level tests...
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests... DGS=0mV
- Starting BPMU Performance Verification on slot 2, DGS=-218.025 mV
 - Verifying BPMU Forced Voltage Accuracy
 - Verifying BPMU Voltage Measure Accuracy
 - Verifying BPMU Forced Current Accuracy
 - Verifying BPMU Current Measure Accuracy
 - Verifying BPMU Voltage Clamping Accuracy
 - Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 2, DGS= 4.663 mV
 - Verifying BPMU Forced Voltage Accuracy
 - Verifying BPMU Voltage Measure Accuracy
 - Verifying BPMU Forced Current Accuracy
 - Verifying BPMU Current Measure Accuracy
 - Verifying BPMU Voltage Clamping Accuracy
 - Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 2, DGS= 181.974 mV
 - Verifying BPMU Forced Voltage Accuracy
 - Verifying BPMU Voltage Measure Accuracy
 - Verifying BPMU Forced Current Accuracy
 - Verifying BPMU Current Measure Accuracy

- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy

- Finished BPMU Performance Verification on slot 2

- Starting High Voltage DC Performance Verification on slot 2
- Finished High Voltage DC Performance Verification on slot 2

%JOB_END - ****PASSED**** Channel_Board Performance Verification of slot 2
(C1287A1) at 7:34:03 PM

%JOB_START - Beginning Channel_Board Performance Verification test on slot 3 at
7:34:08 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Starting dib_test
- Temperature at PE Ch00 is 53 deg C
- Temperature at PE Ch60 is 37 deg C
- Temperature at Incoming Air is 25 deg C
- Temperature at TG Ch00 is 45 deg C
- Performing VIH/VIL level tests... at DGS=0mV
- Performing IOH/IOL level tests...
- Performing VT level tests...
- Starting Channel Comparator Performance Verification on slot 3
- Finished Channel Comparator Performance Verification on slot 3
- Performing Clamp level tests...
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests... DGS=0mV
- Starting BPMU Performance Verification on slot 3, DGS=-219.355 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy

- Starting BPMU Performance Verification on slot 3, DGS= 3.372 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 3, DGS= 180.293 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Finished BPMU Performance Verification on slot 3
- Starting High Voltage DC Performance Verification on slot 3
- Finished High Voltage DC Performance Verification on slot 3

%JOB_END - ****PASSED**** Channel_Board Performance Verification of slot 3
(C00A4BD) at 7:42:34 PM

%JOB_START - Beginning Channel_Board Performance Verification test on slot 4 at
7:42:40 PM on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Starting dib_test
- Temperature at PE Ch00 is 53 deg C
- Temperature at PE Ch60 is 38 deg C
- Temperature at Incoming Air is 25 deg C
- Temperature at TG Ch00 is 45 deg C
- Performing VIH/VIL level tests... at DGS=0mV
- Performing IOH/IOL level tests...
- Performing VT level tests...
- Starting Channel Comparator Performance Verification on slot 4
- Finished Channel Comparator Performance Verification on slot 4

- Performing Clamp level tests...
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests... DGS=0mV
- Starting BPMU Performance Verification on slot 4, DGS=-216.969 mV
 - Verifying BPMU Forced Voltage Accuracy
 - Verifying BPMU Voltage Measure Accuracy
 - Verifying BPMU Forced Current Accuracy
 - Verifying BPMU Current Measure Accuracy
 - Verifying BPMU Voltage Clamping Accuracy
 - Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 4, DGS= 5.670 mV
 - Verifying BPMU Forced Voltage Accuracy
 - Verifying BPMU Voltage Measure Accuracy
 - Verifying BPMU Forced Current Accuracy
 - Verifying BPMU Current Measure Accuracy
 - Verifying BPMU Voltage Clamping Accuracy
 - Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 4, DGS= 182.688 mV
 - Verifying BPMU Forced Voltage Accuracy
 - Verifying BPMU Voltage Measure Accuracy
 - Verifying BPMU Forced Current Accuracy
 - Verifying BPMU Current Measure Accuracy
 - Verifying BPMU Voltage Clamping Accuracy
 - Verifying BPMU Current Clamping Accuracy
- Finished BPMU Performance Verification on slot 4
- Starting High Voltage DC Performance Verification on slot 4
- Finished High Voltage DC Performance Verification on slot 4

%JOB_END - ****PASSED**** Channel_Board Performance Verification of slot 4
(C00D321) at 7:51:04 PM

%JOB_START - Beginning Channel_Board Performance Verification test on slot 5 at
7:51:10 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #

23BA64 Rev 1137A

- Starting dib_test
- Temperature at PE Ch00 is 53 deg C
- Temperature at PE Ch60 is 39 deg C
- Temperature at Incoming Air is 24 deg C
- Temperature at TG Ch00 is 44 deg C
- Performing VIH/VIL level tests... at DGS=0mV
- Performing IOH/IOL level tests...
- Performing VT level tests...
- Starting Channel Comparator Performance Verification on slot 5
- Finished Channel Comparator Performance Verification on slot 5
- Performing Clamp level tests...
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests... DGS=0mV
- Starting BPMU Performance Verification on slot 5, DGS=-223.920 mV
 - Verifying BPMU Forced Voltage Accuracy
 - Verifying BPMU Voltage Measure Accuracy
 - Verifying BPMU Forced Current Accuracy
 - Verifying BPMU Current Measure Accuracy
 - Verifying BPMU Voltage Clamping Accuracy
 - Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 5, DGS=- 1.329 mV
 - Verifying BPMU Forced Voltage Accuracy
 - Verifying BPMU Voltage Measure Accuracy
 - Verifying BPMU Forced Current Accuracy
 - Verifying BPMU Current Measure Accuracy
 - Verifying BPMU Voltage Clamping Accuracy
 - Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 5, DGS= 176.344 mV
 - Verifying BPMU Forced Voltage Accuracy
 - Verifying BPMU Voltage Measure Accuracy
 - Verifying BPMU Forced Current Accuracy
 - Verifying BPMU Current Measure Accuracy
 - Verifying BPMU Voltage Clamping Accuracy
 - Verifying BPMU Current Clamping Accuracy

- Finished BPMU Performance Verification on slot 5
- Starting High Voltage DC Performance Verification on slot 5
- Finished High Voltage DC Performance Verification on slot 5

%JOB_END - ****PASSED**** Channel_Board Performance Verification of slot 5
(C003C8E) at 7:59:35 PM

%JOB_START - Beginning Channel_Board Performance Verification test on slot 6 at
7:59:40 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Starting dib_test
- Temperature at PE Ch00 is 53 deg C
- Temperature at PE Ch60 is 42 deg C
- Temperature at Incoming Air is 24 deg C
- Temperature at TG Ch00 is 44 deg C
- Performing VIH/VIL level tests... at DGS=0mV
- Performing IOH/IOL level tests...
- Performing VT level tests...
- Starting Channel Comparator Performance Verification on slot 6
- Finished Channel Comparator Performance Verification on slot 6
- Performing Clamp level tests...
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests... DGS=0mV
- Starting BPMU Performance Verification on slot 6, DGS=-215.963 mV
 - Verifying BPMU Forced Voltage Accuracy
 - Verifying BPMU Voltage Measure Accuracy
 - Verifying BPMU Forced Current Accuracy
 - Verifying BPMU Current Measure Accuracy
 - Verifying BPMU Voltage Clamping Accuracy
 - Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 6, DGS= 6.774 mV
 - Verifying BPMU Forced Voltage Accuracy

- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 6, DGS= 184.203 mV
- Verifying BPMU Forced Voltage Accuracy
- Verifying BPMU Voltage Measure Accuracy
- Verifying BPMU Forced Current Accuracy
- Verifying BPMU Current Measure Accuracy
- Verifying BPMU Voltage Clamping Accuracy
- Verifying BPMU Current Clamping Accuracy

- Finished BPMU Performance Verification on slot 6

- Starting High Voltage DC Performance Verification on slot 6
- Finished High Voltage DC Performance Verification on slot 6

%JOB_END - ****PASSED**** Channel_Board Performance Verification of slot 6
(C0DC273) at 8:08:04 PM

%JOB_START - Beginning Channel_Board Performance Verification test on slot 7 at
8:08:09 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Starting dib_test
- Temperature at PE Ch00 is 51 deg C
- Temperature at PE Ch60 is 42 deg C
- Temperature at Incoming Air is 25 deg C
- Temperature at TG Ch00 is 44 deg C
- Performing VIH/VIL level tests... at DGS=0mV
- Performing IOH/IOL level tests...
- Performing VT level tests...
- Starting Channel Comparator Performance Verification on slot 7
- Finished Channel Comparator Performance Verification on slot 7
- Performing Clamp level tests...
- Performing PPMU force voltage tests...

- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests... DGS=0mV
- Starting BPMU Performance Verification on slot 7, DGS=-222.258 mV
 - Verifying BPMU Forced Voltage Accuracy
 - Verifying BPMU Voltage Measure Accuracy
 - Verifying BPMU Forced Current Accuracy
 - Verifying BPMU Current Measure Accuracy
 - Verifying BPMU Voltage Clamping Accuracy
 - Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 7, DGS= 449.657 uV
 - Verifying BPMU Forced Voltage Accuracy
 - Verifying BPMU Voltage Measure Accuracy
 - Verifying BPMU Forced Current Accuracy
 - Verifying BPMU Current Measure Accuracy
 - Verifying BPMU Voltage Clamping Accuracy
 - Verifying BPMU Current Clamping Accuracy
- Starting BPMU Performance Verification on slot 7, DGS= 177.400 mV
 - Verifying BPMU Forced Voltage Accuracy
 - Verifying BPMU Voltage Measure Accuracy
 - Verifying BPMU Forced Current Accuracy
 - Verifying BPMU Current Measure Accuracy
 - Verifying BPMU Voltage Clamping Accuracy
 - Verifying BPMU Current Clamping Accuracy
- Finished BPMU Performance Verification on slot 7
- Starting High Voltage DC Performance Verification on slot 7
- Finished High Voltage DC Performance Verification on slot 7

%JOB_END - ****PASSED**** Channel_Board Performance Verification of slot 7
(5000F42) at 8:16:35 PM

%JOB_START - Beginning DPS_DIB Performance Verification test on slot 21 at 8:16:41
PM on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Starting DPS Performance Verification on slot 21

- Verifying DPS Voltage Accuracy
- Verifying DPS Current Limit Accuracy
- Verifying DPS Current Measure Accuracy
 - Channel 0
 - Channel 1
 - Channel 2
 - Channel 3
 - Channel 4
 - Channel 5
 - Channel 6
 - Channel 7
- Verifying DPS DIB MOUT Output Impedance Test

- Verifying DPS DIB Current Measure Output Accuracy

- Finished DPS Performance Verification on slot 21

%JOB_END - ****PASSED**** DPS_DIB Performance Verification of slot 21 (C0681E7)
at 8:17:34 PM

%JOB_START - Beginning DPS_DIB Performance Verification test on slot 22 at 8:17:39
PM on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Starting DPS Performance Verification on slot 22

- Verifying DPS Voltage Accuracy
- Verifying DPS Current Limit Accuracy
- Verifying DPS Current Measure Accuracy
 - Channel 0
 - Channel 1
 - Channel 2
 - Channel 3
 - Channel 4

- Channel 5
- Channel 6
- Channel 7
- Verifying DPS DIB MOUT Output Impedance Test

- Verifying DPS DIB Current Measure Output Accuracy

- Finished DPS Performance Verification on slot 22

%JOB_END - ****PASSED**** DPS_DIB Performance Verification of slot 22 (C06862A)
at 8:18:32 PM

%JOB_START - Beginning DPS_DIB Performance Verification test on slot 23 at 8:18:38
PM on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Starting DPS Performance Verification on slot 23

- Verifying DPS Voltage Accuracy
- Verifying DPS Current Limit Accuracy
- Verifying DPS Current Measure Accuracy
- Channel 0
- Channel 1
- Channel 2
- Channel 3
- Channel 4
- Channel 5
- Channel 6
- Channel 7
- Verifying DPS DIB MOUT Output Impedance Test

- Verifying DPS DIB Current Measure Output Accuracy

- Finished DPS Performance Verification on slot 23

%JOB_END - ****PASSED**** DPS_DIB Performance Verification of slot 23 (C0681E9)
at 8:19:31 PM

%JOB_START - Beginning DPS_DIB Performance Verification test on slot 24 at 8:19:37
PM on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Starting DPS Performance Verification on slot 24

- Verifying DPS Voltage Accuracy
- Verifying DPS Current Limit Accuracy
- Verifying DPS Current Measure Accuracy
- Channel 0
- Channel 1
- Channel 2
- Channel 3
- Channel 4
- Channel 5
- Channel 6
- Channel 7
- Verifying DPS DIB MOUT Output Impedance Test

- Verifying DPS DIB Current Measure Output Accuracy

- Finished DPS Performance Verification on slot 24

%JOB_END - ****PASSED**** DPS_DIB Performance Verification of slot 24 (C068630)
at 8:20:30 PM

%JOB_START - Beginning CTO_DIB Performance Verification test on slot 17 at 8:20:35
PM on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Performing source verification...
- Performing capture verification...

- Performing VREF verification...
- Performing PPMU Force V verification...
- Performing PPMU Measure V verification...
- Performing PPMU Force I verification...
- Performing PPMU Measure I verification...

%JOB_END - ****PASSED**** CTO_DIB Performance Verification of slot 17 (C320E91)
at 8:21:42 PM

%JOB_START - Beginning AC Performance Verification at 8:21:47 PM on 2/29/2020 in
High Accuracy Mode
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Beginning Digital Channel Timing Performance Verification
- Started at 2/29/2020 8:21:47 PM

- Building List of Digital Channels

- Checking CalDIB AC Continuity for all Digital Channels
- Calibrating Super Linear Interpolator (SLI)
- Calibrating user DIB RTD
- Loading drive deskew registers
- Loading receive deskew registers

- Beginning of Drive Skew Test
- Acquiring Data for Slot 0
- Acquiring Data for Slot 1
- Acquiring Data for Slot 2
- Acquiring Data for Slot 3
- Acquiring Data for Slot 4
- Acquiring Data for Slot 5
- Acquiring Data for Slot 6
- Acquiring Data for Slot 7
- End of Drive Skew Test

- Beginning of Receive Skew Test
- Acquiring Data for Slot 0
- Acquiring Data for Slot 1
- Acquiring Data for Slot 2
- Acquiring Data for Slot 3
- Acquiring Data for Slot 4
- Acquiring Data for Slot 5
- Acquiring Data for Slot 6
- Acquiring Data for Slot 7
- End of Receive Skew Test

- Beginning of Drive Linearity Test
- Acquiring Data for Slot 0
- Acquiring Data for Slot 1
- Acquiring Data for Slot 2
- Acquiring Data for Slot 3
- Acquiring Data for Slot 4
- Acquiring Data for Slot 5
- Acquiring Data for Slot 6
- Acquiring Data for Slot 7
- End of Drive Linearity Test

- Beginning of Receive Linearity Test
- Acquiring Data for Slot 0
- Acquiring Data for Slot 1
- Acquiring Data for Slot 2
- Acquiring Data for Slot 3
- Acquiring Data for Slot 4
- Acquiring Data for Slot 5
- Acquiring Data for Slot 6
- Acquiring Data for Slot 7
- End of Receive Linearity Test

- Adding up error budget

- Completed Digital Channel Timing Performance Verification

- Finished at 2/29/2020 9:04:17 PM

%JOB_END - ****PASSED**** AC Performance Verification at 9:04:17 PM

Quick_Module_check_PASS

%JOB_START - Beginning PCIT Quick Check test on slot 0 at 12:50:50 PM on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- PCIT CARD INFORMATION:
 - Part Number: 939-360-00
 - Serial Number: 0
 - Revision Date: A0422

%JOB_END - ****PASSED**** PCIT Quick Check of slot 0 at 12:50:51 PM

%JOB_START - Beginning CUB Quick Check test on slot 18 at 12:51:02 PM on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Starting DIB Power Tests
- Completed DIB Power Tests
- Starting System Fan Checks
- Completed System Fan Checks
- Starting PG_History_Ram
- Completed PG_History_Ram
- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Skipping PG_LVM_BIST_Ram
- Beginning Qck_Register Test
- Completed Qck_Register Test
- Starting CalCub_TG_Register Tests
- LRS Off
- LRS On
- Completed CalCub_TG_Register Tests
- Started IdProm Test

- Completed IdProm Test
- Beginning Force Voltage Test
- Completed Force Voltage Test
- Beginning TestRefToDac
- Completed TestRefToDac
- Starting the CalCubSLITest
- Completed the CalCubSLITest

%JOB_END - ****PASSED**** CUB Quick Check of slot 18 (1FC1CA) at 12:51:07 PM

%JOB_START - Beginning Channel_Board Quick Check test on slot 0 at 12:51:13 PM on
 2/29/2020
 Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
 23BA64 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting PG_History_Ram
- Completed PG_History_Ram
- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Starting PG_LVM_BIST_Ram (up to 32 sec)
- Completed PG_LVM_BIST_Ram in 21.6 sec
- LRS Off
- Starting TG Register Tests
- Completed TG Register Tests
- LRS On
- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG_Period_Ram
- Completed TG_Period_Ram
- Starting TG_Period_Map_Ram

- Completed TG_Period_Map_Ram
- Starting TG_LVM_BIST_Ram (up to 32 sec)
- Completed TG_LVM_BIST_Ram in 10.8 sec
- Starting TG_History_Ram
- Completed TG_History_Ram
- Starting TG_SVM_Ram
- Completed TG_SVM_Ram
- Starting TG_ADSS_Ram
- Completed TG_ADSS_Ram
- Starting TG_KeepAlive_Ram
- Completed TG_KeepAlive_Ram
- Starting TG_Tset_LkDwn_Ram
- Completed TG_Tset_LkDwn_Ram
- Starting TG_Edge_Ram
- Completed TG_Edge_Ram
- Starting TG_Format_Ram
- Completed TG_Format_Ram
- Starting TG_FormatLkDwn_Ram
- Completed TG_FormatLkDwn_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB_END - ****PASSED**** Channel_Board Quick Check of slot 0 (C0035DE) at
12:52:02 PM

%JOB_START - Beginning Channel_Board Quick Check test on slot 1 at 12:52:08 PM on
2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting PG_History_Ram

- Completed PG_History_Ram
- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Starting PG_LVM_BIST_Ram (up to 32 sec)
- Completed PG_LVM_BIST_Ram in 21.6 sec
- LRS Off
- Starting TG Register Tests
- Completed TG Register Tests
- LRS On
- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG_Period_Ram
- Completed TG_Period_Ram
- Starting TG_Period_Map_Ram
- Completed TG_Period_Map_Ram
- Starting TG_LVM_BIST_Ram (up to 32 sec)
- Completed TG_LVM_BIST_Ram in 10.8 sec
- Starting TG_History_Ram
- Completed TG_History_Ram
- Starting TG_SVM_Ram
- Completed TG_SVM_Ram
- Starting TG_ADSS_Ram
- Completed TG_ADSS_Ram
- Starting TG_KeepAlive_Ram
- Completed TG_KeepAlive_Ram
- Starting TG_Tset_LkDwn_Ram
- Completed TG_Tset_LkDwn_Ram
- Starting TG_Edge_Ram
- Completed TG_Edge_Ram
- Starting TG_Format_Ram
- Completed TG_Format_Ram
- Starting TG_FormatLkDwn_Ram

- Completed TG_FormatLkDwn_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB_END - ****PASSED**** Channel_Board Quick Check of slot 1 (C00533C) at
12:52:58 PM

%JOB_START - Beginning Channel_Board Quick Check test on slot 2 at 12:53:03 PM on
2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting PG_History_Ram
- Completed PG_History_Ram
- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Starting PG_LVM_BIST_Ram (up to 32 sec)
- Completed PG_LVM_BIST_Ram in 21.6 sec
- LRS Off
- Starting TG Register Tests
- Completed TG Register Tests
- LRS On
- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG_Period_Ram

- Completed TG_Period_Ram
- Starting TG_Period_Map_Ram
- Completed TG_Period_Map_Ram
- Starting TG_LVM_BIST_Ram (up to 32 sec)
- Completed TG_LVM_BIST_Ram in 10.8 sec
- Starting TG_History_Ram
- Completed TG_History_Ram
- Starting TG_SVM_Ram
- Completed TG_SVM_Ram
- Starting TG_ADSS_Ram
- Completed TG_ADSS_Ram
- Starting TG_KeepAlive_Ram
- Completed TG_KeepAlive_Ram
- Starting TG_Tset_LkDwn_Ram
- Completed TG_Tset_LkDwn_Ram
- Starting TG_Edge_Ram
- Completed TG_Edge_Ram
- Starting TG_Format_Ram
- Completed TG_Format_Ram
- Starting TG_FormatLkDwn_Ram
- Completed TG_FormatLkDwn_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB_END - ****PASSED**** Channel_Board Quick Check of slot 2 (C1287A1) at
12:53:53 PM

%JOB_START - Beginning Channel_Board Quick Check test on slot 3 at 12:53:59 PM on
2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Started IdProm Test

- Completed IdProm Test
- Starting PG_History_Ram

- Completed PG_History_Ram
- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Starting PG_LVM_BIST_Ram (up to 32 sec)
- Completed PG_LVM_BIST_Ram in 21.6 sec
- LRS Off
- Starting TG Register Tests
- Completed TG Register Tests
- LRS On
- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG_Period_Ram
- Completed TG_Period_Ram
- Starting TG_Period_Map_Ram
- Completed TG_Period_Map_Ram
- Starting TG_LVM_BIST_Ram (up to 32 sec)
- Completed TG_LVM_BIST_Ram in 10.8 sec
- Starting TG_History_Ram
- Completed TG_History_Ram
- Starting TG_SVM_Ram
- Completed TG_SVM_Ram
- Starting TG_ADSS_Ram
- Completed TG_ADSS_Ram
- Starting TG_KeepAlive_Ram
- Completed TG_KeepAlive_Ram
- Starting TG_Tset_LkDwn_Ram
- Completed TG_Tset_LkDwn_Ram
- Starting TG_Edge_Ram
- Completed TG_Edge_Ram

- Starting TG_Format_Ram
- Completed TG_Format_Ram
- Starting TG_FormatLkDwn_Ram
- Completed TG_FormatLkDwn_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB_END - ****PASSED**** Channel_Board Quick Check of slot 3 (C00A4BD) at
12:54:48 PM

%JOB_START - Beginning Channel_Board Quick Check test on slot 4 at 12:54:54 PM on
2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting PG_History_Ram
- Completed PG_History_Ram
- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Starting PG_LVM_BIST_Ram (up to 32 sec)
- Completed PG_LVM_BIST_Ram in 21.6 sec
- LRS Off
- Starting TG Register Tests
- Completed TG Register Tests
- LRS On
- Starting TG Register Tests
- Completed TG Register Tests

- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG_Period_Ram
- Completed TG_Period_Ram
- Starting TG_Period_Map_Ram
- Completed TG_Period_Map_Ram
- Starting TG_LVM_BIST_Ram (up to 32 sec)
- Completed TG_LVM_BIST_Ram in 10.8 sec
- Starting TG_History_Ram
- Completed TG_History_Ram
- Starting TG_SVM_Ram
- Completed TG_SVM_Ram
- Starting TG_ADSS_Ram
- Completed TG_ADSS_Ram
- Starting TG_KeepAlive_Ram
- Completed TG_KeepAlive_Ram
- Starting TG_Tset_LkDwn_Ram
- Completed TG_Tset_LkDwn_Ram
- Starting TG_Edge_Ram
- Completed TG_Edge_Ram
- Starting TG_Format_Ram
- Completed TG_Format_Ram
- Starting TG_FormatLkDwn_Ram
- Completed TG_FormatLkDwn_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB_END - ****PASSED**** Channel_Board Quick Check of slot 4 (C00D321) at
12:55:43 PM

%JOB_START - Beginning Channel_Board Quick Check test on slot 5 at 12:55:49 PM on
2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #

23BA64 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting PG_History_Ram
- Completed PG_History_Ram
- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Starting PG_LVM_BIST_Ram (up to 32 sec)
- Completed PG_LVM_BIST_Ram in 21.6 sec
- LRS Off
- Starting TG Register Tests
- Completed TG Register Tests
- LRS On
- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG_Period_Ram
- Completed TG_Period_Ram
- Starting TG_Period_Map_Ram
- Completed TG_Period_Map_Ram
- Starting TG_LVM_BIST_Ram (up to 32 sec)
- Completed TG_LVM_BIST_Ram in 10.8 sec
- Starting TG_History_Ram
- Completed TG_History_Ram
- Starting TG_SVM_Ram
- Completed TG_SVM_Ram
- Starting TG_ADSS_Ram
- Completed TG_ADSS_Ram
- Starting TG_KeepAlive_Ram
- Completed TG_KeepAlive_Ram
- Starting TG_Tset_LkDwn_Ram
- Completed TG_Tset_LkDwn_Ram

- Starting TG_Edge_Ram
- Completed TG_Edge_Ram
- Starting TG_Format_Ram
- Completed TG_Format_Ram
- Starting TG_FormatLkDwn_Ram
- Completed TG_FormatLkDwn_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB_END - ****PASSED**** Channel_Board Quick Check of slot 5 (C003C8E) at
12:56:39 PM

%JOB_START - Beginning Channel_Board Quick Check test on slot 6 at 12:56:44 PM on
2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting PG_History_Ram
- Completed PG_History_Ram
- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Starting PG_LVM_BIST_Ram (up to 32 sec)
- Completed PG_LVM_BIST_Ram in 21.6 sec
- LRS Off
- Starting TG Register Tests
- Completed TG Register Tests
- LRS On

- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG_Period_Ram
- Completed TG_Period_Ram
- Starting TG_Period_Map_Ram
- Completed TG_Period_Map_Ram
- Starting TG_LVM_BIST_Ram (up to 32 sec)
- Completed TG_LVM_BIST_Ram in 10.8 sec
- Starting TG_History_Ram
- Completed TG_History_Ram
- Starting TG_SVM_Ram
- Completed TG_SVM_Ram
- Starting TG_ADSS_Ram
- Completed TG_ADSS_Ram
- Starting TG_KeepAlive_Ram
- Completed TG_KeepAlive_Ram
- Starting TG_Tset_LkDwn_Ram
- Completed TG_Tset_LkDwn_Ram
- Starting TG_Edge_Ram
- Completed TG_Edge_Ram
- Starting TG_Format_Ram
- Completed TG_Format_Ram
- Starting TG_FormatLkDwn_Ram
- Completed TG_FormatLkDwn_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB_END - ****PASSED**** Channel_Board Quick Check of slot 6 (C0DC273) at
12:57:34 PM

%JOB_START - Beginning Channel_Board Quick Check test on slot 7 at 12:57:40 PM on

2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting PG_History_Ram
- Completed PG_History_Ram
- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Starting PG_LVM_BIST_Ram (up to 32 sec)
- Completed PG_LVM_BIST_Ram in 21.6 sec
- LRS Off
- Starting TG Register Tests
- Completed TG Register Tests
- LRS On
- Starting TG Register Tests
- Completed TG Register Tests
- Starting 32 bit Read Test Using ADB Register
- Completed 32 bit Read Test Using ADB Register
- Starting TG_Period_Ram
- Completed TG_Period_Ram
- Starting TG_Period_Map_Ram
- Completed TG_Period_Map_Ram
- Starting TG_LVM_BIST_Ram (up to 32 sec)
- Completed TG_LVM_BIST_Ram in 10.8 sec
- Starting TG_History_Ram
- Completed TG_History_Ram
- Starting TG_SVM_Ram
- Completed TG_SVM_Ram
- Starting TG_ADSS_Ram
- Completed TG_ADSS_Ram
- Starting TG_KeepAlive_Ram
- Completed TG_KeepAlive_Ram

- Starting TG_Tset_LkDwn_Ram
- Completed TG_Tset_LkDwn_Ram
- Starting TG_Edge_Ram
- Completed TG_Edge_Ram
- Starting TG_Format_Ram
- Completed TG_Format_Ram
- Starting TG_FormatLkDwn_Ram
- Completed TG_FormatLkDwn_Ram
- Starting DCC FPGA Registers
- Completed DCC FPGA Registers
- Starting PPMU FPGA Registers
- Completed PPMU FPGA Registers
- Started Temp Sensor Test
- Completed Temp Sensor Test
- Checker COMPLETE!

%JOB_END - ****PASSED**** Channel_Board Quick Check of slot 7 (5000F42) at
12:58:29 PM

%JOB_START - Beginning Relay_Board_Lower Quick Check test on slot 0 at 12:58:35 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 0 in Quick Mode (Cal Relay DIB Not Required)

%JOB_END - ****PASSED**** Relay_Board_Lower Quick Check of slot 0 at 12:58:36 PM

%JOB_START - Beginning Relay_Board_Lower Quick Check test on slot 1 at 12:58:41 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 1 in Quick Mode (Cal Relay DIB Not Required)

%JOB_END - ****PASSED**** Relay_Board_Lower Quick Check of slot 1 at 12:58:42 PM

%JOB_START - Beginning Relay_Board_Lower Quick Check test on slot 2 at 12:58:48 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 2 in Quick Mode (Cal Relay DIB Not
Required)

%JOB_END - ****PASSED**** Relay_Board_Lower Quick Check of slot 2 at 12:58:48 PM

%JOB_START - Beginning Relay_Board_Lower Quick Check test on slot 3 at 12:58:54 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 3 in Quick Mode (Cal Relay DIB Not
Required)

%JOB_END - ****PASSED**** Relay_Board_Lower Quick Check of slot 3 at 12:58:54 PM

%JOB_START - Beginning Relay_Board_Lower Quick Check test on slot 4 at 12:59:00 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 4 in Quick Mode (Cal Relay DIB Not
Required)

%JOB_END - ****PASSED**** Relay_Board_Lower Quick Check of slot 4 at 12:59:01 PM

%JOB_START - Beginning Relay_Board_Lower Quick Check test on slot 5 at 12:59:06 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 5 in Quick Mode (Cal Relay DIB Not
Required)

%JOB_END - ****PASSED**** Relay_Board_Lower Quick Check of slot 5 at 12:59:07 PM

%JOB_START - Beginning Relay_Board_Lower Quick Check test on slot 6 at 12:59:13 PM

on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #

23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 6 in Quick Mode (Cal Relay DIB Not Required)

%JOB_END - ****PASSED**** Relay_Board_Lower Quick Check of slot 6 at 12:59:13 PM

%JOB_START - Beginning Relay_Board_Lower Quick Check test on slot 7 at 12:59:19 PM

on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #

23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 7 in Quick Mode (Cal Relay DIB Not Required)

%JOB_END - ****PASSED**** Relay_Board_Lower Quick Check of slot 7 at 12:59:20 PM

%JOB_START - Beginning Relay_Board_Upper Quick Check test on slot 0 at 12:59:25 PM

on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #

23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 0 in Quick Mode (Cal Relay DIB Not Required)

%JOB_END - ****PASSED**** Relay_Board_Upper Quick Check of slot 0 at 12:59:26 PM

%JOB_START - Beginning Relay_Board_Upper Quick Check test on slot 1 at 12:59:32 PM

on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #

23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 1 in Quick Mode (Cal Relay DIB Not Required)

%JOB_END - ****PASSED**** Relay_Board_Upper Quick Check of slot 1 at 12:59:32 PM

%JOB_START - Beginning Relay_Board_Upper Quick Check test on slot 2 at 12:59:38 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 2 in Quick Mode (Cal Relay DIB Not Required)

%JOB_END - ****PASSED**** Relay_Board_Upper Quick Check of slot 2 at 12:59:39 PM

%JOB_START - Beginning Relay_Board_Upper Quick Check test on slot 3 at 12:59:44 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 3 in Quick Mode (Cal Relay DIB Not Required)

%JOB_END - ****PASSED**** Relay_Board_Upper Quick Check of slot 3 at 12:59:45 PM

%JOB_START - Beginning Relay_Board_Upper Quick Check test on slot 4 at 12:59:51 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 4 in Quick Mode (Cal Relay DIB Not Required)

%JOB_END - ****PASSED**** Relay_Board_Upper Quick Check of slot 4 at 12:59:51 PM

%JOB_START - Beginning Relay_Board_Upper Quick Check test on slot 5 at 12:59:57 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #

23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 5 in Quick Mode (Cal Relay DIB Not Required)

%JOB_END - ****PASSED**** Relay_Board_Upper Quick Check of slot 5 at 12:59:58 PM

%JOB_START - Beginning Relay_Board_Upper Quick Check test on slot 6 at 1:00:03 PM
on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 6 in Quick Mode (Cal Relay DIB Not Required)

%JOB_END - ****PASSED**** Relay_Board_Upper Quick Check of slot 6 at 1:00:04 PM

%JOB_START - Beginning Relay_Board_Upper Quick Check test on slot 7 at 1:00:09 PM
on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 7 in Quick Mode (Cal Relay DIB Not Required)

%JOB_END - ****PASSED**** Relay_Board_Upper Quick Check of slot 7 at 1:00:10 PM

%JOB_START - Beginning CTO Quick Check test on slot 17 at 1:00:16 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Performing register test...
- Completed register test.
- Performing idprom and temperature test...
- Completed idprom and temperature test.
- Performing PG test...
- Starting PG_History_Ram
- Completed PG_History_Ram

- Starting PG_Svm_Ram
- Completed PG_Svm_Ram
- Starting PG_Scramble_Ram, ADSS
- Completed PG_Scramble_Ram
- Starting PG_Scramble_Ram, Tset
- Completed PG_Scramble_Ram
- Starting PG_LVM_BIST_Ram (up to 32 sec)
- Completed PG_LVM_BIST_Ram in 21.6 sec
- Completed PG test.
- Performing internal loopback test...
- Completed internal loopback test.
- Performing local reference test...
- Completed local reference test.
- Performing internal Capture burst test...
- Completed internal Capture burst test.
- Performing internal loopback burst test...
- Completed internal loopback burst test.

%JOB_END - ****PASSED**** CTO Quick Check of slot 17 (C320E91) at 1:00:40 PM

%JOB_START - Beginning CTO_DIB Quick Check test on slot 17 at 1:00:46 PM on
2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Performing relay test...

%JOB_END - ****PASSED**** CTO_DIB Quick Check of slot 17 (C320E91) at 1:00:46 PM

%JOB_START - Beginning DPS Quick Check test on slot 21 at 1:00:52 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Verifying DPS Current Leakage
- Channel 0
- Channel 1
- Channel 2
- Channel 3

- Channel 4
- Channel 5
- Channel 6
- Channel 7
- Finished Verifying DPS Current Leakage

%JOB_END - ****PASSED**** DPS Quick Check of slot 21 (C0681E7) at 1:00:54 PM

%JOB_START - Beginning DPS Quick Check test on slot 22 at 1:01:00 PM on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Verifying DPS Current Leakage
- Channel 0
- Channel 1
- Channel 2
- Channel 3
- Channel 4
- Channel 5
- Channel 6
- Channel 7
- Finished Verifying DPS Current Leakage

%JOB_END - ****PASSED**** DPS Quick Check of slot 22 (C06862A) at 1:01:03 PM

%JOB_START - Beginning DPS Quick Check test on slot 23 at 1:01:08 PM on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Verifying DPS Current Leakage
- Channel 0
- Channel 1
- Channel 2
- Channel 3
- Channel 4
- Channel 5

- Channel 6
- Channel 7
- Finished Verifying DPS Current Leakage

%JOB_END - ****PASSED**** DPS Quick Check of slot 23 (C0681E9) at 1:01:11 PM

%JOB_START - Beginning DPS Quick Check test on slot 24 at 1:01:16 PM on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Verifying DPS Current Leakage
- Channel 0
- Channel 1
- Channel 2
- Channel 3
- Channel 4
- Channel 5
- Channel 6
- Channel 7
- Finished Verifying DPS Current Leakage

%JOB_END - ****PASSED**** DPS Quick Check of slot 24 (C068630) at 1:01:19 PM

%JOB_START - Beginning systemwide tests at 1:01:25 PM on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Systemwide functionality and continuity to slot 0
- Systemwide functionality and continuity to slot 1
- Systemwide functionality and continuity to slot 2
- Systemwide functionality and continuity to slot 3
- Systemwide functionality and continuity to slot 4
- Systemwide functionality and continuity to slot 5
- Systemwide functionality and continuity to slot 6
- Systemwide functionality and continuity to slot 7
- Starting BackPlane Fail Bus test

- Completed BackPlane Fail Bus test
- Completed Extra

%JOB_END - ****PASSED**** Systemwide tests at 1:02:32 PM

%JOB_START - Beginning CUB Module Check test on slot 18 at 1:02:38 PM on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Testing CalCUB and CalDIB Leakage
- Completed Cal Dib Leakage Test
- Testing CalCUB Voltage Sources on CalDIB
- Beginning Cal DIB RawV Test on Slot18
- Completed Cal DIB RawV Test on Slot 18
- Beginning Cal Dib to BPMU Test on Slot 0
- Completed Cal Dib to BPMU Test on Slot 0
- Beginning Cal Dib to EXTERN Test on Slot 0
- Completed Cal Dib to EXTERN Test on Slot 0
- Testing CalDIB Utility Bit Relays using Channel Board Utility Bits for Slot 0
- Completed Cal Dib to UTIL Test on Slot 0
- Beginning Cal Dib RF Tree & Pogo Test

- Beginning Cal Dib to BPMU Test on Slot 1
- Completed Cal Dib to BPMU Test on Slot 1
- Beginning Cal Dib to EXTERN Test on Slot 1
- Completed Cal Dib to EXTERN Test on Slot 1
- Testing CalDIB Utility Bit Relays using Channel Board Utility Bits for Slot 1
- Completed Cal Dib to UTIL Test on Slot 1
- Beginning Cal Dib RF Tree & Pogo Test

- Beginning Cal Dib to BPMU Test on Slot 2
- Completed Cal Dib to BPMU Test on Slot 2
- Beginning Cal Dib to EXTERN Test on Slot 2
- Completed Cal Dib to EXTERN Test on Slot 2
- Testing CalDIB Utility Bit Relays using Channel Board Utility Bits for Slot 2
- Completed Cal Dib to UTIL Test on Slot 2
- Beginning Cal Dib RF Tree & Pogo Test

- Beginning Cal Dib to BPMU Test on Slot 3
- Completed Cal Dib to BPMU Test on Slot 3
- Beginning Cal Dib to EXTERN Test on Slot 3
- Completed Cal Dib to EXTERN Test on Slot 3
- Testing CalDIB Utility Bit Relays using Channel Board Utility Bits for Slot 3
- Completed Cal Dib to UTIL Test on Slot 3
- Beginning Cal Dib RF Tree & Pogo Test

- Beginning Cal Dib to BPMU Test on Slot 4
- Completed Cal Dib to BPMU Test on Slot 4
- Beginning Cal Dib to EXTERN Test on Slot 4
- Completed Cal Dib to EXTERN Test on Slot 4
- Testing CalDIB Utility Bit Relays using Channel Board Utility Bits for Slot 4
- Completed Cal Dib to UTIL Test on Slot 4
- Beginning Cal Dib RF Tree & Pogo Test

- Beginning Cal Dib to BPMU Test on Slot 5
- Completed Cal Dib to BPMU Test on Slot 5
- Beginning Cal Dib to EXTERN Test on Slot 5
- Completed Cal Dib to EXTERN Test on Slot 5
- Testing CalDIB Utility Bit Relays using Channel Board Utility Bits for Slot 5
- Completed Cal Dib to UTIL Test on Slot 5
- Beginning Cal Dib RF Tree & Pogo Test

- Beginning Cal Dib to BPMU Test on Slot 6
- Completed Cal Dib to BPMU Test on Slot 6
- Beginning Cal Dib to EXTERN Test on Slot 6
- Completed Cal Dib to EXTERN Test on Slot 6
- Testing CalDIB Utility Bit Relays using Channel Board Utility Bits for Slot 6
- Completed Cal Dib to UTIL Test on Slot 6
- Beginning Cal Dib RF Tree & Pogo Test

- Beginning Cal Dib to BPMU Test on Slot 7
- Completed Cal Dib to BPMU Test on Slot 7
- Beginning Cal Dib to EXTERN Test on Slot 7
- Completed Cal Dib to EXTERN Test on Slot 7
- Testing CalDIB Utility Bit Relays using Channel Board Utility Bits for Slot 7
- Completed Cal Dib to UTIL Test on Slot 7

- Beginning Cal Dib RF Tree & Pogo Test
- Beginning Cal DIB to DPS_FSG Test on Slot 21
- Completed Cal DIB to DPS_FSG Test on Slot 21
- Beginning Cal DIB to DPS_FSG Test on Slot 22
- Completed Cal DIB to DPS_FSG Test on Slot 22
- Beginning Cal DIB to DPS_FSG Test on Slot 23
- Completed Cal DIB to DPS_FSG Test on Slot 23
- Beginning Cal DIB to DPS_FSG Test on Slot 24
- Completed Cal DIB to DPS_FSG Test on Slot 24
- Testing Device Ground Sense on CalDIB
- Completed DGS Test
- ****Completed CalDib Test****
- Start Compare Level Vol test
- Completed Compare Level Vol test
- Start Compare Level Voh test
- Completed Compare Level Voh test
- Start Drive_Level_test VIL
- Completed Drive_Level_test VIL
- Start Drive_Level_test VIH
- Completed Drive_Level_test VIH
- Beginning CalCub_Measure_Current using Bpmu in Slot 0
- Completed CalCub_Measure_Current

%JOB_END - ****PASSED**** CUB Module Check of slot 18 (1FC1CA) at 1:04:13 PM

%JOB_START - Beginning Channel_Board_DIB Module Check test on slot 0 at 1:04:18 PM

on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #

23BA64 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test
- Starting Pin PMU Checker
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...

- Performing PPMU force current tests...
- Performing PPMU measure current tests...
- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
- Completed Compare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
 - Continuing KeepAlive test
 - Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode
 - Continuing KeepAlive test
 - Continuing KeepAlive test
- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz

- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
 - Statebus : Checking STB lines : Normal mode, 30MHz
 - Statebus : Checking STB lines : Normal mode, 50MHz
 - Statebus : Checking STB lines : Normal mode, 80MHz
 - Statebus : Checking STB lines : Normal mode, 100MHz
 - Statebus : Checking State number lines : Extended mode, 25MHz
 - Statebus : Checking State number lines : Extended mode, 30MHz
 - Statebus : Checking State number lines : Extended mode, 50MHz
 - Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 25MHz
 - Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 30MHz
 - Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 35MHz
 - Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 50MHz

- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 80MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
- Started Scan Load test
- Completed Scan Load test
- Started Scan ADB test
- Completed Scan ADB test
- Completed Scan test
- Starting MultiClock test
- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel_Board_DIB test on slot 0

%JOB_END - ****PASSED**** Channel_Board_DIB Module Check of slot 0 (C0035DE) at
1:08:44 PM

%JOB_START - Beginning Channel_Board_DIB Module Check test on slot 1 at 1:08:49 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test
- Starting Pin PMU Checker
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...

- Performing PPMU force current tests...
- Performing PPMU measure current tests...
- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
- Completed Compare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
 - Continuing KeepAlive test
 - Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode
 - Continuing KeepAlive test
 - Continuing KeepAlive test
- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz

- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
 - Statebus : Checking STB lines : Normal mode, 30MHz
 - Statebus : Checking STB lines : Normal mode, 50MHz
 - Statebus : Checking STB lines : Normal mode, 80MHz
 - Statebus : Checking STB lines : Normal mode, 100MHz
 - Statebus : Checking State number lines : Extended mode, 25MHz
 - Statebus : Checking State number lines : Extended mode, 30MHz
 - Statebus : Checking State number lines : Extended mode, 50MHz
 - Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 25MHz
 - Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 30MHz
 - Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 35MHz
 - Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 50MHz

- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 80MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
- Started Scan Load test
- Completed Scan Load test
- Started Scan ADB test
- Completed Scan ADB test
- Completed Scan test
- Starting MultiClock test
- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel_Board_DIB test on slot 1

%JOB_END - ****PASSED**** Channel_Board_DIB Module Check of slot 1 (C00533C) at
1:13:14 PM

%JOB_START - Beginning Channel_Board_DIB Module Check test on slot 2 at 1:13:20 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test
- Starting Pin PMU Checker
- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...

- Performing PPMU force current tests...
- Performing PPMU measure current tests...
- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
- Completed Compare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
 - Continuing KeepAlive test
 - Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode
 - Continuing KeepAlive test
 - Continuing KeepAlive test

- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
- Statebus : Checking STB lines : Normal mode, 30MHz
- Statebus : Checking STB lines : Normal mode, 50MHz
- Statebus : Checking STB lines : Normal mode, 80MHz
- Statebus : Checking STB lines : Normal mode, 100MHz
- Statebus : Checking State number lines : Extended mode, 25MHz
- Statebus : Checking State number lines : Extended mode, 30MHz
- Statebus : Checking State number lines : Extended mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 25MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 30MHz

- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 80MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
- Started Scan Load test
- Completed Scan Load test
- Started Scan ADB test
- Completed Scan ADB test
- Completed Scan test
- Starting MultiClock test
- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel_Board_DIB test on slot 2

%JOB_END - ****PASSED**** Channel_Board_DIB Module Check of slot 2 (C1287A1) at
1:17:44 PM

%JOB_START - Beginning Channel_Board_DIB Module Check test on slot 3 at 1:17:50 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test
- Starting Pin PMU Checker

- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests...
- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
- Completed Compare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
 - Continuing KeepAlive test
 - Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode
 - Continuing KeepAlive test
 - Continuing KeepAlive test

- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
- Statebus : Checking STB lines : Normal mode, 30MHz
- Statebus : Checking STB lines : Normal mode, 50MHz
- Statebus : Checking STB lines : Normal mode, 80MHz
- Statebus : Checking STB lines : Normal mode, 100MHz
- Statebus : Checking State number lines : Extended mode, 25MHz
- Statebus : Checking State number lines : Extended mode, 30MHz
- Statebus : Checking State number lines : Extended mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 25MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 30MHz

- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 80MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
- Started Scan Load test
- Completed Scan Load test
- Started Scan ADB test
- Completed Scan ADB test
- Completed Scan test
- Starting MultiClock test
- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel_Board_DIB test on slot 3

%JOB_END - ****PASSED**** Channel_Board_DIB Module Check of slot 3 (C00A4BD) at
1:22:14 PM

%JOB_START - Beginning Channel_Board_DIB Module Check test on slot 4 at 1:22:20 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test
- Starting Pin PMU Checker

- Performing PPMU force voltage tests...
- Performing PPMU measure voltage tests...
- Performing PPMU force current tests...
- Performing PPMU measure current tests...
- Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
- Completed Compare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
 - Continuing KeepAlive test
 - Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode
 - Continuing KeepAlive test

- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
 - Statebus : Checking STB lines : Normal mode, 30MHz
 - Statebus : Checking STB lines : Normal mode, 50MHz
 - Statebus : Checking STB lines : Normal mode, 80MHz
 - Statebus : Checking STB lines : Normal mode, 100MHz
 - Statebus : Checking State number lines : Extended mode, 25MHz
 - Statebus : Checking State number lines : Extended mode, 30MHz
 - Statebus : Checking State number lines : Extended mode, 50MHz
 - Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 25MHz

- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 30MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 80MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
 - Started Scan Load test
 - Completed Scan Load test
 - Started Scan ADB test
 - Completed Scan ADB test
- Completed Scan test
- Starting MultiClock test
- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel_Board_DIB test on slot 4

%JOB_END - ****PASSED**** Channel_Board_DIB Module Check of slot 4 (C00D321) at
1:26:44 PM

%JOB_START - Beginning Channel_Board_DIB Module Check test on slot 5 at 1:26:50 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test
- Completed Board PMU test

- Starting Pin PMU Checker
 - Performing PPMU force voltage tests...
 - Performing PPMU measure voltage tests...
 - Performing PPMU force current tests...
 - Performing PPMU measure current tests...
 - Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
- Completed Compare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
 - Continuing KeepAlive test
 - Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode

- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
- Statebus : Checking STB lines : Normal mode, 30MHz
- Statebus : Checking STB lines : Normal mode, 50MHz
- Statebus : Checking STB lines : Normal mode, 80MHz
- Statebus : Checking STB lines : Normal mode, 100MHz
- Statebus : Checking State number lines : Extended mode, 25MHz
- Statebus : Checking State number lines : Extended mode, 30MHz
- Statebus : Checking State number lines : Extended mode, 50MHz

- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 25MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 30MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 80MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
- Started Scan Load test
- Completed Scan Load test
- Started Scan ADB test
- Completed Scan ADB test
- Completed Scan test
- Starting MultiClock test
- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel_Board_DIB test on slot 5

%JOB_END - ****PASSED**** Channel_Board_DIB Module Check of slot 5 (C003C8E) at
1:31:15 PM

%JOB_START - Beginning Channel_Board_DIB Module Check test on slot 6 at 1:31:20 PM
on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test

- Completed Board PMU test
- Starting Pin PMU Checker
 - Performing PPMU force voltage tests...
 - Performing PPMU measure voltage tests...
 - Performing PPMU force current tests...
 - Performing PPMU measure current tests...
 - Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
- Completed Compare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
 - Continuing KeepAlive test
 - Continuing KeepAlive test
- Completed KeepAlive test.
- Starting KeepAlive Test, Normal Mode

- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
- Statebus : Checking STB lines : Normal mode, 30MHz
- Statebus : Checking STB lines : Normal mode, 50MHz
- Statebus : Checking STB lines : Normal mode, 80MHz
- Statebus : Checking STB lines : Normal mode, 100MHz
- Statebus : Checking State number lines : Extended mode, 25MHz
- Statebus : Checking State number lines : Extended mode, 30MHz
- Statebus : Checking State number lines : Extended mode, 50MHz

- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 25MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 30MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 80MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
 - Started Scan Load test
 - Completed Scan Load test
 - Started Scan ADB test
 - Completed Scan ADB test
- Completed Scan test
- Starting MultiClock test
- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel_Board_DIB test on slot 6

%JOB_END - ****PASSED**** Channel_Board_DIB Module Check of slot 6 (C0DC273) at
1:35:45 PM

%JOB_START - Beginning Channel_Board_DIB Module Check test on slot 7 at 1:35:50 PM
on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Started IdProm Test
- Completed IdProm Test
- Starting Board PMU test

- Completed Board PMU test
- Starting Pin PMU Checker
 - Performing PPMU force voltage tests...
 - Performing PPMU measure voltage tests...
 - Performing PPMU force current tests...
 - Performing PPMU measure current tests...
 - Performing PPMU list and ram tests...
- Completed Pin PMU Checker.
- Starting Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
 - Continuing Drive Level tests
- Completed Drive Level tests
- Starting Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
 - Continuing Compare Level tests
- Completed Compare Level tests
- Starting Drive / Compare Format test, Extended Mode
- Completed Drive / Compare Format test, Extended Mode
- Starting Drive / Compare Format test, Normal Mode
- Completed Drive / Compare Format test, Normal Mode
- Starting PG Opcode test
- Completed PG Opcode test.
- Starting High Voltage test
- Completed High Voltage test
- Starting Frequency Count test
- Completed Frequency Count test.
- Starting KeepAlive Test, Extended Mode
 - Continuing KeepAlive test
 - Continuing KeepAlive test
- Completed KeepAlive test.

- Starting KeepAlive Test, Normal Mode
- Continuing KeepAlive test
- Continuing KeepAlive test
- Completed KeepAlive test.
- Starting Random Pattern test, Extended Mode, SVM, at 50 MHz
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 50 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, LVM, at 100 MHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Extended Mode, LVM, at 100 Hz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting Random Pattern test, Normal Mode, SVM, at 100 KHz
- Continuing Random Pattern test.
- Completed Random Pattern test.
- Starting DownLoad tests
- Completed DownLoad tests
- Beginning Utility Bit test
- Completed Utility Bit test
- Starting Muxed Pin test
- Completed Muxed Pin test.
- Starting State Bus test
- Statebus : Checking STB lines : Normal mode, 30MHz
- Statebus : Checking STB lines : Normal mode, 50MHz
- Statebus : Checking STB lines : Normal mode, 80MHz
- Statebus : Checking STB lines : Normal mode, 100MHz
- Statebus : Checking State number lines : Extended mode, 25MHz
- Statebus : Checking State number lines : Extended mode, 30MHz

- Statebus : Checking State number lines : Extended mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 25MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 30MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Extended mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 35MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 50MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 80MHz
- Statebus : Checking TSET, ADSS, MASK, CFAIL, FmtFAIL : Normal mode, 100MHz
- Completed State Bus test
- Starting Fail Bus test
- Completed Fail Bus test
- Starting Period Generator tests
- Completed Period Generator tests
- Starting Scan test
 - Started Scan Load test
 - Completed Scan Load test
 - Started Scan ADB test
 - Completed Scan ADB test
- Completed Scan test
- Starting MultiClock test
- Completed MultiClock test.
- Starting SCIO test
- Completed SCIO test.
- Starting Timing Edge Test
- Completed Timing Edge Test
- Completed Channel_Board_DIB test on slot 7

%JOB_END - ****PASSED**** Channel_Board_DIB Module Check of slot 7 (5000F42) at
1:40:15 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 0 at 1:40:20 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 0 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 0 at 1:40:21 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 1 at 1:40:26 PM
on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 1 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 1 at 1:40:27 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 2 at 1:40:32 PM
on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 2 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 2 at 1:40:33 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 3 at 1:40:38 PM
on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 3 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 3 at 1:40:39 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 4 at 1:40:44 PM
on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 4 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 4 at 1:40:45 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 5 at 1:40:50 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 5 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 5 at 1:40:51 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 6 at 1:40:56 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 6 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 6 at 1:40:57 PM

%JOB_START - Beginning Relay_Board_Lower Module Check test on slot 7 at 1:41:02 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG012 in Slot 7 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Lower Module Check of slot 7 at 1:41:03 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 0 at 1:41:08 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 0 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 0 at 1:41:09 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 1 at 1:41:14 PM
on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 1 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 1 at 1:41:15 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 2 at 1:41:21 PM
on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 2 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 2 at 1:41:21 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 3 at 1:41:27 PM
on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 3 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 3 at 1:41:27 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 4 at 1:41:33 PM
on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 4 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 4 at 1:41:33 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 5 at 1:41:39 PM
on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 5 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 5 at 1:41:39 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 6 at 1:41:45 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 6 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 6 at 1:41:45 PM

%JOB_START - Beginning Relay_Board_Upper Module Check test on slot 7 at 1:41:51 PM
on 2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Running Relay Checker Ver 1.03 on AG009 in Slot 7 in Full Mode

%JOB_END - ****PASSED**** Relay_Board_Upper Module Check of slot 7 at 1:41:52 PM

%JOB_START - Beginning DPS_DIB Module Check test on slot 21 at 1:41:57 PM on
2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Verifying DPS DIB MOUT Output Impedance Test

- Verifying DPS DIB Current Measure Output Test

%JOB_END - ****PASSED**** DPS_DIB Module Check of slot 21 (C0681E7) at 1:42:16 PM

%JOB_START - Beginning DPS_DIB Module Check test on slot 22 at 1:42:22 PM on
2/29/2020
Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Verifying DPS DIB MOUT Output Impedance Test
- Verifying DPS DIB Current Measure Output Test

%JOB_END - ****PASSED**** DPS_DIB Module Check of slot 22 (C06862A) at 1:42:41 PM

%JOB_START - Beginning DPS_DIB Module Check test on slot 23 at 1:42:46 PM on
2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Verifying DPS DIB MOUT Output Impedance Test
- Verifying DPS DIB Current Measure Output Test

%JOB_END - ****PASSED**** DPS_DIB Module Check of slot 23 (C0681E9) at 1:43:05 PM

%JOB_START - Beginning DPS_DIB Module Check test on slot 24 at 1:43:11 PM on
2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Verifying DPS DIB MOUT Output Impedance Test
- Verifying DPS DIB Current Measure Output Test

%JOB_END - ****PASSED**** DPS_DIB Module Check of slot 24 (C068630) at 1:43:30 PM

%JOB_START - Beginning CTO Module Check test on slot 17 at 1:43:35 PM on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Performing CTO calibration test...
- Performing RAM test...

%JOB_END - ****PASSED**** CTO Module Check of slot 17 (C320E91) at 1:43:39 PM

%JOB_START - Beginning CTO_DIB Module Check test on slot 17 at 1:43:44 PM on
2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Performing internal reference test...
- Performing VREF voltage test...
- Performing PPMU force V test...
- Performing PPMU force I test...
- Performing PPMU measure V test...
- Performing PPMU measure I test...
- Performing Source voltage test...
- Performing Capture voltage test...
- Performing Source/Capture loopback test...
- Performing Source burst test...
- Performing Capture burst test...
- Performing Source/Capture loopback burst test...

%JOB_END - ****PASSED**** CTO_DIB Module Check of slot 17 (C320E91) at 1:45:46 PM

slot17_excal

%JOB_START - Beginning CTO_DIB External Calibration test on slot 17 at 4:30:18 PM
on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

%PASS - Slot 17 channel 0 3V Source offset in mV
Measured: 0.5210 low limit: -50 high limit: 50

%PASS - Slot 17 channel 0 3V Source gain in mV
Measured: 0.9997 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 1 3V Source offset in mV
Measured: 0.2845 low limit: -50 high limit: 50

%PASS - Slot 17 channel 1 3V Source gain in mV
Measured: 0.9997 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 2 3V Source offset in mV
Measured: 9.536E-02 low limit: -50 high limit: 50

%PASS - Slot 17 channel 2 3V Source gain in mV
Measured: 0.9995 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 3 3V Source offset in mV
Measured: 0.6630 low limit: -50 high limit: 50

%PASS - Slot 17 channel 3 3V Source gain in mV
Measured: 0.9997 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 4 3V Source offset in mV
Measured: 0.7576 low limit: -50 high limit: 50

%PASS - Slot 17 channel 4 3V Source gain in mV
Measured: 0.9996 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 5 3V Source offset in mV

Measured: 0.8522 low limit: -50 high limit: 50

%PASS - Slot 17 channel 5 3V Source gain in mV

Measured: 0.9996 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 6 3V Source offset in mV

Measured: 1.372 low limit: -50 high limit: 50

%PASS - Slot 17 channel 6 3V Source gain in mV

Measured: 0.9995 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 7 3V Source offset in mV

Measured: 1.845 low limit: -50 high limit: 50

%PASS - Slot 17 channel 7 3V Source gain in mV

Measured: 0.9992 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 0 6V Source offset in mV

Measured: 1.326 low limit: -100 high limit: 100

%PASS - Slot 17 channel 0 6V Source gain in mV

Measured: 0.9998 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 1 6V Source offset in mV

Measured: 0.7583 low limit: -100 high limit: 100

%PASS - Slot 17 channel 1 6V Source gain in mV

Measured: 0.9999 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 2 6V Source offset in mV

Measured: 0.6637 low limit: -100 high limit: 100

%PASS - Slot 17 channel 2 6V Source gain in mV

Measured: 0.9998 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 3 6V Source offset in mV

Measured: 1.609 low limit: -100 high limit: 100

%PASS - Slot 17 channel 3 6V Source gain in mV
Measured: 0.9999 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 4 6V Source offset in mV
Measured: 1.042 low limit: -100 high limit: 100

%PASS - Slot 17 channel 4 6V Source gain in mV
Measured: 0.9997 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 5 6V Source offset in mV
Measured: 1.988 low limit: -100 high limit: 100

%PASS - Slot 17 channel 5 6V Source gain in mV
Measured: 0.9996 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 6 6V Source offset in mV
Measured: 2.082 low limit: -100 high limit: 100

%PASS - Slot 17 channel 6 6V Source gain in mV
Measured: 0.9997 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 7 6V Source offset in mV
Measured: 2.839 low limit: -100 high limit: 100

%PASS - Slot 17 channel 7 6V Source gain in mV
Measured: 0.9994 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 0 at 0V on 3V range
Measured: -0.0001225 low limit: -0.05 high limit: 0.05

%PASS - Slot 17 channel 0 at 0V on 3V range
Measured: -2.961E-06 low limit: -0.05 high limit: 0.05

%PASS - Slot 17 channel 0 at 3V on 3V range
Measured: 3.001 low limit: 2.95 high limit: 3.05

%PASS - Slot 17 channel 0 at 3V on 3V range
Measured: 3.000 low limit: 2.95 high limit: 3.05

%PASS - Slot 17 channel 0 at 0V on 6V range
Measured: -0.0001738 low limit: -0.1 high limit: 0.1

%PASS - Slot 17 channel 0 at 0V on 6V range
Measured: -2.517E-05 low limit: -0.1 high limit: 0.1

%PASS - Slot 17 channel 0 at 6V on 6V range
Measured: 6.001 low limit: 5.9 high limit: 6.1

%PASS - Slot 17 channel 0 at 6V on 6V range
Measured: 6.000 low limit: 5.9 high limit: 6.1

%PASS - Slot 17 channel 1 at 0V on 3V range
Measured: -0.0001109 low limit: -0.05 high limit: 0.05

%PASS - Slot 17 channel 1 at 0V on 3V range
Measured: 1.147E-05 low limit: -0.05 high limit: 0.05

%PASS - Slot 17 channel 1 at 3V on 3V range
Measured: 3.001 low limit: 2.95 high limit: 3.05

%PASS - Slot 17 channel 1 at 3V on 3V range
Measured: 3.000 low limit: 2.95 high limit: 3.05

%PASS - Slot 17 channel 1 at 0V on 6V range
Measured: -0.0001113 low limit: -0.1 high limit: 0.1

%PASS - Slot 17 channel 1 at 0V on 6V range
Measured: -8.144E-06 low limit: -0.1 high limit: 0.1

%PASS - Slot 17 channel 1 at 6V on 6V range
Measured: 6.001 low limit: 5.9 high limit: 6.1

%PASS - Slot 17 channel 1 at 6V on 6V range
Measured: 6.000 low limit: 5.9 high limit: 6.1

%PASS - Slot 17 channel 2 at 0V on 3V range

Measured: -0.0001655 low limit: -0.05 high limit: 0.05

%PASS - Slot 17 channel 2 at 0V on 3V range

Measured: -5.773E-05 low limit: -0.05 high limit: 0.05

%PASS - Slot 17 channel 2 at 3V on 3V range

Measured: 3.001 low limit: 2.95 high limit: 3.05

%PASS - Slot 17 channel 2 at 3V on 3V range

Measured: 3.000 low limit: 2.95 high limit: 3.05

%PASS - Slot 17 channel 2 at 0V on 6V range

Measured: -0.0001469 low limit: -0.1 high limit: 0.1

%PASS - Slot 17 channel 2 at 0V on 6V range

Measured: -4.959E-05 low limit: -0.1 high limit: 0.1

%PASS - Slot 17 channel 2 at 6V on 6V range

Measured: 6.001 low limit: 5.9 high limit: 6.1

%PASS - Slot 17 channel 2 at 6V on 6V range

Measured: 6.000 low limit: 5.9 high limit: 6.1

%PASS - Slot 17 channel 3 at 0V on 3V range

Measured: -0.0001543 low limit: -0.05 high limit: 0.05

%PASS - Slot 17 channel 3 at 0V on 3V range

Measured: -2.183E-05 low limit: -0.05 high limit: 0.05

%PASS - Slot 17 channel 3 at 3V on 3V range

Measured: 3.001 low limit: 2.95 high limit: 3.05

%PASS - Slot 17 channel 3 at 3V on 3V range

Measured: 3.000 low limit: 2.95 high limit: 3.05

%PASS - Slot 17 channel 3 at 0V on 6V range

Measured: -0.0002055 low limit: -0.1 high limit: 0.1

%PASS - Slot 17 channel 3 at 0V on 6V range
Measured: -1.058E-04 low limit: -0.1 high limit: 0.1

%PASS - Slot 17 channel 3 at 6V on 6V range
Measured: 6.001 low limit: 5.9 high limit: 6.1

%PASS - Slot 17 channel 3 at 6V on 6V range
Measured: 6.000 low limit: 5.9 high limit: 6.1

%PASS - Slot 17 channel 4 at 0V on 3V range
Measured: -0.0001357 low limit: -0.05 high limit: 0.05

%PASS - Slot 17 channel 4 at 0V on 3V range
Measured: -1.628E-05 low limit: -0.05 high limit: 0.05

%PASS - Slot 17 channel 4 at 3V on 3V range
Measured: 3.001 low limit: 2.95 high limit: 3.05

%PASS - Slot 17 channel 4 at 3V on 3V range
Measured: 3.000 low limit: 2.95 high limit: 3.05

%PASS - Slot 17 channel 4 at 0V on 6V range
Measured: -0.0001571 low limit: -0.1 high limit: 0.1

%PASS - Slot 17 channel 4 at 0V on 6V range
Measured: 3.701E-06 low limit: -0.1 high limit: 0.1

%PASS - Slot 17 channel 4 at 6V on 6V range
Measured: 6.001 low limit: 5.9 high limit: 6.1

%PASS - Slot 17 channel 4 at 6V on 6V range
Measured: 6.000 low limit: 5.9 high limit: 6.1

%PASS - Slot 17 channel 5 at 0V on 3V range
Measured: -0.0001185 low limit: -0.05 high limit: 0.05

%PASS - Slot 17 channel 5 at 0V on 3V range
Measured: -7.401E-07 low limit: -0.05 high limit: 0.05

%PASS - Slot 17 channel 5 at 3V on 3V range
Measured: 3.001 low limit: 2.95 high limit: 3.05

%PASS - Slot 17 channel 5 at 3V on 3V range
Measured: 3.000 low limit: 2.95 high limit: 3.05

%PASS - Slot 17 channel 5 at 0V on 6V range
Measured: -0.0001601 low limit: -0.1 high limit: 0.1

%PASS - Slot 17 channel 5 at 0V on 6V range
Measured: -8.216E-05 low limit: -0.1 high limit: 0.1

%PASS - Slot 17 channel 5 at 6V on 6V range
Measured: 6.001 low limit: 5.9 high limit: 6.1

%PASS - Slot 17 channel 5 at 6V on 6V range
Measured: 6.000 low limit: 5.9 high limit: 6.1

%PASS - Slot 17 channel 6 at 0V on 3V range
Measured: -0.0001290 low limit: -0.05 high limit: 0.05

%PASS - Slot 17 channel 6 at 0V on 3V range
Measured: -3.701E-06 low limit: -0.05 high limit: 0.05

%PASS - Slot 17 channel 6 at 3V on 3V range
Measured: 3.001 low limit: 2.95 high limit: 3.05

%PASS - Slot 17 channel 6 at 3V on 3V range
Measured: 3.000 low limit: 2.95 high limit: 3.05

%PASS - Slot 17 channel 6 at 0V on 6V range
Measured: -0.0001778 low limit: -0.1 high limit: 0.1

%PASS - Slot 17 channel 6 at 0V on 6V range
Measured: -4.960E-05 low limit: -0.1 high limit: 0.1

%PASS - Slot 17 channel 6 at 6V on 6V range

Measured: 6.001 low limit: 5.9 high limit: 6.1

%PASS - Slot 17 channel 6 at 6V on 6V range

Measured: 6.000 low limit: 5.9 high limit: 6.1

%PASS - Slot 17 channel 7 at 0V on 3V range

Measured: -0.0001608 low limit: -0.05 high limit: 0.05

%PASS - Slot 17 channel 7 at 0V on 3V range

Measured: -3.700E-05 low limit: -0.05 high limit: 0.05

%PASS - Slot 17 channel 7 at 3V on 3V range

Measured: 3.001 low limit: 2.95 high limit: 3.05

%PASS - Slot 17 channel 7 at 3V on 3V range

Measured: 3.000 low limit: 2.95 high limit: 3.05

%PASS - Slot 17 channel 7 at 0V on 6V range

Measured: -0.0001840 low limit: -0.1 high limit: 0.1

%PASS - Slot 17 channel 7 at 0V on 6V range

Measured: -9.473E-05 low limit: -0.1 high limit: 0.1

%PASS - Slot 17 channel 7 at 6V on 6V range

Measured: 6.001 low limit: 5.9 high limit: 6.1

%PASS - Slot 17 channel 7 at 6V on 6V range

Measured: 6.000 low limit: 5.9 high limit: 6.1

%JOB_END - ****PASSED**** CTO_DIB External Calibration of slot 17 (C320E91) at
4:31:14 PM

slot17_expv

%JOB_START - Beginning CTO_DIB External Verification test on slot 17 at 4:31:38 PM
on 2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Performing source and capture verification...

%PASS - Slot 17 channel 0 3V Source offset in mV

Measured: 0.3985 low limit: -50 high limit: 50

%PASS - Slot 17 channel 0 3V Source gain in mV

Measured: 1.000 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 1 3V Source offset in mV

Measured: 0.1736 low limit: -50 high limit: 50

%PASS - Slot 17 channel 1 3V Source gain in mV

Measured: 1.000 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 2 3V Source offset in mV

Measured: -7.013E-02 low limit: -50 high limit: 50

%PASS - Slot 17 channel 2 3V Source gain in mV

Measured: 1.000 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 3 3V Source offset in mV

Measured: 0.5086 low limit: -50 high limit: 50

%PASS - Slot 17 channel 3 3V Source gain in mV

Measured: 1.000 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 4 3V Source offset in mV

Measured: 0.6218 low limit: -50 high limit: 50

%PASS - Slot 17 channel 4 3V Source gain in mV

Measured: 1.000 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 5 3V Source offset in mV

Measured: 0.7336 low limit: -50 high limit: 50

%PASS - Slot 17 channel 5 3V Source gain in mV

Measured: 1.000 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 6 3V Source offset in mV

Measured: 1.243 low limit: -50 high limit: 50

%PASS - Slot 17 channel 6 3V Source gain in mV

Measured: 1.000 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 7 3V Source offset in mV

Measured: 1.637 low limit: -50 high limit: 50

%PASS - Slot 17 channel 7 3V Source gain in mV

Measured: 0.9997 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 0 6V Source offset in mV

Measured: 1.152 low limit: -100 high limit: 100

%PASS - Slot 17 channel 0 6V Source gain in mV

Measured: 1.000 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 1 6V Source offset in mV

Measured: 0.6470 low limit: -100 high limit: 100

%PASS - Slot 17 channel 1 6V Source gain in mV

Measured: 1.000 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 2 6V Source offset in mV

Measured: 0.5168 low limit: -100 high limit: 100

%PASS - Slot 17 channel 2 6V Source gain in mV

Measured: 1.000 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 3 6V Source offset in mV

Measured: 1.404 low limit: -100 high limit: 100

%PASS - Slot 17 channel 3 6V Source gain in mV

Measured: 1.000 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 4 6V Source offset in mV

Measured: 0.8850 low limit: -100 high limit: 100

%PASS - Slot 17 channel 4 6V Source gain in mV

Measured: 1.000 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 5 6V Source offset in mV

Measured: 1.828 low limit: -100 high limit: 100

%PASS - Slot 17 channel 5 6V Source gain in mV

Measured: 0.9999 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 6 6V Source offset in mV

Measured: 1.905 low limit: -100 high limit: 100

%PASS - Slot 17 channel 6 6V Source gain in mV

Measured: 1.000 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 7 6V Source offset in mV

Measured: 2.655 low limit: -100 high limit: 100

%PASS - Slot 17 channel 7 6V Source gain in mV

Measured: 0.9996 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 0 source accuracy at 0V on 3V range

Measured: 0.00001413369V low limit: -0.00018V high limit: 0.00018V

%PASS - Slot 17 channel 0 acquire accuracy at 0.0000141336939V on 3V range

Measured: 2.270799E-05V low limit: -0.0001658663V high limit: 0.0001941336V

%PASS - Slot 17 channel 0 source accuracy at 0.5V on 3V range

Measured: 0.5000042V low limit: 0.49982V high limit: 0.50018V

%PASS - Slot 17 channel 0 acquire accuracy at 0.5000042332V on 3V range

Measured: 0.5000832V low limit: 0.4998242V high limit: 0.5001842V

%PASS - Slot 17 channel 0 source accuracy at 1V on 3V range

Measured: 1.000004V low limit: 0.99982V high limit: 1.00018V

%PASS - Slot 17 channel 0 acquire accuracy at 1.000004805V on 3V range

Measured: 1.000046V low limit: 0.9998248V high limit: 1.000184V

%PASS - Slot 17 channel 0 source accuracy at 1.5V on 3V range

Measured: 1.499939V low limit: 1.49982V high limit: 1.50018V

%PASS - Slot 17 channel 0 acquire accuracy at 1.499939626V on 3V range

Measured: 1.499988V low limit: 1.499759V high limit: 1.500119V

%PASS - Slot 17 channel 0 source accuracy at 2V on 3V range

Measured: 1.999975V low limit: 1.99982V high limit: 2.00018V

%PASS - Slot 17 channel 0 acquire accuracy at 1.999975326V on 3V range

Measured: 2.000027V low limit: 1.999795V high limit: 2.000155V

%PASS - Slot 17 channel 0 source accuracy at 2.5V on 3V range

Measured: 2.499942V low limit: 2.49982V high limit: 2.50018V

%PASS - Slot 17 channel 0 acquire accuracy at 2.499942855V on 3V range

Measured: 2.499922V low limit: 2.499762V high limit: 2.500122V

%PASS - Slot 17 channel 0 source accuracy at 3V on 3V range

Measured: 2.999927V low limit: 2.99982V high limit: 3.00018V

%PASS - Slot 17 channel 0 acquire accuracy at 2.999927984V on 3V range

Measured: 2.999920V low limit: 2.999747V high limit: 3.000107V

%PASS - Slot 17 channel 0 source accuracy at 0V on 6V range

Measured: 0.00002731362V low limit: -0.00036V high limit: 0.00036V

%PASS - Slot 17 channel 0 acquire accuracy at 0.00002731362049V on 6V range

Measured: 2.653693E-05V low limit: -0.0003326863V high limit: 0.0003873136V

%PASS - Slot 17 channel 0 source accuracy at 1V on 6V range
Measured: 1.000006V low limit: 0.99964V high limit: 1.00036V

%PASS - Slot 17 channel 0 acquire accuracy at 1.000006819V on 6V range
Measured: 1.000233V low limit: 0.9996468V high limit: 1.000366V

%PASS - Slot 17 channel 0 source accuracy at 2V on 6V range
Measured: 1.999999V low limit: 1.99964V high limit: 2.00036V

%PASS - Slot 17 channel 0 acquire accuracy at 1.999999189V on 6V range
Measured: 2.000096V low limit: 1.999639V high limit: 2.000359V

%PASS - Slot 17 channel 0 source accuracy at 3V on 6V range
Measured: 2.999976V low limit: 2.99964V high limit: 3.00036V

%PASS - Slot 17 channel 0 acquire accuracy at 2.999976365V on 6V range
Measured: 3.000036V low limit: 2.999616V high limit: 3.000336V

%PASS - Slot 17 channel 0 source accuracy at 4V on 6V range
Measured: 4.000065V low limit: 3.99964V high limit: 4.00036V

%PASS - Slot 17 channel 0 acquire accuracy at 4.000065498V on 6V range
Measured: 4.000178V low limit: 3.999705V high limit: 4.000425V

%PASS - Slot 17 channel 0 source accuracy at 5V on 6V range
Measured: 5.000006V low limit: 4.99964V high limit: 5.00036V

%PASS - Slot 17 channel 0 acquire accuracy at 5.000006597V on 6V range
Measured: 4.999976V low limit: 4.999646V high limit: 5.000366V

%PASS - Slot 17 channel 0 source accuracy at 6V on 6V range
Measured: 5.999995V low limit: 5.99964V high limit: 6.00036V

%PASS - Slot 17 channel 0 acquire accuracy at 5.999995508V on 6V range
Measured: 6.000005V low limit: 5.999635V high limit: 6.000355V

%PASS - Slot 17 channel 1 source accuracy at 0V on 3V range
Measured: -0.00001346124V low limit: -0.00018V high limit: 0.00018V

%PASS - Slot 17 channel 1 acquire accuracy at -0.0000134612442V on 3V range
Measured: 3.108638E-07V low limit: -0.0001934612V high limit: 0.0001665387V

%PASS - Slot 17 channel 1 source accuracy at 0.5V on 3V range
Measured: 0.4999512V low limit: 0.49982V high limit: 0.50018V

%PASS - Slot 17 channel 1 acquire accuracy at 0.4999512245V on 3V range
Measured: 0.4999878V low limit: 0.4997712V high limit: 0.5001312V

%PASS - Slot 17 channel 1 source accuracy at 1V on 3V range
Measured: 0.9999689V low limit: 0.99982V high limit: 1.00018V

%PASS - Slot 17 channel 1 acquire accuracy at 0.9999689453V on 3V range
Measured: 1.000013V low limit: 0.9997889V high limit: 1.000148V

%PASS - Slot 17 channel 1 source accuracy at 1.5V on 3V range
Measured: 1.499927V low limit: 1.49982V high limit: 1.50018V

%PASS - Slot 17 channel 1 acquire accuracy at 1.49992706V on 3V range
Measured: 1.499980V low limit: 1.499747V high limit: 1.500107V

%PASS - Slot 17 channel 1 source accuracy at 2V on 3V range
Measured: 1.999990V low limit: 1.99982V high limit: 2.00018V

%PASS - Slot 17 channel 1 acquire accuracy at 1.999990563V on 3V range
Measured: 2.000045V low limit: 1.999810V high limit: 2.000170V

%PASS - Slot 17 channel 1 source accuracy at 2.5V on 3V range
Measured: 2.499983V low limit: 2.49982V high limit: 2.50018V

%PASS - Slot 17 channel 1 acquire accuracy at 2.499983705V on 3V range
Measured: 2.500000V low limit: 2.499803V high limit: 2.500163V

%PASS - Slot 17 channel 1 source accuracy at 3V on 3V range
Measured: 2.999925V low limit: 2.99982V high limit: 3.00018V

%PASS - Slot 17 channel 1 acquire accuracy at 2.99992562V on 3V range

Measured: 2.999964V low limit: 2.999745V high limit: 3.000105V

%PASS - Slot 17 channel 1 source accuracy at 0V on 6V range

Measured: -0.000003002179V low limit: -0.00036V high limit: 0.00036V

%PASS - Slot 17 channel 1 acquire accuracy at -0.000003002179462V on 6V range

Measured: 3.733770E-05V low limit: -0.0003630021V high limit: 0.0003569978V

%PASS - Slot 17 channel 1 source accuracy at 1V on 6V range

Measured: 0.9999114V low limit: 0.99964V high limit: 1.00036V

%PASS - Slot 17 channel 1 acquire accuracy at 0.9999114773V on 6V range

Measured: 1.000082V low limit: 0.9995514V high limit: 1.000271V

%PASS - Slot 17 channel 1 source accuracy at 2V on 6V range

Measured: 1.999955V low limit: 1.99964V high limit: 2.00036V

%PASS - Slot 17 channel 1 acquire accuracy at 1.999955142V on 6V range

Measured: 2.000108V low limit: 1.999595V high limit: 2.000315V

%PASS - Slot 17 channel 1 source accuracy at 3V on 6V range

Measured: 2.999888V low limit: 2.99964V high limit: 3.00036V

%PASS - Slot 17 channel 1 acquire accuracy at 2.999888753V on 6V range

Measured: 3.000005V low limit: 2.999528V high limit: 3.000248V

%PASS - Slot 17 channel 1 source accuracy at 4V on 6V range

Measured: 4.000031V low limit: 3.99964V high limit: 4.00036V

%PASS - Slot 17 channel 1 acquire accuracy at 4.000031347V on 6V range

Measured: 4.000162V low limit: 3.999671V high limit: 4.000391V

%PASS - Slot 17 channel 1 source accuracy at 5V on 6V range

Measured: 5.000035V low limit: 4.99964V high limit: 5.00036V

%PASS - Slot 17 channel 1 acquire accuracy at 5.000035013V on 6V range

Measured: 5.000077V low limit: 4.999675V high limit: 5.000395V

%PASS - Slot 17 channel 1 source accuracy at 6V on 6V range
Measured: 5.999966V low limit: 5.99964V high limit: 6.00036V

%PASS - Slot 17 channel 1 acquire accuracy at 5.999966961V on 6V range
Measured: 6.000003V low limit: 5.999606V high limit: 6.000326V

%PASS - Slot 17 channel 2 source accuracy at 0V on 3V range
Measured: -0.00002221446V low limit: -0.00018V high limit: 0.00018V

%PASS - Slot 17 channel 2 acquire accuracy at -0.00002221446549V on 3V range
Measured: 1.457525E-05V low limit: -0.0002022144V high limit: 0.0001577855V

%PASS - Slot 17 channel 2 source accuracy at 0.5V on 3V range
Measured: 0.4999613V low limit: 0.49982V high limit: 0.50018V

%PASS - Slot 17 channel 2 acquire accuracy at 0.4999613961V on 3V range
Measured: 0.5000441V low limit: 0.4997813V high limit: 0.5001413V

%PASS - Slot 17 channel 2 source accuracy at 1V on 3V range
Measured: 0.9999624V low limit: 0.99982V high limit: 1.00018V

%PASS - Slot 17 channel 2 acquire accuracy at 0.9999624709V on 3V range
Measured: 1.000017V low limit: 0.9997824V high limit: 1.000142V

%PASS - Slot 17 channel 2 source accuracy at 1.5V on 3V range
Measured: 1.499946V low limit: 1.49982V high limit: 1.50018V

%PASS - Slot 17 channel 2 acquire accuracy at 1.499946369V on 3V range
Measured: 1.499949V low limit: 1.499766V high limit: 1.500126V

%PASS - Slot 17 channel 2 source accuracy at 2V on 3V range
Measured: 1.999989V low limit: 1.99982V high limit: 2.00018V

%PASS - Slot 17 channel 2 acquire accuracy at 1.999989906V on 3V range
Measured: 1.999972V low limit: 1.999809V high limit: 2.000169V

%PASS - Slot 17 channel 2 source accuracy at 2.5V on 3V range
Measured: 2.499977V low limit: 2.49982V high limit: 2.50018V

%PASS - Slot 17 channel 2 acquire accuracy at 2.499977181V on 3V range
Measured: 2.499955V low limit: 2.499797V high limit: 2.500157V

%PASS - Slot 17 channel 2 source accuracy at 3V on 3V range
Measured: 2.999947V low limit: 2.99982V high limit: 3.00018V

%PASS - Slot 17 channel 2 acquire accuracy at 2.999947993V on 3V range
Measured: 2.999931V low limit: 2.999767V high limit: 3.000127V

%PASS - Slot 17 channel 2 source accuracy at 0V on 6V range
Measured: 0.00003612503V low limit: -0.00036V high limit: 0.00036V

%PASS - Slot 17 channel 2 acquire accuracy at 0.00003612503034V on 6V range
Measured: 4.986422E-05V low limit: -0.0003238749V high limit: 0.0003961250V

%PASS - Slot 17 channel 2 source accuracy at 1V on 6V range
Measured: 1.000031V low limit: 0.99964V high limit: 1.00036V

%PASS - Slot 17 channel 2 acquire accuracy at 1.000031907V on 6V range
Measured: 1.000264V low limit: 0.9996719V high limit: 1.000391V

%PASS - Slot 17 channel 2 source accuracy at 2V on 6V range
Measured: 1.999956V low limit: 1.99964V high limit: 2.00036V

%PASS - Slot 17 channel 2 acquire accuracy at 1.999956455V on 6V range
Measured: 2.000111V low limit: 1.999596V high limit: 2.000316V

%PASS - Slot 17 channel 2 source accuracy at 3V on 6V range
Measured: 2.999973V low limit: 2.99964V high limit: 3.00036V

%PASS - Slot 17 channel 2 acquire accuracy at 2.999973914V on 6V range
Measured: 3.000029V low limit: 2.999613V high limit: 3.000333V

%PASS - Slot 17 channel 2 source accuracy at 4V on 6V range
Measured: 4.000090V low limit: 3.99964V high limit: 4.00036V

%PASS - Slot 17 channel 2 acquire accuracy at 4.000090631V on 6V range

Measured: 4.000137V low limit: 3.999730V high limit: 4.000450V

%PASS - Slot 17 channel 2 source accuracy at 5V on 6V range

Measured: 5.000016V low limit: 4.99964V high limit: 5.00036V

%PASS - Slot 17 channel 2 acquire accuracy at 5.000016843V on 6V range

Measured: 4.999978V low limit: 4.999656V high limit: 5.000376V

%PASS - Slot 17 channel 2 source accuracy at 6V on 6V range

Measured: 6.000015V low limit: 5.99964V high limit: 6.00036V

%PASS - Slot 17 channel 2 acquire accuracy at 6.000015736V on 6V range

Measured: 6.000050V low limit: 5.999655V high limit: 6.000375V

%PASS - Slot 17 channel 3 source accuracy at 0V on 3V range

Measured: -0.000004925026V low limit: -0.00018V high limit: 0.00018V

%PASS - Slot 17 channel 3 acquire accuracy at -0.000004925026844V on 3V range

Measured: 1.718328E-05V low limit: -0.0001849250V high limit: 0.0001750749V

%PASS - Slot 17 channel 3 source accuracy at 0.5V on 3V range

Measured: 0.4999648V low limit: 0.49982V high limit: 0.50018V

%PASS - Slot 17 channel 3 acquire accuracy at 0.499964826V on 3V range

Measured: 0.4999901V low limit: 0.4997848V high limit: 0.5001448V

%PASS - Slot 17 channel 3 source accuracy at 1V on 3V range

Measured: 0.9999339V low limit: 0.99982V high limit: 1.00018V

%PASS - Slot 17 channel 3 acquire accuracy at 0.9999339844V on 3V range

Measured: 0.9999719V low limit: 0.9997539V high limit: 1.000113V

%PASS - Slot 17 channel 3 source accuracy at 1.5V on 3V range

Measured: 1.499907V low limit: 1.49982V high limit: 1.50018V

%PASS - Slot 17 channel 3 acquire accuracy at 1.499907313V on 3V range

Measured: 1.499924V low limit: 1.499727V high limit: 1.500087V

%PASS - Slot 17 channel 3 source accuracy at 2V on 3V range

Measured: 1.999986V low limit: 1.99982V high limit: 2.00018V

%PASS - Slot 17 channel 3 acquire accuracy at 1.999986666V on 3V range

Measured: 1.999971V low limit: 1.999806V high limit: 2.000166V

%PASS - Slot 17 channel 3 source accuracy at 2.5V on 3V range

Measured: 2.499944V low limit: 2.49982V high limit: 2.50018V

%PASS - Slot 17 channel 3 acquire accuracy at 2.499944037V on 3V range

Measured: 2.499905V low limit: 2.499764V high limit: 2.500124V

%PASS - Slot 17 channel 3 source accuracy at 3V on 3V range

Measured: 2.999902V low limit: 2.99982V high limit: 3.00018V

%PASS - Slot 17 channel 3 acquire accuracy at 2.999902633V on 3V range

Measured: 2.999887V low limit: 2.999722V high limit: 3.000082V

%PASS - Slot 17 channel 3 source accuracy at 0V on 6V range

Measured: -0.00001187965V low limit: -0.00036V high limit: 0.00036V

%PASS - Slot 17 channel 3 acquire accuracy at -0.00001187965301V on 6V range

Measured: 2.895389E-05V low limit: -0.0003718796V high limit: 0.0003481203V

%PASS - Slot 17 channel 3 source accuracy at 1V on 6V range

Measured: 0.9999585V low limit: 0.99964V high limit: 1.00036V

%PASS - Slot 17 channel 3 acquire accuracy at 0.9999585022V on 6V range

Measured: 1.000133V low limit: 0.9995985V high limit: 1.000318V

%PASS - Slot 17 channel 3 source accuracy at 2V on 6V range

Measured: 1.999918V low limit: 1.99964V high limit: 2.00036V

%PASS - Slot 17 channel 3 acquire accuracy at 1.999918976V on 6V range

Measured: 1.999982V low limit: 1.999558V high limit: 2.000278V

%PASS - Slot 17 channel 3 source accuracy at 3V on 6V range

Measured: 2.999900V low limit: 2.99964V high limit: 3.00036V

%PASS - Slot 17 channel 3 acquire accuracy at 2.999900356V on 6V range
Measured: 3.000057V low limit: 2.999540V high limit: 3.000260V

%PASS - Slot 17 channel 3 source accuracy at 4V on 6V range
Measured: 3.999986V low limit: 3.99964V high limit: 4.00036V

%PASS - Slot 17 channel 3 acquire accuracy at 3.999986731V on 6V range
Measured: 4.000013V low limit: 3.999626V high limit: 4.000346V

%PASS - Slot 17 channel 3 source accuracy at 5V on 6V range
Measured: 4.999965V low limit: 4.99964V high limit: 5.00036V

%PASS - Slot 17 channel 3 acquire accuracy at 4.999965396V on 6V range
Measured: 4.999940V low limit: 4.999605V high limit: 5.000325V

%PASS - Slot 17 channel 3 source accuracy at 6V on 6V range
Measured: 5.999932V low limit: 5.99964V high limit: 6.00036V

%PASS - Slot 17 channel 3 acquire accuracy at 5.999932152V on 6V range
Measured: 5.999920V low limit: 5.999572V high limit: 6.000292V

%PASS - Slot 17 channel 4 source accuracy at 0V on 3V range
Measured: 0.000003439249V low limit: -0.00018V high limit: 0.00018V

%PASS - Slot 17 channel 4 acquire accuracy at 0.000003439249891V on 3V range
Measured: -7.102496E-06V low limit: -0.0001765607V high limit: 0.0001834392V

%PASS - Slot 17 channel 4 source accuracy at 0.5V on 3V range
Measured: 0.4999742V low limit: 0.49982V high limit: 0.50018V

%PASS - Slot 17 channel 4 acquire accuracy at 0.4999742923V on 3V range
Measured: 0.5000778V low limit: 0.4997942V high limit: 0.5001542V

%PASS - Slot 17 channel 4 source accuracy at 1V on 3V range
Measured: 0.9999511V low limit: 0.99982V high limit: 1.00018V

%PASS - Slot 17 channel 4 acquire accuracy at 0.9999511736V on 3V range

Measured: 0.9999880V low limit: 0.9997711V high limit: 1.000131V

%PASS - Slot 17 channel 4 source accuracy at 1.5V on 3V range

Measured: 1.499949V low limit: 1.49982V high limit: 1.50018V

%PASS - Slot 17 channel 4 acquire accuracy at 1.499949302V on 3V range

Measured: 1.499939V low limit: 1.499769V high limit: 1.500129V

%PASS - Slot 17 channel 4 source accuracy at 2V on 3V range

Measured: 1.999983V low limit: 1.99982V high limit: 2.00018V

%PASS - Slot 17 channel 4 acquire accuracy at 1.999983383V on 3V range

Measured: 2.000018V low limit: 1.999803V high limit: 2.000163V

%PASS - Slot 17 channel 4 source accuracy at 2.5V on 3V range

Measured: 2.499952V low limit: 2.49982V high limit: 2.50018V

%PASS - Slot 17 channel 4 acquire accuracy at 2.499952706V on 3V range

Measured: 2.499914V low limit: 2.499772V high limit: 2.500132V

%PASS - Slot 17 channel 4 source accuracy at 3V on 3V range

Measured: 2.999912V low limit: 2.99982V high limit: 3.00018V

%PASS - Slot 17 channel 4 acquire accuracy at 2.999912047V on 3V range

Measured: 2.999907V low limit: 2.999732V high limit: 3.000092V

%PASS - Slot 17 channel 4 source accuracy at 0V on 6V range

Measured: 0.00003157757V low limit: -0.00036V high limit: 0.00036V

%PASS - Slot 17 channel 4 acquire accuracy at 0.00003157757284V on 6V range

Measured: 2.396852E-05V low limit: -0.0003284224V high limit: 0.0003915775V

%PASS - Slot 17 channel 4 source accuracy at 1V on 6V range

Measured: 0.9999579V low limit: 0.99964V high limit: 1.00036V

%PASS - Slot 17 channel 4 acquire accuracy at 0.9999579678V on 6V range

Measured: 1.000115V low limit: 0.9995979V high limit: 1.000317V

%PASS - Slot 17 channel 4 source accuracy at 2V on 6V range

Measured: 1.999902V low limit: 1.99964V high limit: 2.00036V

%PASS - Slot 17 channel 4 acquire accuracy at 1.999902863V on 6V range

Measured: 2.000017V low limit: 1.999542V high limit: 2.000262V

%PASS - Slot 17 channel 4 source accuracy at 3V on 6V range

Measured: 2.999900V low limit: 2.99964V high limit: 3.00036V

%PASS - Slot 17 channel 4 acquire accuracy at 2.999900006V on 6V range

Measured: 2.999984V low limit: 2.999540V high limit: 3.000260V

%PASS - Slot 17 channel 4 source accuracy at 4V on 6V range

Measured: 3.999969V low limit: 3.99964V high limit: 4.00036V

%PASS - Slot 17 channel 4 acquire accuracy at 3.999969567V on 6V range

Measured: 4.000010V low limit: 3.999609V high limit: 4.000329V

%PASS - Slot 17 channel 4 source accuracy at 5V on 6V range

Measured: 4.999928V low limit: 4.99964V high limit: 5.00036V

%PASS - Slot 17 channel 4 acquire accuracy at 4.999928355V on 6V range

Measured: 4.999942V low limit: 4.999568V high limit: 5.000288V

%PASS - Slot 17 channel 4 source accuracy at 6V on 6V range

Measured: 5.999889V low limit: 5.99964V high limit: 6.00036V

%PASS - Slot 17 channel 4 acquire accuracy at 5.999889769V on 6V range

Measured: 5.999933V low limit: 5.999529V high limit: 6.000249V

%PASS - Slot 17 channel 5 source accuracy at 0V on 3V range

Measured: -0.00002823019V low limit: -0.00018V high limit: 0.00018V

%PASS - Slot 17 channel 5 acquire accuracy at -0.00002823019962V on 3V range

Measured: -1.695544E-05V low limit: -0.0002082301V high limit: 0.0001517698V

%PASS - Slot 17 channel 5 source accuracy at 0.5V on 3V range

Measured: 0.4999212V low limit: 0.49982V high limit: 0.50018V

%PASS - Slot 17 channel 5 acquire accuracy at 0.499921218V on 3V range
Measured: 0.4999944V low limit: 0.4997412V high limit: 0.5001012V

%PASS - Slot 17 channel 5 source accuracy at 1V on 3V range
Measured: 0.9999313V low limit: 0.99982V high limit: 1.00018V

%PASS - Slot 17 channel 5 acquire accuracy at 0.9999313211V on 3V range
Measured: 1.000005V low limit: 0.9997513V high limit: 1.000111V

%PASS - Slot 17 channel 5 source accuracy at 1.5V on 3V range
Measured: 1.499895V low limit: 1.49982V high limit: 1.50018V

%PASS - Slot 17 channel 5 acquire accuracy at 1.499895711V on 3V range
Measured: 1.499928V low limit: 1.499715V high limit: 1.500075V

%PASS - Slot 17 channel 5 source accuracy at 2V on 3V range
Measured: 1.999954V low limit: 1.99982V high limit: 2.00018V

%PASS - Slot 17 channel 5 acquire accuracy at 1.999954441V on 3V range
Measured: 1.999975V low limit: 1.999774V high limit: 2.000134V

%PASS - Slot 17 channel 5 source accuracy at 2.5V on 3V range
Measured: 2.499954V low limit: 2.49982V high limit: 2.50018V

%PASS - Slot 17 channel 5 acquire accuracy at 2.499954939V on 3V range
Measured: 2.499957V low limit: 2.499774V high limit: 2.500134V

%PASS - Slot 17 channel 5 source accuracy at 3V on 3V range
Measured: 2.999897V low limit: 2.99982V high limit: 3.00018V

%PASS - Slot 17 channel 5 acquire accuracy at 2.99989751V on 3V range
Measured: 2.999894V low limit: 2.999717V high limit: 3.000077V

%PASS - Slot 17 channel 5 source accuracy at 0V on 6V range
Measured: 0.00002482463V low limit: -0.00036V high limit: 0.00036V

%PASS - Slot 17 channel 5 acquire accuracy at 0.00002482463784V on 6V range

Measured: 1.142341E-05V low limit: -0.0003351753V high limit: 0.0003848246V

%PASS - Slot 17 channel 5 source accuracy at 1V on 6V range

Measured: 0.9999359V low limit: 0.99964V high limit: 1.00036V

%PASS - Slot 17 channel 5 acquire accuracy at 0.9999359337V on 6V range

Measured: 1.000149V low limit: 0.9995759V high limit: 1.000295V

%PASS - Slot 17 channel 5 source accuracy at 2V on 6V range

Measured: 1.999971V low limit: 1.99964V high limit: 2.00036V

%PASS - Slot 17 channel 5 acquire accuracy at 1.999971429V on 6V range

Measured: 2.000080V low limit: 1.999611V high limit: 2.000331V

%PASS - Slot 17 channel 5 source accuracy at 3V on 6V range

Measured: 2.999921V low limit: 2.99964V high limit: 3.00036V

%PASS - Slot 17 channel 5 acquire accuracy at 2.999921548V on 6V range

Measured: 2.999958V low limit: 2.999561V high limit: 3.000281V

%PASS - Slot 17 channel 5 source accuracy at 4V on 6V range

Measured: 3.999964V low limit: 3.99964V high limit: 4.00036V

%PASS - Slot 17 channel 5 acquire accuracy at 3.999964445V on 6V range

Measured: 3.999912V low limit: 3.999604V high limit: 4.000324V

%PASS - Slot 17 channel 5 source accuracy at 5V on 6V range

Measured: 4.999990V low limit: 4.99964V high limit: 5.00036V

%PASS - Slot 17 channel 5 acquire accuracy at 4.999990879V on 6V range

Measured: 4.999950V low limit: 4.999630V high limit: 5.000350V

%PASS - Slot 17 channel 5 source accuracy at 6V on 6V range

Measured: 5.999919V low limit: 5.99964V high limit: 6.00036V

%PASS - Slot 17 channel 5 acquire accuracy at 5.999919017V on 6V range

Measured: 5.999928V low limit: 5.999559V high limit: 6.000279V

%PASS - Slot 17 channel 6 source accuracy at 0V on 3V range
Measured: 0.00001089088V low limit: -0.00018V high limit: 0.00018V

%PASS - Slot 17 channel 6 acquire accuracy at 0.00001089088507V on 3V range
Measured: 1.546583E-05V low limit: -0.0001691091V high limit: 0.0001908908V

%PASS - Slot 17 channel 6 source accuracy at 0.5V on 3V range
Measured: 0.5000105V low limit: 0.49982V high limit: 0.50018V

%PASS - Slot 17 channel 6 acquire accuracy at 0.5000105762V on 3V range
Measured: 0.5000457V low limit: 0.4998305V high limit: 0.5001905V

%PASS - Slot 17 channel 6 source accuracy at 1V on 3V range
Measured: 0.9999676V low limit: 0.99982V high limit: 1.00018V

%PASS - Slot 17 channel 6 acquire accuracy at 0.9999676881V on 3V range
Measured: 0.9999782V low limit: 0.9997876V high limit: 1.000147V

%PASS - Slot 17 channel 6 source accuracy at 1.5V on 3V range
Measured: 1.499983V low limit: 1.49982V high limit: 1.50018V

%PASS - Slot 17 channel 6 acquire accuracy at 1.499983148V on 3V range
Measured: 1.500035V low limit: 1.499803V high limit: 1.500163V

%PASS - Slot 17 channel 6 source accuracy at 2V on 3V range
Measured: 2.000037V low limit: 1.99982V high limit: 2.00018V

%PASS - Slot 17 channel 6 acquire accuracy at 2.0000375V on 3V range
Measured: 2.000062V low limit: 1.999857V high limit: 2.000217V

%PASS - Slot 17 channel 6 source accuracy at 2.5V on 3V range
Measured: 2.499984V low limit: 2.49982V high limit: 2.50018V

%PASS - Slot 17 channel 6 acquire accuracy at 2.499984449V on 3V range
Measured: 2.499944V low limit: 2.499804V high limit: 2.500164V

%PASS - Slot 17 channel 6 source accuracy at 3V on 3V range
Measured: 2.999961V low limit: 2.99982V high limit: 3.00018V

%PASS - Slot 17 channel 6 acquire accuracy at 2.999961523V on 3V range

Measured: 2.999957V low limit: 2.999781V high limit: 3.000141V

%PASS - Slot 17 channel 6 source accuracy at 0V on 6V range

Measured: 0.00001173833V low limit: -0.00036V high limit: 0.00036V

%PASS - Slot 17 channel 6 acquire accuracy at 0.00001173833794V on 6V range

Measured: 1.741128E-05V low limit: -0.0003482616V high limit: 0.0003717383V

%PASS - Slot 17 channel 6 source accuracy at 1V on 6V range

Measured: 0.9999957V low limit: 0.99964V high limit: 1.00036V

%PASS - Slot 17 channel 6 acquire accuracy at 0.999995776V on 6V range

Measured: 1.000079V low limit: 0.9996357V high limit: 1.000355V

%PASS - Slot 17 channel 6 source accuracy at 2V on 6V range

Measured: 1.999983V low limit: 1.99964V high limit: 2.00036V

%PASS - Slot 17 channel 6 acquire accuracy at 1.999983207V on 6V range

Measured: 2.000077V low limit: 1.999623V high limit: 2.000343V

%PASS - Slot 17 channel 6 source accuracy at 3V on 6V range

Measured: 2.999899V low limit: 2.99964V high limit: 3.00036V

%PASS - Slot 17 channel 6 acquire accuracy at 2.999899918V on 6V range

Measured: 3.000003V low limit: 2.999539V high limit: 3.000259V

%PASS - Slot 17 channel 6 source accuracy at 4V on 6V range

Measured: 3.999994V low limit: 3.99964V high limit: 4.00036V

%PASS - Slot 17 channel 6 acquire accuracy at 3.999994524V on 6V range

Measured: 4.000113V low limit: 3.999634V high limit: 4.000354V

%PASS - Slot 17 channel 6 source accuracy at 5V on 6V range

Measured: 4.999986V low limit: 4.99964V high limit: 5.00036V

%PASS - Slot 17 channel 6 acquire accuracy at 4.999986106V on 6V range

Measured: 4.999986V low limit: 4.999626V high limit: 5.000346V

%PASS - Slot 17 channel 6 source accuracy at 6V on 6V range

Measured: 5.999971V low limit: 5.99964V high limit: 6.00036V

%PASS - Slot 17 channel 6 acquire accuracy at 5.999971252V on 6V range

Measured: 5.999977V low limit: 5.999611V high limit: 6.000331V

%PASS - Slot 17 channel 7 source accuracy at 0V on 3V range

Measured: 0.00002667442V low limit: -0.00018V high limit: 0.00018V

%PASS - Slot 17 channel 7 acquire accuracy at 0.0000266744214V on 3V range

Measured: 3.071792E-05V low limit: -0.0001533255V high limit: 0.0002066744V

%PASS - Slot 17 channel 7 source accuracy at 0.5V on 3V range

Measured: 0.5000020V low limit: 0.49982V high limit: 0.50018V

%PASS - Slot 17 channel 7 acquire accuracy at 0.5000020342V on 3V range

Measured: 0.5000791V low limit: 0.4998220V high limit: 0.5001820V

%PASS - Slot 17 channel 7 source accuracy at 1V on 3V range

Measured: 1.000026V low limit: 0.99982V high limit: 1.00018V

%PASS - Slot 17 channel 7 acquire accuracy at 1.000026346V on 3V range

Measured: 1.000059V low limit: 0.9998463V high limit: 1.000206V

%PASS - Slot 17 channel 7 source accuracy at 1.5V on 3V range

Measured: 1.500011V low limit: 1.49982V high limit: 1.50018V

%PASS - Slot 17 channel 7 acquire accuracy at 1.500011651V on 3V range

Measured: 1.500045V low limit: 1.499831V high limit: 1.500191V

%PASS - Slot 17 channel 7 source accuracy at 2V on 3V range

Measured: 2.000028V low limit: 1.99982V high limit: 2.00018V

%PASS - Slot 17 channel 7 acquire accuracy at 2.00002848V on 3V range

Measured: 2.000064V low limit: 1.999848V high limit: 2.000208V

%PASS - Slot 17 channel 7 source accuracy at 2.5V on 3V range

Measured: 2.500033V low limit: 2.49982V high limit: 2.50018V

%PASS - Slot 17 channel 7 acquire accuracy at 2.500033444V on 3V range

Measured: 2.500030V low limit: 2.499853V high limit: 2.500213V

%PASS - Slot 17 channel 7 source accuracy at 3V on 3V range

Measured: 2.999989V low limit: 2.99982V high limit: 3.00018V

%PASS - Slot 17 channel 7 acquire accuracy at 2.999989457V on 3V range

Measured: 3.000025V low limit: 2.999809V high limit: 3.000169V

%PASS - Slot 17 channel 7 source accuracy at 0V on 6V range

Measured: 0.000001522965V low limit: -0.00036V high limit: 0.00036V

%PASS - Slot 17 channel 7 acquire accuracy at 0.000001522965128V on 6V range

Measured: 6.767950E-06V low limit: -0.0003584770V high limit: 0.0003615229V

%PASS - Slot 17 channel 7 source accuracy at 1V on 6V range

Measured: 1.000086V low limit: 0.99964V high limit: 1.00036V

%PASS - Slot 17 channel 7 acquire accuracy at 1.000086199V on 6V range

Measured: 1.000201V low limit: 0.9997261V high limit: 1.000446V

%PASS - Slot 17 channel 7 source accuracy at 2V on 6V range

Measured: 2.000075V low limit: 1.99964V high limit: 2.00036V

%PASS - Slot 17 channel 7 acquire accuracy at 2.000075723V on 6V range

Measured: 2.000189V low limit: 1.999715V high limit: 2.000435V

%PASS - Slot 17 channel 7 source accuracy at 3V on 6V range

Measured: 3.000081V low limit: 2.99964V high limit: 3.00036V

%PASS - Slot 17 channel 7 acquire accuracy at 3.000081316V on 6V range

Measured: 3.000177V low limit: 2.999721V high limit: 3.000441V

%PASS - Slot 17 channel 7 source accuracy at 4V on 6V range

Measured: 4.000160V low limit: 3.99964V high limit: 4.00036V

%PASS - Slot 17 channel 7 acquire accuracy at 4.000160116V on 6V range
Measured: 4.000159V low limit: 3.999800V high limit: 4.000520V

%PASS - Slot 17 channel 7 source accuracy at 5V on 6V range
Measured: 5.000109V low limit: 4.99964V high limit: 5.00036V

%PASS - Slot 17 channel 7 acquire accuracy at 5.000109884V on 6V range
Measured: 5.000070V low limit: 4.999749V high limit: 5.000469V

%PASS - Slot 17 channel 7 source accuracy at 6V on 6V range
Measured: 6.000171V low limit: 5.99964V high limit: 6.00036V

%PASS - Slot 17 channel 7 acquire accuracy at 6.000171258V on 6V range
Measured: 6.000200V low limit: 5.999811V high limit: 6.000531V

- Performing source linearity verification...

%PASS - Slot 17 channel 0 3V Source offset in mV
Measured: 0.3985 low limit: -50 high limit: 50

%PASS - Slot 17 channel 0 3V Source gain in mV
Measured: 1.000 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 1 3V Source offset in mV
Measured: 0.1736 low limit: -50 high limit: 50

%PASS - Slot 17 channel 1 3V Source gain in mV
Measured: 1.000 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 2 3V Source offset in mV
Measured: -0.1174 low limit: -50 high limit: 50

%PASS - Slot 17 channel 2 3V Source gain in mV
Measured: 1.000 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 3 3V Source offset in mV
Measured: 0.5086 low limit: -50 high limit: 50

%PASS - Slot 17 channel 3 3V Source gain in mV
Measured: 1.000 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 4 3V Source offset in mV
Measured: 0.6218 low limit: -50 high limit: 50

%PASS - Slot 17 channel 4 3V Source gain in mV
Measured: 1.000 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 5 3V Source offset in mV
Measured: 0.7336 low limit: -50 high limit: 50

%PASS - Slot 17 channel 5 3V Source gain in mV
Measured: 1.000 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 6 3V Source offset in mV
Measured: 1.243 low limit: -50 high limit: 50

%PASS - Slot 17 channel 6 3V Source gain in mV
Measured: 1.000 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 7 3V Source offset in mV
Measured: 1.637 low limit: -50 high limit: 50

%PASS - Slot 17 channel 7 3V Source gain in mV
Measured: 0.9997 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 0 6V Source offset in mV
Measured: 1.152 low limit: -100 high limit: 100

%PASS - Slot 17 channel 0 6V Source gain in mV
Measured: 1.000 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 1 6V Source offset in mV
Measured: 0.6470 low limit: -100 high limit: 100

%PASS - Slot 17 channel 1 6V Source gain in mV
Measured: 1.000 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 2 6V Source offset in mV

Measured: 0.5168 low limit: -100 high limit: 100

%PASS - Slot 17 channel 2 6V Source gain in mV

Measured: 1.000 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 3 6V Source offset in mV

Measured: 1.404 low limit: -100 high limit: 100

%PASS - Slot 17 channel 3 6V Source gain in mV

Measured: 1.000 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 4 6V Source offset in mV

Measured: 0.8850 low limit: -100 high limit: 100

%PASS - Slot 17 channel 4 6V Source gain in mV

Measured: 1.000 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 5 6V Source offset in mV

Measured: 1.828 low limit: -100 high limit: 100

%PASS - Slot 17 channel 5 6V Source gain in mV

Measured: 0.9999 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 6 6V Source offset in mV

Measured: 1.810 low limit: -100 high limit: 100

%PASS - Slot 17 channel 6 6V Source gain in mV

Measured: 1.000 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 7 6V Source offset in mV

Measured: 2.655 low limit: -100 high limit: 100

%PASS - Slot 17 channel 7 6V Source gain in mV

Measured: 0.9996 low limit: 0.98 high limit: 1.02

%PASS - Slot 17 channel 0 linearity at 0V on 3V range

Measured: 0.00002288166V low limit: -5.667200E-05V high limit: 1.233279E-04V

%PASS - Slot 17 channel 0 linearity at .125V on 3V range

Measured: 0.1250262V low limit: 0.1249390V high limit: 0.1251190V

%PASS - Slot 17 channel 0 linearity at .25V on 3V range

Measured: 0.2500253V low limit: 0.2499347V high limit: 0.2501147V

%PASS - Slot 17 channel 0 linearity at .375V on 3V range

Measured: 0.3750049V low limit: 0.3749304V high limit: 0.3751104V

%PASS - Slot 17 channel 0 linearity at .5V on 3V range

Measured: 0.5000160V low limit: 0.4999261V high limit: 0.5001061V

%PASS - Slot 17 channel 0 linearity at .625V on 3V range

Measured: 0.6250070V low limit: 0.6249218V high limit: 0.6251018V

%PASS - Slot 17 channel 0 linearity at .75V on 3V range

Measured: 0.7500061V low limit: 0.7499176V high limit: 0.7500976V

%PASS - Slot 17 channel 0 linearity at .875V on 3V range

Measured: 0.8750077V low limit: 0.8749133V high limit: 0.8750933V

%PASS - Slot 17 channel 0 linearity at 1V on 3V range

Measured: 1.000011V low limit: 0.9999090V high limit: 1.000089V

%PASS - Slot 17 channel 0 linearity at 1.125V on 3V range

Measured: 1.124999V low limit: 1.124904V high limit: 1.125084V

%PASS - Slot 17 channel 0 linearity at 1.25V on 3V range

Measured: 1.250008V low limit: 1.249900V high limit: 1.250080V

%PASS - Slot 17 channel 0 linearity at 1.375V on 3V range

Measured: 1.374994V low limit: 1.374896V high limit: 1.375076V

%PASS - Slot 17 channel 0 linearity at 1.5V on 3V range

Measured: 1.499943V low limit: 1.499891V high limit: 1.500071V

%PASS - Slot 17 channel 0 linearity at 1.625V on 3V range
Measured: 1.624990V low limit: 1.624887V high limit: 1.625067V

%PASS - Slot 17 channel 0 linearity at 1.75V on 3V range
Measured: 1.749991V low limit: 1.749883V high limit: 1.750063V

%PASS - Slot 17 channel 0 linearity at 1.875V on 3V range
Measured: 1.874978V low limit: 1.874879V high limit: 1.875059V

%PASS - Slot 17 channel 0 linearity at 2V on 3V range
Measured: 1.999979V low limit: 1.999874V high limit: 2.000054V

%PASS - Slot 17 channel 0 linearity at 2.125V on 3V range
Measured: 2.124968V low limit: 2.124870V high limit: 2.125050V

%PASS - Slot 17 channel 0 linearity at 2.25V on 3V range
Measured: 2.249961V low limit: 2.249866V high limit: 2.250046V

%PASS - Slot 17 channel 0 linearity at 2.375V on 3V range
Measured: 2.374939V low limit: 2.374861V high limit: 2.375041V

%PASS - Slot 17 channel 0 linearity at 2.5V on 3V range
Measured: 2.499943V low limit: 2.499857V high limit: 2.500037V

%PASS - Slot 17 channel 0 linearity at 2.625V on 3V range
Measured: 2.624942V low limit: 2.624853V high limit: 2.625033V

%PASS - Slot 17 channel 0 linearity at 2.75V on 3V range
Measured: 2.749933V low limit: 2.749849V high limit: 2.750029V

%PASS - Slot 17 channel 0 linearity at 2.875V on 3V range
Measured: 2.874921V low limit: 2.874844V high limit: 2.875024V

%PASS - Slot 17 channel 0 linearity at 3V on 3V range
Measured: 2.999923V low limit: 2.999840V high limit: 3.000020V

%PASS - Slot 17 channel 0 maximum linearity error on 3V range
Measured: 3.797417E-05V high limit: 0.00009V

%PASS - Slot 17 channel 0 linearity at 0V on 6V range
Measured: 0.00003372529V low limit: -1.353953E-04V high limit: 2.246046E-04V

%PASS - Slot 17 channel 0 linearity at .25V on 6V range
Measured: 0.2501183V low limit: 0.2498626V high limit: 0.2502226V

%PASS - Slot 17 channel 0 linearity at .5V on 6V range
Measured: 0.4999938V low limit: 0.4998606V high limit: 0.5002206V

%PASS - Slot 17 channel 0 linearity at .75V on 6V range
Measured: 0.7500391V low limit: 0.7498586V high limit: 0.7502186V

%PASS - Slot 17 channel 0 linearity at 1V on 6V range
Measured: 1.000008V low limit: 0.9998566V high limit: 1.000216V

%PASS - Slot 17 channel 0 linearity at 1.25V on 6V range
Measured: 1.250073V low limit: 1.249854V high limit: 1.250214V

%PASS - Slot 17 channel 0 linearity at 1.5V on 6V range
Measured: 1.499948V low limit: 1.499852V high limit: 1.500212V

%PASS - Slot 17 channel 0 linearity at 1.75V on 6V range
Measured: 1.750039V low limit: 1.749850V high limit: 1.750210V

%PASS - Slot 17 channel 0 linearity at 2V on 6V range
Measured: 2.000002V low limit: 1.999848V high limit: 2.000208V

%PASS - Slot 17 channel 0 linearity at 2.25V on 6V range
Measured: 2.250079V low limit: 2.249846V high limit: 2.250206V

%PASS - Slot 17 channel 0 linearity at 2.5V on 6V range
Measured: 2.499961V low limit: 2.499844V high limit: 2.500204V

%PASS - Slot 17 channel 0 linearity at 2.75V on 6V range
Measured: 2.750025V low limit: 2.749842V high limit: 2.750202V

%PASS - Slot 17 channel 0 linearity at 3V on 6V range

Measured: 2.999976V low limit: 2.999840V high limit: 3.000200V

%PASS - Slot 17 channel 0 linearity at 3.25V on 6V range

Measured: 3.250050V low limit: 3.249838V high limit: 3.250198V

%PASS - Slot 17 channel 0 linearity at 3.5V on 6V range

Measured: 3.500127V low limit: 3.499836V high limit: 3.500196V

%PASS - Slot 17 channel 0 linearity at 3.75V on 6V range

Measured: 3.749982V low limit: 3.749834V high limit: 3.750194V

%PASS - Slot 17 channel 0 linearity at 4V on 6V range

Measured: 4.000065V low limit: 3.999832V high limit: 4.000192V

%PASS - Slot 17 channel 0 linearity at 4.25V on 6V range

Measured: 4.250034V low limit: 4.249830V high limit: 4.250190V

%PASS - Slot 17 channel 0 linearity at 4.5V on 6V range

Measured: 4.500070V low limit: 4.499828V high limit: 4.500188V

%PASS - Slot 17 channel 0 linearity at 4.75V on 6V range

Measured: 4.749939V low limit: 4.749826V high limit: 4.750186V

%PASS - Slot 17 channel 0 linearity at 5V on 6V range

Measured: 5.000005V low limit: 4.999824V high limit: 5.000184V

%PASS - Slot 17 channel 0 linearity at 5.25V on 6V range

Measured: 5.249992V low limit: 5.249822V high limit: 5.250182V

%PASS - Slot 17 channel 0 linearity at 5.5V on 6V range

Measured: 5.500041V low limit: 5.499820V high limit: 5.500180V

%PASS - Slot 17 channel 0 linearity at 5.75V on 6V range

Measured: 5.749912V low limit: 5.749818V high limit: 5.750178V

%PASS - Slot 17 channel 0 linearity at 6V on 6V range

Measured: 5.999997V low limit: 5.999816V high limit: 6.000176V

%PASS - Slot 17 channel 0 maximum linearity error on 6V range

Measured: 1.104756E-04V high limit: 0.00018V

%PASS - Slot 17 channel 1 linearity at 0V on 3V range

Measured: -0.000007483135V low limit: -8.847915E-05V high limit: 9.152084E-05V

%PASS - Slot 17 channel 1 linearity at .125V on 3V range

Measured: 0.1249965V low limit: 0.1249093V high limit: 0.1250893V

%PASS - Slot 17 channel 1 linearity at .25V on 3V range

Measured: 0.2499984V low limit: 0.2499072V high limit: 0.2500872V

%PASS - Slot 17 channel 1 linearity at .375V on 3V range

Measured: 0.3749976V low limit: 0.3749051V high limit: 0.3750851V

%PASS - Slot 17 channel 1 linearity at .5V on 3V range

Measured: 0.4999627V low limit: 0.4999029V high limit: 0.5000829V

%PASS - Slot 17 channel 1 linearity at .625V on 3V range

Measured: 0.6249984V low limit: 0.6249008V high limit: 0.6250808V

%PASS - Slot 17 channel 1 linearity at .75V on 3V range

Measured: 0.7499660V low limit: 0.7498987V high limit: 0.7500787V

%PASS - Slot 17 channel 1 linearity at .875V on 3V range

Measured: 0.8750193V low limit: 0.8748965V high limit: 0.8750765V

%PASS - Slot 17 channel 1 linearity at 1V on 3V range

Measured: 0.9999775V low limit: 0.9998944V high limit: 1.000074V

%PASS - Slot 17 channel 1 linearity at 1.125V on 3V range

Measured: 1.124973V low limit: 1.124892V high limit: 1.125072V

%PASS - Slot 17 channel 1 linearity at 1.25V on 3V range

Measured: 1.249989V low limit: 1.249890V high limit: 1.250070V

%PASS - Slot 17 channel 1 linearity at 1.375V on 3V range

Measured: 1.374990V low limit: 1.374888V high limit: 1.375068V

%PASS - Slot 17 channel 1 linearity at 1.5V on 3V range
Measured: 1.499934V low limit: 1.499885V high limit: 1.500065V

%PASS - Slot 17 channel 1 linearity at 1.625V on 3V range
Measured: 1.624998V low limit: 1.624883V high limit: 1.625063V

%PASS - Slot 17 channel 1 linearity at 1.75V on 3V range
Measured: 1.749996V low limit: 1.749881V high limit: 1.750061V

%PASS - Slot 17 channel 1 linearity at 1.875V on 3V range
Measured: 1.874997V low limit: 1.874879V high limit: 1.875059V

%PASS - Slot 17 channel 1 linearity at 2V on 3V range
Measured: 1.999995V low limit: 1.999877V high limit: 2.000057V

%PASS - Slot 17 channel 1 linearity at 2.125V on 3V range
Measured: 2.124943V low limit: 2.124875V high limit: 2.125055V

%PASS - Slot 17 channel 1 linearity at 2.25V on 3V range
Measured: 2.249992V low limit: 2.249873V high limit: 2.250053V

%PASS - Slot 17 channel 1 linearity at 2.375V on 3V range
Measured: 2.374941V low limit: 2.374870V high limit: 2.375050V

%PASS - Slot 17 channel 1 linearity at 2.5V on 3V range
Measured: 2.499988V low limit: 2.499868V high limit: 2.500048V

%PASS - Slot 17 channel 1 linearity at 2.625V on 3V range
Measured: 2.624948V low limit: 2.624866V high limit: 2.625046V

%PASS - Slot 17 channel 1 linearity at 2.75V on 3V range
Measured: 2.749932V low limit: 2.749864V high limit: 2.750044V

%PASS - Slot 17 channel 1 linearity at 2.875V on 3V range
Measured: 2.874934V low limit: 2.874862V high limit: 2.875042V

%PASS - Slot 17 channel 1 linearity at 3V on 3V range

Measured: 2.999930V low limit: 2.999860V high limit: 3.000040V

%PASS - Slot 17 channel 1 maximum linearity error on 3V range

Measured: 4.126716E-05V high limit: 0.00009V

%PASS - Slot 17 channel 1 linearity at 0V on 6V range

Measured: 0.000001658155V low limit: -1.940538E-04V high limit: 1.659461E-04V

%PASS - Slot 17 channel 1 linearity at .25V on 6V range

Measured: 0.2500150V low limit: 0.2498054V high limit: 0.2501654V

%PASS - Slot 17 channel 1 linearity at .5V on 6V range

Measured: 0.4999977V low limit: 0.4998050V high limit: 0.5001650V

%PASS - Slot 17 channel 1 linearity at .75V on 6V range

Measured: 0.7499988V low limit: 0.7498045V high limit: 0.7501645V

%PASS - Slot 17 channel 1 linearity at 1V on 6V range

Measured: 0.9999044V low limit: 0.9998041V high limit: 1.000164V

%PASS - Slot 17 channel 1 linearity at 1.25V on 6V range

Measured: 1.250008V low limit: 1.249803V high limit: 1.250163V

%PASS - Slot 17 channel 1 linearity at 1.5V on 6V range

Measured: 1.499917V low limit: 1.499803V high limit: 1.500163V

%PASS - Slot 17 channel 1 linearity at 1.75V on 6V range

Measured: 1.750051V low limit: 1.749802V high limit: 1.750162V

%PASS - Slot 17 channel 1 linearity at 2V on 6V range

Measured: 1.999956V low limit: 1.999802V high limit: 2.000162V

%PASS - Slot 17 channel 1 linearity at 2.25V on 6V range

Measured: 2.249954V low limit: 2.249801V high limit: 2.250161V

%PASS - Slot 17 channel 1 linearity at 2.5V on 6V range

Measured: 2.499987V low limit: 2.499801V high limit: 2.500161V

%PASS - Slot 17 channel 1 linearity at 2.75V on 6V range
Measured: 2.749961V low limit: 2.749800V high limit: 2.750160V

%PASS - Slot 17 channel 1 linearity at 3V on 6V range
Measured: 2.999892V low limit: 2.999800V high limit: 3.000160V

%PASS - Slot 17 channel 1 linearity at 3.25V on 6V range
Measured: 3.250011V low limit: 3.249799V high limit: 3.250159V

%PASS - Slot 17 channel 1 linearity at 3.5V on 6V range
Measured: 3.500020V low limit: 3.499799V high limit: 3.500159V

%PASS - Slot 17 channel 1 linearity at 3.75V on 6V range
Measured: 3.750020V low limit: 3.749799V high limit: 3.750159V

%PASS - Slot 17 channel 1 linearity at 4V on 6V range
Measured: 4.000032V low limit: 3.999798V high limit: 4.000158V

%PASS - Slot 17 channel 1 linearity at 4.25V on 6V range
Measured: 4.249928V low limit: 4.249798V high limit: 4.250158V

%PASS - Slot 17 channel 1 linearity at 4.5V on 6V range
Measured: 4.500039V low limit: 4.499797V high limit: 4.500157V

%PASS - Slot 17 channel 1 linearity at 4.75V on 6V range
Measured: 4.749942V low limit: 4.749797V high limit: 4.750157V

%PASS - Slot 17 channel 1 linearity at 5V on 6V range
Measured: 5.000032V low limit: 4.999796V high limit: 5.000156V

%PASS - Slot 17 channel 1 linearity at 5.25V on 6V range
Measured: 5.249968V low limit: 5.249796V high limit: 5.250156V

%PASS - Slot 17 channel 1 linearity at 5.5V on 6V range
Measured: 5.499952V low limit: 5.499795V high limit: 5.500155V

%PASS - Slot 17 channel 1 linearity at 5.75V on 6V range
Measured: 5.749955V low limit: 5.749795V high limit: 5.750155V

%PASS - Slot 17 channel 1 linearity at 6V on 6V range

Measured: 5.999958V low limit: 5.999794V high limit: 6.000154V

%PASS - Slot 17 channel 1 maximum linearity error on 6V range

Measured: 8.810773E-05V high limit: 0.00018V

%PASS - Slot 17 channel 2 linearity at 0V on 3V range

Measured: 0.00002450613V low limit: -6.093214E-05V high limit: 1.190678E-04V

%PASS - Slot 17 channel 2 linearity at .125V on 3V range

Measured: 0.1250606V low limit: 0.1249364V high limit: 0.1251164V

%PASS - Slot 17 channel 2 linearity at .25V on 3V range

Measured: 0.2499967V low limit: 0.2499338V high limit: 0.2501138V

%PASS - Slot 17 channel 2 linearity at .375V on 3V range

Measured: 0.3750322V low limit: 0.3749312V high limit: 0.3751112V

%PASS - Slot 17 channel 2 linearity at .5V on 3V range

Measured: 0.5000187V low limit: 0.4999286V high limit: 0.5001086V

%PASS - Slot 17 channel 2 linearity at .625V on 3V range

Measured: 0.6250012V low limit: 0.6249260V high limit: 0.6251060V

%PASS - Slot 17 channel 2 linearity at .75V on 3V range

Measured: 0.7499855V low limit: 0.7499234V high limit: 0.7501034V

%PASS - Slot 17 channel 2 linearity at .875V on 3V range

Measured: 0.8750215V low limit: 0.8749208V high limit: 0.8751008V

%PASS - Slot 17 channel 2 linearity at 1V on 3V range

Measured: 1.000014V low limit: 0.9999182V high limit: 1.000098V

%PASS - Slot 17 channel 2 linearity at 1.125V on 3V range

Measured: 1.124993V low limit: 1.124915V high limit: 1.125095V

%PASS - Slot 17 channel 2 linearity at 1.25V on 3V range

Measured: 1.249987V low limit: 1.249913V high limit: 1.250093V

%PASS - Slot 17 channel 2 linearity at 1.375V on 3V range

Measured: 1.375030V low limit: 1.374910V high limit: 1.375090V

%PASS - Slot 17 channel 2 linearity at 1.5V on 3V range

Measured: 1.499953V low limit: 1.499907V high limit: 1.500087V

%PASS - Slot 17 channel 2 linearity at 1.625V on 3V range

Measured: 1.624988V low limit: 1.624905V high limit: 1.625085V

%PASS - Slot 17 channel 2 linearity at 1.75V on 3V range

Measured: 1.750025V low limit: 1.749902V high limit: 1.750082V

%PASS - Slot 17 channel 2 linearity at 1.875V on 3V range

Measured: 1.875009V low limit: 1.874900V high limit: 1.875080V

%PASS - Slot 17 channel 2 linearity at 2V on 3V range

Measured: 1.999992V low limit: 1.999897V high limit: 2.000077V

%PASS - Slot 17 channel 2 linearity at 2.125V on 3V range

Measured: 2.124983V low limit: 2.124894V high limit: 2.125074V

%PASS - Slot 17 channel 2 linearity at 2.25V on 3V range

Measured: 2.250018V low limit: 2.249892V high limit: 2.250072V

%PASS - Slot 17 channel 2 linearity at 2.375V on 3V range

Measured: 2.374987V low limit: 2.374889V high limit: 2.375069V

%PASS - Slot 17 channel 2 linearity at 2.5V on 3V range

Measured: 2.499981V low limit: 2.499887V high limit: 2.500067V

%PASS - Slot 17 channel 2 linearity at 2.625V on 3V range

Measured: 2.624967V low limit: 2.624884V high limit: 2.625064V

%PASS - Slot 17 channel 2 linearity at 2.75V on 3V range

Measured: 2.749992V low limit: 2.749881V high limit: 2.750061V

%PASS - Slot 17 channel 2 linearity at 2.875V on 3V range
Measured: 2.874924V low limit: 2.874879V high limit: 2.875059V

%PASS - Slot 17 channel 2 linearity at 3V on 3V range
Measured: 2.999951V low limit: 2.999876V high limit: 3.000056V

%PASS - Slot 17 channel 2 maximum linearity error on 3V range
Measured: 4.454808E-05V high limit: 0.00009V

%PASS - Slot 17 channel 2 linearity at 0V on 6V range
Measured: 0.00003355817V low limit: -1.508304E-04V high limit: 2.091695E-04V

%PASS - Slot 17 channel 2 linearity at .25V on 6V range
Measured: 0.2500890V low limit: 0.2498483V high limit: 0.2502083V

%PASS - Slot 17 channel 2 linearity at .5V on 6V range
Measured: 0.4999816V low limit: 0.4998475V high limit: 0.5002075V

%PASS - Slot 17 channel 2 linearity at .75V on 6V range
Measured: 0.7500486V low limit: 0.7498467V high limit: 0.7502067V

%PASS - Slot 17 channel 2 linearity at 1V on 6V range
Measured: 1.000032V low limit: 0.9998459V high limit: 1.000205V

%PASS - Slot 17 channel 2 linearity at 1.25V on 6V range
Measured: 1.250004V low limit: 1.249845V high limit: 1.250205V

%PASS - Slot 17 channel 2 linearity at 1.5V on 6V range
Measured: 1.499979V low limit: 1.499844V high limit: 1.500204V

%PASS - Slot 17 channel 2 linearity at 1.75V on 6V range
Measured: 1.750067V low limit: 1.749843V high limit: 1.750203V

%PASS - Slot 17 channel 2 linearity at 2V on 6V range
Measured: 1.999956V low limit: 1.999842V high limit: 2.000202V

%PASS - Slot 17 channel 2 linearity at 2.25V on 6V range
Measured: 2.250021V low limit: 2.249841V high limit: 2.250201V

%PASS - Slot 17 channel 2 linearity at 2.5V on 6V range
Measured: 2.500018V low limit: 2.499841V high limit: 2.500201V

%PASS - Slot 17 channel 2 linearity at 2.75V on 6V range
Measured: 2.750015V low limit: 2.749840V high limit: 2.750200V

%PASS - Slot 17 channel 2 linearity at 3V on 6V range
Measured: 2.999972V low limit: 2.999839V high limit: 3.000199V

%PASS - Slot 17 channel 2 linearity at 3.25V on 6V range
Measured: 3.250045V low limit: 3.249838V high limit: 3.250198V

%PASS - Slot 17 channel 2 linearity at 3.5V on 6V range
Measured: 3.500038V low limit: 3.499837V high limit: 3.500197V

%PASS - Slot 17 channel 2 linearity at 3.75V on 6V range
Measured: 3.750008V low limit: 3.749837V high limit: 3.750197V

%PASS - Slot 17 channel 2 linearity at 4V on 6V range
Measured: 4.000088V low limit: 3.999836V high limit: 4.000196V

%PASS - Slot 17 channel 2 linearity at 4.25V on 6V range
Measured: 4.249983V low limit: 4.249835V high limit: 4.250195V

%PASS - Slot 17 channel 2 linearity at 4.5V on 6V range
Measured: 4.500063V low limit: 4.499834V high limit: 4.500194V

%PASS - Slot 17 channel 2 linearity at 4.75V on 6V range
Measured: 4.750025V low limit: 4.749833V high limit: 4.750193V

%PASS - Slot 17 channel 2 linearity at 5V on 6V range
Measured: 5.000013V low limit: 4.999833V high limit: 5.000193V

%PASS - Slot 17 channel 2 linearity at 5.25V on 6V range
Measured: 5.250001V low limit: 5.249832V high limit: 5.250192V

%PASS - Slot 17 channel 2 linearity at 5.5V on 6V range

Measured: 5.500056V low limit: 5.499831V high limit: 5.500191V

%PASS - Slot 17 channel 2 linearity at 5.75V on 6V range

Measured: 5.749932V low limit: 5.749830V high limit: 5.750190V

%PASS - Slot 17 channel 2 linearity at 6V on 6V range

Measured: 6.000009V low limit: 5.999829V high limit: 6.000189V

%PASS - Slot 17 channel 2 maximum linearity error on 6V range

Measured: 7.844203E-05V high limit: 0.00018V

%PASS - Slot 17 channel 3 linearity at 0V on 3V range

Measured: 0.000003746817V low limit: -9.371412E-05V high limit: 8.628587E-05V

%PASS - Slot 17 channel 3 linearity at .125V on 3V range

Measured: 0.1250096V low limit: 0.1249031V high limit: 0.1250831V

%PASS - Slot 17 channel 3 linearity at .25V on 3V range

Measured: 0.2499633V low limit: 0.2499000V high limit: 0.2500800V

%PASS - Slot 17 channel 3 linearity at .375V on 3V range

Measured: 0.3750066V low limit: 0.3748969V high limit: 0.3750769V

%PASS - Slot 17 channel 3 linearity at .5V on 3V range

Measured: 0.4999755V low limit: 0.4998938V high limit: 0.5000738V

%PASS - Slot 17 channel 3 linearity at .625V on 3V range

Measured: 0.6249682V low limit: 0.6248907V high limit: 0.6250707V

%PASS - Slot 17 channel 3 linearity at .75V on 3V range

Measured: 0.7499685V low limit: 0.7498876V high limit: 0.7500676V

%PASS - Slot 17 channel 3 linearity at .875V on 3V range

Measured: 0.8749908V low limit: 0.8748845V high limit: 0.8750645V

%PASS - Slot 17 channel 3 linearity at 1V on 3V range

Measured: 0.9999433V low limit: 0.9998814V high limit: 1.000061V

%PASS - Slot 17 channel 3 linearity at 1.125V on 3V range
Measured: 1.124989V low limit: 1.124878V high limit: 1.125058V

%PASS - Slot 17 channel 3 linearity at 1.25V on 3V range
Measured: 1.249952V low limit: 1.249875V high limit: 1.250055V

%PASS - Slot 17 channel 3 linearity at 1.375V on 3V range
Measured: 1.374965V low limit: 1.374872V high limit: 1.375052V

%PASS - Slot 17 channel 3 linearity at 1.5V on 3V range
Measured: 1.499914V low limit: 1.499869V high limit: 1.500049V

%PASS - Slot 17 channel 3 linearity at 1.625V on 3V range
Measured: 1.624969V low limit: 1.624865V high limit: 1.625045V

%PASS - Slot 17 channel 3 linearity at 1.75V on 3V range
Measured: 1.749980V low limit: 1.749862V high limit: 1.750042V

%PASS - Slot 17 channel 3 linearity at 1.875V on 3V range
Measured: 1.874935V low limit: 1.874859V high limit: 1.875039V

%PASS - Slot 17 channel 3 linearity at 2V on 3V range
Measured: 1.999990V low limit: 1.999856V high limit: 2.000036V

%PASS - Slot 17 channel 3 linearity at 2.125V on 3V range
Measured: 2.124934V low limit: 2.124853V high limit: 2.125033V

%PASS - Slot 17 channel 3 linearity at 2.25V on 3V range
Measured: 2.249951V low limit: 2.249850V high limit: 2.250030V

%PASS - Slot 17 channel 3 linearity at 2.375V on 3V range
Measured: 2.374948V low limit: 2.374847V high limit: 2.375027V

%PASS - Slot 17 channel 3 linearity at 2.5V on 3V range
Measured: 2.499946V low limit: 2.499844V high limit: 2.500024V

%PASS - Slot 17 channel 3 linearity at 2.625V on 3V range
Measured: 2.624916V low limit: 2.624841V high limit: 2.625021V

%PASS - Slot 17 channel 3 linearity at 2.75V on 3V range
Measured: 2.749943V low limit: 2.749837V high limit: 2.750017V

%PASS - Slot 17 channel 3 linearity at 2.875V on 3V range
Measured: 2.874905V low limit: 2.874834V high limit: 2.875014V

%PASS - Slot 17 channel 3 linearity at 3V on 3V range
Measured: 2.999903V low limit: 2.999831V high limit: 3.000011V

%PASS - Slot 17 channel 3 maximum linearity error on 3V range
Measured: 4.492254E-05V high limit: 0.00009V

%PASS - Slot 17 channel 3 linearity at 0V on 6V range
Measured: -0.00001294279V low limit: -2.163298E-04V high limit: 1.436701E-04V

%PASS - Slot 17 channel 3 linearity at .25V on 6V range
Measured: 0.2500088V low limit: 0.2497832V high limit: 0.2501432V

%PASS - Slot 17 channel 3 linearity at .5V on 6V range
Measured: 0.4999213V low limit: 0.4997828V high limit: 0.5001428V

%PASS - Slot 17 channel 3 linearity at .75V on 6V range
Measured: 0.7500123V low limit: 0.7497824V high limit: 0.7501424V

%PASS - Slot 17 channel 3 linearity at 1V on 6V range
Measured: 0.9999629V low limit: 0.9997820V high limit: 1.000142V

%PASS - Slot 17 channel 3 linearity at 1.25V on 6V range
Measured: 1.249957V low limit: 1.249781V high limit: 1.250141V

%PASS - Slot 17 channel 3 linearity at 1.5V on 6V range
Measured: 1.499877V low limit: 1.499781V high limit: 1.500141V

%PASS - Slot 17 channel 3 linearity at 1.75V on 6V range
Measured: 1.750004V low limit: 1.749780V high limit: 1.750140V

%PASS - Slot 17 channel 3 linearity at 2V on 6V range

Measured: 1.999922V low limit: 1.999780V high limit: 2.000140V

%PASS - Slot 17 channel 3 linearity at 2.25V on 6V range

Measured: 2.249948V low limit: 2.249779V high limit: 2.250139V

%PASS - Slot 17 channel 3 linearity at 2.5V on 6V range

Measured: 2.499862V low limit: 2.499779V high limit: 2.500139V

%PASS - Slot 17 channel 3 linearity at 2.75V on 6V range

Measured: 2.749998V low limit: 2.749779V high limit: 2.750139V

%PASS - Slot 17 channel 3 linearity at 3V on 6V range

Measured: 2.999904V low limit: 2.999778V high limit: 3.000138V

%PASS - Slot 17 channel 3 linearity at 3.25V on 6V range

Measured: 3.249932V low limit: 3.249778V high limit: 3.250138V

%PASS - Slot 17 channel 3 linearity at 3.5V on 6V range

Measured: 3.500057V low limit: 3.499777V high limit: 3.500137V

%PASS - Slot 17 channel 3 linearity at 3.75V on 6V range

Measured: 3.749976V low limit: 3.749777V high limit: 3.750137V

%PASS - Slot 17 channel 3 linearity at 4V on 6V range

Measured: 3.999988V low limit: 3.999777V high limit: 4.000137V

%PASS - Slot 17 channel 3 linearity at 4.25V on 6V range

Measured: 4.249908V low limit: 4.249776V high limit: 4.250136V

%PASS - Slot 17 channel 3 linearity at 4.5V on 6V range

Measured: 4.500049V low limit: 4.499776V high limit: 4.500136V

%PASS - Slot 17 channel 3 linearity at 4.75V on 6V range

Measured: 4.749960V low limit: 4.749775V high limit: 4.750135V

%PASS - Slot 17 channel 3 linearity at 5V on 6V range

Measured: 4.999971V low limit: 4.999775V high limit: 5.000135V

%PASS - Slot 17 channel 3 linearity at 5.25V on 6V range
Measured: 5.249909V low limit: 5.249775V high limit: 5.250135V

%PASS - Slot 17 channel 3 linearity at 5.5V on 6V range
Measured: 5.500001V low limit: 5.499774V high limit: 5.500134V

%PASS - Slot 17 channel 3 linearity at 5.75V on 6V range
Measured: 5.749918V low limit: 5.749774V high limit: 5.750134V

%PASS - Slot 17 channel 3 linearity at 6V on 6V range
Measured: 5.999925V low limit: 5.999773V high limit: 6.000133V

%PASS - Slot 17 channel 3 maximum linearity error on 6V range
Measured: 9.916872E-05V high limit: 0.00018V

%PASS - Slot 17 channel 4 linearity at 0V on 3V range
Measured: 0.000004159825V low limit: -9.861190E-05V high limit: 8.138809E-05V

%PASS - Slot 17 channel 4 linearity at .125V on 3V range
Measured: 0.1249960V low limit: 0.1249007V high limit: 0.1250807V

%PASS - Slot 17 channel 4 linearity at .25V on 3V range
Measured: 0.2499866V low limit: 0.2499000V high limit: 0.2500800V

%PASS - Slot 17 channel 4 linearity at .375V on 3V range
Measured: 0.3749745V low limit: 0.3748993V high limit: 0.3750793V

%PASS - Slot 17 channel 4 linearity at .5V on 3V range
Measured: 0.4999752V low limit: 0.4998987V high limit: 0.5000787V

%PASS - Slot 17 channel 4 linearity at .625V on 3V range
Measured: 0.6250155V low limit: 0.6248980V high limit: 0.6250780V

%PASS - Slot 17 channel 4 linearity at .75V on 3V range
Measured: 0.7499590V low limit: 0.7498974V high limit: 0.7500774V

%PASS - Slot 17 channel 4 linearity at .875V on 3V range
Measured: 0.8750089V low limit: 0.8748967V high limit: 0.8750767V

%PASS - Slot 17 channel 4 linearity at 1V on 3V range
Measured: 0.9999506V low limit: 0.9998960V high limit: 1.000076V

%PASS - Slot 17 channel 4 linearity at 1.125V on 3V range
Measured: 1.124999V low limit: 1.124895V high limit: 1.125075V

%PASS - Slot 17 channel 4 linearity at 1.25V on 3V range
Measured: 1.249944V low limit: 1.249894V high limit: 1.250074V

%PASS - Slot 17 channel 4 linearity at 1.375V on 3V range
Measured: 1.374995V low limit: 1.374894V high limit: 1.375074V

%PASS - Slot 17 channel 4 linearity at 1.5V on 3V range
Measured: 1.499945V low limit: 1.499893V high limit: 1.500073V

%PASS - Slot 17 channel 4 linearity at 1.625V on 3V range
Measured: 1.624989V low limit: 1.624892V high limit: 1.625072V

%PASS - Slot 17 channel 4 linearity at 1.75V on 3V range
Measured: 1.750037V low limit: 1.749892V high limit: 1.750072V

%PASS - Slot 17 channel 4 linearity at 1.875V on 3V range
Measured: 1.874984V low limit: 1.874891V high limit: 1.875071V

%PASS - Slot 17 channel 4 linearity at 2V on 3V range
Measured: 2.000022V low limit: 1.999890V high limit: 2.000070V

%PASS - Slot 17 channel 4 linearity at 2.125V on 3V range
Measured: 2.124969V low limit: 2.124890V high limit: 2.125070V

%PASS - Slot 17 channel 4 linearity at 2.25V on 3V range
Measured: 2.250015V low limit: 2.249889V high limit: 2.250069V

%PASS - Slot 17 channel 4 linearity at 2.375V on 3V range
Measured: 2.374952V low limit: 2.374888V high limit: 2.375068V

%PASS - Slot 17 channel 4 linearity at 2.5V on 3V range

Measured: 2.499990V low limit: 2.499888V high limit: 2.500068V

%PASS - Slot 17 channel 4 linearity at 2.625V on 3V range

Measured: 2.624992V low limit: 2.624887V high limit: 2.625067V

%PASS - Slot 17 channel 4 linearity at 2.75V on 3V range

Measured: 2.749970V low limit: 2.749886V high limit: 2.750066V

%PASS - Slot 17 channel 4 linearity at 2.875V on 3V range

Measured: 2.874957V low limit: 2.874886V high limit: 2.875066V

%PASS - Slot 17 channel 4 linearity at 3V on 3V range

Measured: 2.999946V low limit: 2.999885V high limit: 3.000065V

%PASS - Slot 17 channel 4 maximum linearity error on 3V range

Measured: 5.586274E-05V high limit: 0.00009V

%PASS - Slot 17 channel 4 linearity at 0V on 6V range

Measured: 0.00002568171V low limit: -1.776968E-04V high limit: 1.823031E-04V

%PASS - Slot 17 channel 4 linearity at .25V on 6V range

Measured: 0.2500744V low limit: 0.2498201V high limit: 0.2501801V

%PASS - Slot 17 channel 4 linearity at .5V on 6V range

Measured: 0.5000258V low limit: 0.4998179V high limit: 0.5001779V

%PASS - Slot 17 channel 4 linearity at .75V on 6V range

Measured: 0.7499775V low limit: 0.7498157V high limit: 0.7501757V

%PASS - Slot 17 channel 4 linearity at 1V on 6V range

Measured: 0.9999513V low limit: 0.9998135V high limit: 1.000173V

%PASS - Slot 17 channel 4 linearity at 1.25V on 6V range

Measured: 1.250004V low limit: 1.249811V high limit: 1.250171V

%PASS - Slot 17 channel 4 linearity at 1.5V on 6V range

Measured: 1.499956V low limit: 1.499809V high limit: 1.500169V

%PASS - Slot 17 channel 4 linearity at 1.75V on 6V range
Measured: 1.750032V low limit: 1.749807V high limit: 1.750167V

%PASS - Slot 17 channel 4 linearity at 2V on 6V range
Measured: 1.999893V low limit: 1.999804V high limit: 2.000164V

%PASS - Slot 17 channel 4 linearity at 2.25V on 6V range
Measured: 2.249968V low limit: 2.249802V high limit: 2.250162V

%PASS - Slot 17 channel 4 linearity at 2.5V on 6V range
Measured: 2.499929V low limit: 2.499800V high limit: 2.500160V

%PASS - Slot 17 channel 4 linearity at 2.75V on 6V range
Measured: 2.750002V low limit: 2.749798V high limit: 2.750158V

%PASS - Slot 17 channel 4 linearity at 3V on 6V range
Measured: 2.999888V low limit: 2.999796V high limit: 3.000156V

%PASS - Slot 17 channel 4 linearity at 3.25V on 6V range
Measured: 3.249938V low limit: 3.249794V high limit: 3.250154V

%PASS - Slot 17 channel 4 linearity at 3.5V on 6V range
Measured: 3.500019V low limit: 3.499791V high limit: 3.500151V

%PASS - Slot 17 channel 4 linearity at 3.75V on 6V range
Measured: 3.749988V low limit: 3.749789V high limit: 3.750149V

%PASS - Slot 17 channel 4 linearity at 4V on 6V range
Measured: 4.000050V low limit: 3.999787V high limit: 4.000147V

%PASS - Slot 17 channel 4 linearity at 4.25V on 6V range
Measured: 4.249924V low limit: 4.249785V high limit: 4.250145V

%PASS - Slot 17 channel 4 linearity at 4.5V on 6V range
Measured: 4.500001V low limit: 4.499783V high limit: 4.500143V

%PASS - Slot 17 channel 4 linearity at 4.75V on 6V range
Measured: 4.749952V low limit: 4.749780V high limit: 4.750140V

%PASS - Slot 17 channel 4 linearity at 5V on 6V range
Measured: 5.000013V low limit: 4.999778V high limit: 5.000138V

%PASS - Slot 17 channel 4 linearity at 5.25V on 6V range
Measured: 5.249988V low limit: 5.249776V high limit: 5.250136V

%PASS - Slot 17 channel 4 linearity at 5.5V on 6V range
Measured: 5.499934V low limit: 5.499774V high limit: 5.500134V

%PASS - Slot 17 channel 4 linearity at 5.75V on 6V range
Measured: 5.749898V low limit: 5.749772V high limit: 5.750132V

%PASS - Slot 17 channel 4 linearity at 6V on 6V range
Measured: 5.999962V low limit: 5.999770V high limit: 6.000130V

%PASS - Slot 17 channel 4 maximum linearity error on 6V range
Measured: 9.113385E-05V high limit: 0.00018V

%PASS - Slot 17 channel 5 linearity at 0V on 3V range
Measured: -0.00002924478V low limit: -1.321312E-04V high limit: 4.786876E-05V

%PASS - Slot 17 channel 5 linearity at .125V on 3V range
Measured: 0.1249566V low limit: 0.1248657V high limit: 0.1250457V

%PASS - Slot 17 channel 5 linearity at .25V on 3V range
Measured: 0.2499380V low limit: 0.2498636V high limit: 0.2500436V

%PASS - Slot 17 channel 5 linearity at .375V on 3V range
Measured: 0.3749754V low limit: 0.3748615V high limit: 0.3750415V

%PASS - Slot 17 channel 5 linearity at .5V on 3V range
Measured: 0.4999187V low limit: 0.4998594V high limit: 0.5000394V

%PASS - Slot 17 channel 5 linearity at .625V on 3V range
Measured: 0.6249514V low limit: 0.6248573V high limit: 0.6250373V

%PASS - Slot 17 channel 5 linearity at .75V on 3V range

Measured: 0.7499515V low limit: 0.7498552V high limit: 0.7500352V

%PASS - Slot 17 channel 5 linearity at .875V on 3V range

Measured: 0.8749404V low limit: 0.8748531V high limit: 0.8750331V

%PASS - Slot 17 channel 5 linearity at 1V on 3V range

Measured: 0.9999269V low limit: 0.9998510V high limit: 1.000031V

%PASS - Slot 17 channel 5 linearity at 1.125V on 3V range

Measured: 1.124967V low limit: 1.124848V high limit: 1.125028V

%PASS - Slot 17 channel 5 linearity at 1.25V on 3V range

Measured: 1.249905V low limit: 1.249846V high limit: 1.250026V

%PASS - Slot 17 channel 5 linearity at 1.375V on 3V range

Measured: 1.374946V low limit: 1.374844V high limit: 1.375024V

%PASS - Slot 17 channel 5 linearity at 1.5V on 3V range

Measured: 1.499890V low limit: 1.499842V high limit: 1.500022V

%PASS - Slot 17 channel 5 linearity at 1.625V on 3V range

Measured: 1.624930V low limit: 1.624840V high limit: 1.625020V

%PASS - Slot 17 channel 5 linearity at 1.75V on 3V range

Measured: 1.749966V low limit: 1.749838V high limit: 1.750018V

%PASS - Slot 17 channel 5 linearity at 1.875V on 3V range

Measured: 1.874910V low limit: 1.874836V high limit: 1.875016V

%PASS - Slot 17 channel 5 linearity at 2V on 3V range

Measured: 1.999946V low limit: 1.999834V high limit: 2.000014V

%PASS - Slot 17 channel 5 linearity at 2.125V on 3V range

Measured: 2.124935V low limit: 2.124832V high limit: 2.125012V

%PASS - Slot 17 channel 5 linearity at 2.25V on 3V range

Measured: 2.249926V low limit: 2.249830V high limit: 2.250010V

%PASS - Slot 17 channel 5 linearity at 2.375V on 3V range
Measured: 2.374912V low limit: 2.374828V high limit: 2.375008V

%PASS - Slot 17 channel 5 linearity at 2.5V on 3V range
Measured: 2.499946V low limit: 2.499825V high limit: 2.500005V

%PASS - Slot 17 channel 5 linearity at 2.625V on 3V range
Measured: 2.624888V low limit: 2.624823V high limit: 2.625003V

%PASS - Slot 17 channel 5 linearity at 2.75V on 3V range
Measured: 2.749922V low limit: 2.749821V high limit: 2.750001V

%PASS - Slot 17 channel 5 linearity at 2.875V on 3V range
Measured: 2.874905V low limit: 2.874819V high limit: 2.874999V

%PASS - Slot 17 channel 5 linearity at 3V on 3V range
Measured: 2.999886V low limit: 2.999817V high limit: 2.999997V

%PASS - Slot 17 channel 5 maximum linearity error on 3V range
Measured: 4.220148E-05V high limit: 0.00009V

%PASS - Slot 17 channel 5 linearity at 0V on 6V range
Measured: 0.00001895284V low limit: -1.810189E-04V high limit: 1.789810E-04V

%PASS - Slot 17 channel 5 linearity at .25V on 6V range
Measured: 0.2500416V low limit: 0.2498150V high limit: 0.2501750V

%PASS - Slot 17 channel 5 linearity at .5V on 6V range
Measured: 0.4999611V low limit: 0.4998110V high limit: 0.5001710V

%PASS - Slot 17 channel 5 linearity at .75V on 6V range
Measured: 0.7499903V low limit: 0.7498071V high limit: 0.7501671V

%PASS - Slot 17 channel 5 linearity at 1V on 6V range
Measured: 0.9999262V low limit: 0.9998031V high limit: 1.000163V

%PASS - Slot 17 channel 5 linearity at 1.25V on 6V range
Measured: 1.249950V low limit: 1.249799V high limit: 1.250159V

%PASS - Slot 17 channel 5 linearity at 1.5V on 6V range
Measured: 1.499998V low limit: 1.499795V high limit: 1.500155V

%PASS - Slot 17 channel 5 linearity at 1.75V on 6V range
Measured: 1.750029V low limit: 1.749791V high limit: 1.750151V

%PASS - Slot 17 channel 5 linearity at 2V on 6V range
Measured: 1.999958V low limit: 1.999787V high limit: 2.000147V

%PASS - Slot 17 channel 5 linearity at 2.25V on 6V range
Measured: 2.249993V low limit: 2.249783V high limit: 2.250143V

%PASS - Slot 17 channel 5 linearity at 2.5V on 6V range
Measured: 2.499922V low limit: 2.499779V high limit: 2.500139V

%PASS - Slot 17 channel 5 linearity at 2.75V on 6V range
Measured: 2.749959V low limit: 2.749775V high limit: 2.750135V

%PASS - Slot 17 channel 5 linearity at 3V on 6V range
Measured: 2.999903V low limit: 2.999771V high limit: 3.000131V

%PASS - Slot 17 channel 5 linearity at 3.25V on 6V range
Measured: 3.249938V low limit: 3.249767V high limit: 3.250127V

%PASS - Slot 17 channel 5 linearity at 3.5V on 6V range
Measured: 3.499972V low limit: 3.499763V high limit: 3.500123V

%PASS - Slot 17 channel 5 linearity at 3.75V on 6V range
Measured: 3.749911V low limit: 3.749759V high limit: 3.750119V

%PASS - Slot 17 channel 5 linearity at 4V on 6V range
Measured: 3.999948V low limit: 3.999755V high limit: 4.000115V

%PASS - Slot 17 channel 5 linearity at 4.25V on 6V range
Measured: 4.249880V low limit: 4.249751V high limit: 4.250111V

%PASS - Slot 17 channel 5 linearity at 4.5V on 6V range

Measured: 4.499927V low limit: 4.499747V high limit: 4.500107V

%PASS - Slot 17 channel 5 linearity at 4.75V on 6V range

Measured: 4.749950V low limit: 4.749743V high limit: 4.750103V

%PASS - Slot 17 channel 5 linearity at 5V on 6V range

Measured: 4.999972V low limit: 4.999739V high limit: 5.000099V

%PASS - Slot 17 channel 5 linearity at 5.25V on 6V range

Measured: 5.249916V low limit: 5.249735V high limit: 5.250095V

%PASS - Slot 17 channel 5 linearity at 5.5V on 6V range

Measured: 5.499941V low limit: 5.499731V high limit: 5.500091V

%PASS - Slot 17 channel 5 linearity at 5.75V on 6V range

Measured: 5.749869V low limit: 5.749727V high limit: 5.750087V

%PASS - Slot 17 channel 5 linearity at 6V on 6V range

Measured: 5.999903V low limit: 5.999724V high limit: 6.000084V

%PASS - Slot 17 channel 5 maximum linearity error on 6V range

Measured: 5.811299E-05V high limit: 0.00018V

%PASS - Slot 17 channel 6 linearity at 0V on 3V range

Measured: 0.00001039125V low limit: -6.502118E-05V high limit: 1.149788E-04V

%PASS - Slot 17 channel 6 linearity at .125V on 3V range

Measured: 0.1250546V low limit: 0.1249321V high limit: 0.1251121V

%PASS - Slot 17 channel 6 linearity at .25V on 3V range

Measured: 0.2499933V low limit: 0.2499292V high limit: 0.2501092V

%PASS - Slot 17 channel 6 linearity at .375V on 3V range

Measured: 0.3750249V low limit: 0.3749264V high limit: 0.3751064V

%PASS - Slot 17 channel 6 linearity at .5V on 3V range

Measured: 0.5000137V low limit: 0.4999235V high limit: 0.5001035V

%PASS - Slot 17 channel 6 linearity at .625V on 3V range
Measured: 0.6249974V low limit: 0.6249206V high limit: 0.6251006V

%PASS - Slot 17 channel 6 linearity at .75V on 3V range
Measured: 0.7499867V low limit: 0.7499178V high limit: 0.7500978V

%PASS - Slot 17 channel 6 linearity at .875V on 3V range
Measured: 0.8750276V low limit: 0.8749149V high limit: 0.8750949V

%PASS - Slot 17 channel 6 linearity at 1V on 3V range
Measured: 0.9999675V low limit: 0.9999121V high limit: 1.000092V

%PASS - Slot 17 channel 6 linearity at 1.125V on 3V range
Measured: 1.125008V low limit: 1.124909V high limit: 1.125089V

%PASS - Slot 17 channel 6 linearity at 1.25V on 3V range
Measured: 1.250003V low limit: 1.249906V high limit: 1.250086V

%PASS - Slot 17 channel 6 linearity at 1.375V on 3V range
Measured: 1.374983V low limit: 1.374903V high limit: 1.375083V

%PASS - Slot 17 channel 6 linearity at 1.5V on 3V range
Measured: 1.499976V low limit: 1.499900V high limit: 1.500080V

%PASS - Slot 17 channel 6 linearity at 1.625V on 3V range
Measured: 1.625014V low limit: 1.624897V high limit: 1.625077V

%PASS - Slot 17 channel 6 linearity at 1.75V on 3V range
Measured: 1.749994V low limit: 1.749894V high limit: 1.750074V

%PASS - Slot 17 channel 6 linearity at 1.875V on 3V range
Measured: 1.874988V low limit: 1.874892V high limit: 1.875072V

%PASS - Slot 17 channel 6 linearity at 2V on 3V range
Measured: 2.000023V low limit: 1.999889V high limit: 2.000069V

%PASS - Slot 17 channel 6 linearity at 2.125V on 3V range
Measured: 2.124962V low limit: 2.124886V high limit: 2.125066V

%PASS - Slot 17 channel 6 linearity at 2.25V on 3V range
Measured: 2.249996V low limit: 2.249883V high limit: 2.250063V

%PASS - Slot 17 channel 6 linearity at 2.375V on 3V range
Measured: 2.374980V low limit: 2.374880V high limit: 2.375060V

%PASS - Slot 17 channel 6 linearity at 2.5V on 3V range
Measured: 2.499968V low limit: 2.499877V high limit: 2.500057V

%PASS - Slot 17 channel 6 linearity at 2.625V on 3V range
Measured: 2.624951V low limit: 2.624874V high limit: 2.625054V

%PASS - Slot 17 channel 6 linearity at 2.75V on 3V range
Measured: 2.749974V low limit: 2.749872V high limit: 2.750052V

%PASS - Slot 17 channel 6 linearity at 2.875V on 3V range
Measured: 2.874913V low limit: 2.874869V high limit: 2.875049V

%PASS - Slot 17 channel 6 linearity at 3V on 3V range
Measured: 2.999947V low limit: 2.999866V high limit: 3.000046V

%PASS - Slot 17 channel 6 maximum linearity error on 3V range
Measured: 4.563947E-05V high limit: 0.00009V

%PASS - Slot 17 channel 6 linearity at 0V on 6V range
Measured: 0.0001026958V low limit: -7.126660E-05V high limit: 2.887333E-04V

%PASS - Slot 17 channel 6 linearity at .25V on 6V range
Measured: 0.2501782V low limit: 0.2499218V high limit: 0.2502818V

%PASS - Slot 17 channel 6 linearity at .5V on 6V range
Measured: 0.5000577V low limit: 0.4999149V high limit: 0.5002749V

%PASS - Slot 17 channel 6 linearity at .75V on 6V range
Measured: 0.7501123V low limit: 0.7499080V high limit: 0.7502680V

%PASS - Slot 17 channel 6 linearity at 1V on 6V range

Measured: 1.000085V low limit: 0.9999012V high limit: 1.000261V

%PASS - Slot 17 channel 6 linearity at 1.25V on 6V range

Measured: 1.250046V low limit: 1.249894V high limit: 1.250254V

%PASS - Slot 17 channel 6 linearity at 1.5V on 6V range

Measured: 1.500021V low limit: 1.499887V high limit: 1.500247V

%PASS - Slot 17 channel 6 linearity at 1.75V on 6V range

Measured: 1.750101V low limit: 1.749880V high limit: 1.750240V

%PASS - Slot 17 channel 6 linearity at 2V on 6V range

Measured: 1.999974V low limit: 1.999873V high limit: 2.000233V

%PASS - Slot 17 channel 6 linearity at 2.25V on 6V range

Measured: 2.250055V low limit: 2.249866V high limit: 2.250226V

%PASS - Slot 17 channel 6 linearity at 2.5V on 6V range

Measured: 2.500039V low limit: 2.499859V high limit: 2.500219V

%PASS - Slot 17 channel 6 linearity at 2.75V on 6V range

Measured: 2.750001V low limit: 2.749853V high limit: 2.750213V

%PASS - Slot 17 channel 6 linearity at 3V on 6V range

Measured: 2.999986V low limit: 2.999846V high limit: 3.000206V

%PASS - Slot 17 channel 6 linearity at 3.25V on 6V range

Measured: 3.250056V low limit: 3.249839V high limit: 3.250199V

%PASS - Slot 17 channel 6 linearity at 3.5V on 6V range

Measured: 3.500020V low limit: 3.499832V high limit: 3.500192V

%PASS - Slot 17 channel 6 linearity at 3.75V on 6V range

Measured: 3.750008V low limit: 3.749825V high limit: 3.750185V

%PASS - Slot 17 channel 6 linearity at 4V on 6V range

Measured: 4.000086V low limit: 3.999818V high limit: 4.000178V

%PASS - Slot 17 channel 6 linearity at 4.25V on 6V range
Measured: 4.249954V low limit: 4.249811V high limit: 4.250171V

%PASS - Slot 17 channel 6 linearity at 4.5V on 6V range
Measured: 4.500031V low limit: 4.499804V high limit: 4.500164V

%PASS - Slot 17 channel 6 linearity at 4.75V on 6V range
Measured: 4.749999V low limit: 4.749797V high limit: 4.750157V

%PASS - Slot 17 channel 6 linearity at 5V on 6V range
Measured: 4.999970V low limit: 4.999791V high limit: 5.000151V

%PASS - Slot 17 channel 6 linearity at 5.25V on 6V range
Measured: 5.249943V low limit: 5.249784V high limit: 5.250144V

%PASS - Slot 17 channel 6 linearity at 5.5V on 6V range
Measured: 5.499995V low limit: 5.499777V high limit: 5.500137V

%PASS - Slot 17 channel 6 linearity at 5.75V on 6V range
Measured: 5.749875V low limit: 5.749770V high limit: 5.750130V

%PASS - Slot 17 channel 6 linearity at 6V on 6V range
Measured: 5.999948V low limit: 5.999763V high limit: 6.000123V

%PASS - Slot 17 channel 6 maximum linearity error on 6V range
Measured: 8.792452E-05V high limit: 0.00018V

%PASS - Slot 17 channel 7 linearity at 0V on 3V range
Measured: 0.00003193107V low limit: -3.736586E-05V high limit: 1.426341E-04V

%PASS - Slot 17 channel 7 linearity at .125V on 3V range
Measured: 0.1250748V low limit: 0.1249595V high limit: 0.1251395V

%PASS - Slot 17 channel 7 linearity at .25V on 3V range
Measured: 0.2500248V low limit: 0.2499563V high limit: 0.2501363V

%PASS - Slot 17 channel 7 linearity at .375V on 3V range
Measured: 0.3750535V low limit: 0.3749532V high limit: 0.3751332V

%PASS - Slot 17 channel 7 linearity at .5V on 3V range
Measured: 0.5000037V low limit: 0.4999501V high limit: 0.5001301V

%PASS - Slot 17 channel 7 linearity at .625V on 3V range
Measured: 0.6250403V low limit: 0.6249470V high limit: 0.6251270V

%PASS - Slot 17 channel 7 linearity at .75V on 3V range
Measured: 0.7500448V low limit: 0.7499438V high limit: 0.7501238V

%PASS - Slot 17 channel 7 linearity at .875V on 3V range
Measured: 0.8750369V low limit: 0.8749407V high limit: 0.8751207V

%PASS - Slot 17 channel 7 linearity at 1V on 3V range
Measured: 1.000023V low limit: 0.9999376V high limit: 1.000117V

%PASS - Slot 17 channel 7 linearity at 1.125V on 3V range
Measured: 1.125024V low limit: 1.124934V high limit: 1.125114V

%PASS - Slot 17 channel 7 linearity at 1.25V on 3V range
Measured: 1.250018V low limit: 1.249931V high limit: 1.250111V

%PASS - Slot 17 channel 7 linearity at 1.375V on 3V range
Measured: 1.375009V low limit: 1.374928V high limit: 1.375108V

%PASS - Slot 17 channel 7 linearity at 1.5V on 3V range
Measured: 1.500001V low limit: 1.499925V high limit: 1.500105V

%PASS - Slot 17 channel 7 linearity at 1.625V on 3V range
Measured: 1.625039V low limit: 1.624922V high limit: 1.625102V

%PASS - Slot 17 channel 7 linearity at 1.75V on 3V range
Measured: 1.750029V low limit: 1.749918V high limit: 1.750098V

%PASS - Slot 17 channel 7 linearity at 1.875V on 3V range
Measured: 1.875026V low limit: 1.874915V high limit: 1.875095V

%PASS - Slot 17 channel 7 linearity at 2V on 3V range

Measured: 2.000021V low limit: 1.999912V high limit: 2.000092V

%PASS - Slot 17 channel 7 linearity at 2.125V on 3V range

Measured: 2.125007V low limit: 2.124909V high limit: 2.125089V

%PASS - Slot 17 channel 7 linearity at 2.25V on 3V range

Measured: 2.250000V low limit: 2.249906V high limit: 2.250086V

%PASS - Slot 17 channel 7 linearity at 2.375V on 3V range

Measured: 2.374987V low limit: 2.374903V high limit: 2.375083V

%PASS - Slot 17 channel 7 linearity at 2.5V on 3V range

Measured: 2.500018V low limit: 2.499900V high limit: 2.500080V

%PASS - Slot 17 channel 7 linearity at 2.625V on 3V range

Measured: 2.624966V low limit: 2.624897V high limit: 2.625077V

%PASS - Slot 17 channel 7 linearity at 2.75V on 3V range

Measured: 2.749986V low limit: 2.749893V high limit: 2.750073V

%PASS - Slot 17 channel 7 linearity at 2.875V on 3V range

Measured: 2.874935V low limit: 2.874890V high limit: 2.875070V

%PASS - Slot 17 channel 7 linearity at 3V on 3V range

Measured: 2.999970V low limit: 2.999887V high limit: 3.000067V

%PASS - Slot 17 channel 7 maximum linearity error on 3V range

Measured: 4.575549E-05V high limit: 0.00009V

%PASS - Slot 17 channel 7 linearity at 0V on 6V range

Measured: -0.000008091709V low limit: -1.280610E-04V high limit: 2.319389E-04V

%PASS - Slot 17 channel 7 linearity at .25V on 6V range

Measured: 0.2500666V low limit: 0.2498752V high limit: 0.2502352V

%PASS - Slot 17 channel 7 linearity at .5V on 6V range

Measured: 0.5000497V low limit: 0.4998785V high limit: 0.5002385V

%PASS - Slot 17 channel 7 linearity at .75V on 6V range
Measured: 0.7500934V low limit: 0.7498818V high limit: 0.7502418V

%PASS - Slot 17 channel 7 linearity at 1V on 6V range
Measured: 1.000079V low limit: 0.9998851V high limit: 1.000245V

%PASS - Slot 17 channel 7 linearity at 1.25V on 6V range
Measured: 1.250037V low limit: 1.249888V high limit: 1.250248V

%PASS - Slot 17 channel 7 linearity at 1.5V on 6V range
Measured: 1.500040V low limit: 1.499891V high limit: 1.500251V

%PASS - Slot 17 channel 7 linearity at 1.75V on 6V range
Measured: 1.750108V low limit: 1.749894V high limit: 1.750254V

%PASS - Slot 17 channel 7 linearity at 2V on 6V range
Measured: 2.000061V low limit: 1.999898V high limit: 2.000258V

%PASS - Slot 17 channel 7 linearity at 2.25V on 6V range
Measured: 2.250149V low limit: 2.249901V high limit: 2.250261V

%PASS - Slot 17 channel 7 linearity at 2.5V on 6V range
Measured: 2.500024V low limit: 2.499904V high limit: 2.500264V

%PASS - Slot 17 channel 7 linearity at 2.75V on 6V range
Measured: 2.750092V low limit: 2.749908V high limit: 2.750268V

%PASS - Slot 17 channel 7 linearity at 3V on 6V range
Measured: 3.000066V low limit: 2.999911V high limit: 3.000271V

%PASS - Slot 17 channel 7 linearity at 3.25V on 6V range
Measured: 3.250128V low limit: 3.249914V high limit: 3.250274V

%PASS - Slot 17 channel 7 linearity at 3.5V on 6V range
Measured: 3.500194V low limit: 3.499918V high limit: 3.500278V

%PASS - Slot 17 channel 7 linearity at 3.75V on 6V range
Measured: 3.750069V low limit: 3.749921V high limit: 3.750281V

%PASS - Slot 17 channel 7 linearity at 4V on 6V range
Measured: 4.000141V low limit: 3.999924V high limit: 4.000284V

%PASS - Slot 17 channel 7 linearity at 4.25V on 6V range
Measured: 4.250096V low limit: 4.249927V high limit: 4.250287V

%PASS - Slot 17 channel 7 linearity at 4.5V on 6V range
Measured: 4.500170V low limit: 4.499931V high limit: 4.500291V

%PASS - Slot 17 channel 7 linearity at 4.75V on 6V range
Measured: 4.750138V low limit: 4.749934V high limit: 4.750294V

%PASS - Slot 17 channel 7 linearity at 5V on 6V range
Measured: 5.000089V low limit: 4.999937V high limit: 5.000297V

%PASS - Slot 17 channel 7 linearity at 5.25V on 6V range
Measured: 5.250066V low limit: 5.249941V high limit: 5.250301V

%PASS - Slot 17 channel 7 linearity at 5.5V on 6V range
Measured: 5.500101V low limit: 5.499944V high limit: 5.500304V

%PASS - Slot 17 channel 7 linearity at 5.75V on 6V range
Measured: 5.750083V low limit: 5.749947V high limit: 5.750307V

%PASS - Slot 17 channel 7 linearity at 6V on 6V range
Measured: 6.000146V low limit: 5.999951V high limit: 6.000311V

%PASS - Slot 17 channel 7 maximum linearity error on 6V range
Measured: 9.663038E-05V high limit: 0.00018V

- ...checking DAC code transitions...

%PASS - Slot 17 channel 0 raw DAC codes linearity at 3.05V on 3V range
Measured: 3.050902V low limit: 3.050816V high limit: 3.050996V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 3.04995269703212V on 3V range
Measured: 3.050858V low limit: 3.050768V high limit: 3.050948V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 3.04990539406424V on 3V range
Measured: 3.050813V low limit: 3.050721V high limit: 3.050901V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 3.04985809109636V on 3V range
Measured: 3.050767V low limit: 3.050674V high limit: 3.050854V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 3.04981078812848V on 3V range
Measured: 3.050719V low limit: 3.050626V high limit: 3.050806V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 3.04966887922484V on 3V range
Measured: 3.050586V low limit: 3.050484V high limit: 3.050664V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 3.04962157625696V on 3V range
Measured: 3.050533V low limit: 3.050437V high limit: 3.050617V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 3.0492904554818V on 3V range
Measured: 3.050205V low limit: 3.050106V high limit: 3.050286V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 3.04924315251392V on 3V range
Measured: 3.050148V low limit: 3.050059V high limit: 3.050239V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 3.04853360799573V on 3V range
Measured: 3.049449V low limit: 3.049349V high limit: 3.049529V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 3.04848630502785V on 3V range
Measured: 3.049391V low limit: 3.049302V high limit: 3.049482V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 3.04701991302358V on 3V range
Measured: 3.047935V low limit: 3.047835V high limit: 3.048015V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 3.0469726100557V on 3V range
Measured: 3.047877V low limit: 3.047788V high limit: 3.047968V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 3.04399252307927V on 3V range
Measured: 3.044903V low limit: 3.044807V high limit: 3.044987V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 3.04394522011139V on 3V range
Measured: 3.044845V low limit: 3.044760V high limit: 3.044940V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 3.03793774319066V on 3V range
Measured: 3.038846V low limit: 3.038751V high limit: 3.038931V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 3.03789044022278V on 3V range
Measured: 3.038792V low limit: 3.038704V high limit: 3.038884V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 3.02582818341344V on 3V range
Measured: 3.026733V low limit: 3.026640V high limit: 3.026820V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 3.02578088044556V on 3V range
Measured: 3.026677V low limit: 3.026592V high limit: 3.026772V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 3.00160906385901V on 3V range
Measured: 3.002505V low limit: 3.002416V high limit: 3.002596V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 3.00156176089113V on 3V range
Measured: 3.002455V low limit: 3.002369V high limit: 3.002549V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 2.95317082475013V on 3V range
Measured: 2.954058V low limit: 2.953970V high limit: 2.954150V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 2.95312352178225V on 3V range
Measured: 2.954002V low limit: 2.953923V high limit: 2.954103V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 2.85629434653239V on 3V range
Measured: 2.857160V low limit: 2.857077V high limit: 2.857257V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 2.85624704356451V on 3V range
Measured: 2.857105V low limit: 2.857030V high limit: 2.857210V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 2.66254139009689V on 3V range
Measured: 2.663373V low limit: 2.663291V high limit: 2.663471V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 2.66249408712902V on 3V range
Measured: 2.663323V low limit: 2.663244V high limit: 2.663424V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 2.27503547722591V on 3V range

Measured: 2.275803V low limit: 2.275719V high limit: 2.275899V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 2.27498817425803V on 3V range
Measured: 2.275760V low limit: 2.275672V high limit: 2.275852V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 1.50002365148394V on 3V range
Measured: 1.500675V low limit: 1.500575V high limit: 1.500755V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 1.49997634851606V on 3V range
Measured: 1.500625V low limit: 1.500528V high limit: 1.500708V

%PASS - Slot 17 channel 0 raw DAC codes linearity at -.05V on 3V range
Measured: -0.04962456V low limit: -4.971201E-02V high limit: -4.953201E-02V

%PASS - Slot 17 channel 0 raw DAC codes maximum linearity error on 3V range
Measured: 1.418871E-05V high limit: 0.00009V

%PASS - Slot 17 channel 0 raw DAC code binary transition 0 to 1 on 3V range
Measured: 4.413400E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition 1 to 2 on 3V range
Measured: 4.492299E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition 2 to 3 on 3V range
Measured: 4.575399E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition 3 to 4 on 3V range
Measured: 4.868800E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition 7 to 8 on 3V range
Measured: 5.254100E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition 15 to 16 on 3V range
Measured: 5.792599E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition 31 to 32 on 3V range
Measured: 5.840899E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition 63 to 64 on 3V range
Measured: 5.788300E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition 127 to 128 on 3V range
Measured: 5.849599E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition 255 to 256 on 3V range
Measured: 5.442399E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition 511 to 512 on 3V range
Measured: 5.599999E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition 1023 to 1024 on 3V range
Measured: 5.048299E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition 2047 to 2048 on 3V range
Measured: 5.586899E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition 4095 to 4096 on 3V range
Measured: 5.433599E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition 8191 to 8192 on 3V range
Measured: 5.017699E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition 16383 to 16384 on 3V range
Measured: 4.325899E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition 32767 to 32768 on 3V range
Measured: 4.978299E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transitions maximum difference on 3V range
Measured: 5.849599E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transitions minimum difference on 3V range
Measured: 4.325899E-05V low limit: -4.269775E-05V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 6.1V on 6V range
Measured: 6.101640V low limit: 6.101478V high limit: 6.101838V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 6.09990539406424V on 6V range
Measured: 6.101566V low limit: 6.101383V high limit: 6.101743V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 6.09981078812848V on 6V range
Measured: 6.101470V low limit: 6.101289V high limit: 6.101649V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 6.09971618219272V on 6V range
Measured: 6.101386V low limit: 6.101194V high limit: 6.101554V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 6.09962157625696V on 6V range
Measured: 6.101274V low limit: 6.101100V high limit: 6.101460V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 6.09933775844968V on 6V range
Measured: 6.101006V low limit: 6.100816V high limit: 6.101176V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 6.09924315251392V on 6V range
Measured: 6.100900V low limit: 6.100721V high limit: 6.101081V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 6.09858091096361V on 6V range
Measured: 6.100250V low limit: 6.100059V high limit: 6.100419V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 6.09848630502785V on 6V range
Measured: 6.100138V low limit: 6.099964V high limit: 6.100324V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 6.09706721599146V on 6V range
Measured: 6.098740V low limit: 6.098545V high limit: 6.098905V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 6.0969726100557V on 6V range
Measured: 6.098628V low limit: 6.098450V high limit: 6.098810V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 6.09403982604715V on 6V range
Measured: 6.095717V low limit: 6.095517V high limit: 6.095877V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 6.09394522011139V on 6V range
Measured: 6.095603V low limit: 6.095423V high limit: 6.095783V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 6.08798504615854V on 6V range

Measured: 6.089657V low limit: 6.089462V high limit: 6.089822V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 6.08789044022278V on 6V range

Measured: 6.089548V low limit: 6.089367V high limit: 6.089727V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 6.07587548638132V on 6V range

Measured: 6.077550V low limit: 6.077352V high limit: 6.077712V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 6.07578088044556V on 6V range

Measured: 6.077438V low limit: 6.077257V high limit: 6.077617V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 6.05165636682689V on 6V range

Measured: 6.053321V low limit: 6.053130V high limit: 6.053490V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 6.05156176089113V on 6V range

Measured: 6.053213V low limit: 6.053036V high limit: 6.053396V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 6.00321812771801V on 6V range

Measured: 6.004873V low limit: 6.004688V high limit: 6.005048V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 6.00312352178225V on 6V range

Measured: 6.004773V low limit: 6.004593V high limit: 6.004953V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 5.90634164950027V on 6V range

Measured: 5.907982V low limit: 5.907803V high limit: 5.908163V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 5.90624704356451V on 6V range

Measured: 5.907870V low limit: 5.907708V high limit: 5.908068V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 5.71258869306477V on 6V range

Measured: 5.714201V low limit: 5.714033V high limit: 5.714393V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 5.71249408712902V on 6V range

Measured: 5.714091V low limit: 5.713939V high limit: 5.714299V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 5.32508278019379V on 6V range

Measured: 5.326655V low limit: 5.326494V high limit: 5.326854V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 5.32498817425803V on 6V range
Measured: 5.326562V low limit: 5.326399V high limit: 5.326759V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 4.55007095445182V on 6V range
Measured: 4.551584V low limit: 4.551415V high limit: 4.551775V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 4.54997634851606V on 6V range
Measured: 4.551501V low limit: 4.551320V high limit: 4.551680V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 3.00004730296788V on 6V range
Measured: 3.001450V low limit: 3.001257V high limit: 3.001617V

%PASS - Slot 17 channel 0 raw DAC codes linearity at 2.99995269703212V on 6V range
Measured: 3.001355V low limit: 3.001163V high limit: 3.001523V

%PASS - Slot 17 channel 0 raw DAC codes linearity at -.1V on 6V range
Measured: -0.09888001V low limit: -9.905744E-02V high limit: -9.869744E-02V

%PASS - Slot 17 channel 0 raw DAC codes maximum linearity error on 6V range
Measured: 2.799220E-05V high limit: 0.00018V

%PASS - Slot 17 channel 0 raw DAC code binary transition 0 to 1 on 6V range
Measured: 7.329499E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition 1 to 2 on 6V range
Measured: 9.619300E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition 2 to 3 on 6V range
Measured: 8.389100E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition 3 to 4 on 6V range
Measured: 1.116500E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition 7 to 8 on 6V range
Measured: 1.060889E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition 15 to 16 on 6V range

Measured: 1.120429E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition 31 to 32 on 6V range

Measured: 1.119989E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition 63 to 64 on 6V range

Measured: 1.141890E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition 127 to 128 on 6V range

Measured: 1.090670E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition 255 to 256 on 6V range

Measured: 1.118249E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition 511 to 512 on 6V range

Measured: 1.081470E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition 1023 to 1024 on 6V range

Measured: 1.004409E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition 2047 to 2048 on 6V range

Measured: 1.121309E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition 4095 to 4096 on 6V range

Measured: 1.102919E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition 8191 to 8192 on 6V range

Measured: 9.308599E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition 16383 to 16384 on 6V range

Measured: 8.253300E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transition 32767 to 32768 on 6V range

Measured: 9.461700E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transitions maximum difference on 6V range

Measured: 1.141890E-04V high limit: 2.746044E-04V

%PASS - Slot 17 channel 0 raw DAC code binary transitions minimum difference on 6V range
Measured: 7.329499E-05V low limit: -8.539550E-05V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 3.05V on 3V range
Measured: 3.050807V low limit: 3.050731V high limit: 3.050911V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 3.04995269703212V on 3V range
Measured: 3.050758V low limit: 3.050684V high limit: 3.050864V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 3.04990539406424V on 3V range
Measured: 3.050708V low limit: 3.050637V high limit: 3.050817V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 3.04985809109636V on 3V range
Measured: 3.050678V low limit: 3.050590V high limit: 3.050770V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 3.04981078812848V on 3V range
Measured: 3.050630V low limit: 3.050542V high limit: 3.050722V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 3.04966887922484V on 3V range
Measured: 3.050493V low limit: 3.050400V high limit: 3.050580V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 3.04962157625696V on 3V range
Measured: 3.050441V low limit: 3.050353V high limit: 3.050533V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 3.0492904554818V on 3V range
Measured: 3.050116V low limit: 3.050022V high limit: 3.050202V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 3.04924315251392V on 3V range
Measured: 3.050062V low limit: 3.049974V high limit: 3.050154V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 3.04853360799573V on 3V range
Measured: 3.049359V low limit: 3.049265V high limit: 3.049445V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 3.04848630502785V on 3V range
Measured: 3.049308V low limit: 3.049217V high limit: 3.049397V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 3.04701991302358V on 3V range
Measured: 3.047846V low limit: 3.047751V high limit: 3.047931V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 3.0469726100557V on 3V range
Measured: 3.047793V low limit: 3.047703V high limit: 3.047883V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 3.04399252307927V on 3V range
Measured: 3.044807V low limit: 3.044723V high limit: 3.044903V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 3.04394522011139V on 3V range
Measured: 3.044762V low limit: 3.044675V high limit: 3.044855V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 3.03793774319066V on 3V range
Measured: 3.038762V low limit: 3.038667V high limit: 3.038847V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 3.03789044022278V on 3V range
Measured: 3.038709V low limit: 3.038619V high limit: 3.038799V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 3.02582818341344V on 3V range
Measured: 3.026649V low limit: 3.026555V high limit: 3.026735V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 3.02578088044556V on 3V range
Measured: 3.026600V low limit: 3.026507V high limit: 3.026687V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 3.00160906385901V on 3V range
Measured: 3.002425V low limit: 3.002330V high limit: 3.002510V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 3.00156176089113V on 3V range
Measured: 3.002372V low limit: 3.002283V high limit: 3.002463V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 2.95317082475013V on 3V range
Measured: 2.953976V low limit: 2.953882V high limit: 2.954062V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 2.95312352178225V on 3V range
Measured: 2.953921V low limit: 2.953835V high limit: 2.954015V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 2.85629434653239V on 3V range
Measured: 2.857075V low limit: 2.856985V high limit: 2.857165V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 2.85624704356451V on 3V range

Measured: 2.857027V low limit: 2.856938V high limit: 2.857118V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 2.66254139009689V on 3V range
Measured: 2.663286V low limit: 2.663191V high limit: 2.663371V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 2.66249408712902V on 3V range
Measured: 2.663241V low limit: 2.663144V high limit: 2.663324V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 2.27503547722591V on 3V range
Measured: 2.275705V low limit: 2.275603V high limit: 2.275783V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 2.27498817425803V on 3V range
Measured: 2.275654V low limit: 2.275556V high limit: 2.275736V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 1.50002365148394V on 3V range
Measured: 1.500523V low limit: 1.500428V high limit: 1.500608V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 1.49997634851606V on 3V range
Measured: 1.500480V low limit: 1.500381V high limit: 1.500561V

%PASS - Slot 17 channel 1 raw DAC codes linearity at -.05V on 3V range
Measured: -0.04984620V low limit: -4.992235E-02V high limit: -4.974235E-02V

%PASS - Slot 17 channel 1 raw DAC codes maximum linearity error on 3V range
Measured: 1.875616E-05V high limit: 0.00009V

%PASS - Slot 17 channel 1 raw DAC code binary transition 0 to 1 on 3V range
Measured: 4.807500E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transition 1 to 2 on 3V range
Measured: 5.039499E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transition 2 to 3 on 3V range
Measured: 2.990500E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transition 3 to 4 on 3V range
Measured: 4.855599E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transition 7 to 8 on 3V range
Measured: 5.258499E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transition 15 to 16 on 3V range
Measured: 5.359099E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transition 31 to 32 on 3V range
Measured: 5.135899E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transition 63 to 64 on 3V range
Measured: 5.337300E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transition 127 to 128 on 3V range
Measured: 4.514199E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transition 255 to 256 on 3V range
Measured: 5.319799E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transition 511 to 512 on 3V range
Measured: 4.890699E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transition 1023 to 1024 on 3V range
Measured: 5.271599E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transition 2047 to 2048 on 3V range
Measured: 5.503599E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transition 4095 to 4096 on 3V range
Measured: 4.785600E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transition 8191 to 8192 on 3V range
Measured: 4.470299E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transition 16383 to 16384 on 3V range
Measured: 5.109599E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transition 32767 to 32768 on 3V range
Measured: 4.233899E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transitions maximum difference on 3V range
Measured: 5.503599E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transitions minimum difference on 3V range
Measured: 2.990500E-05V low limit: -4.269775E-05V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 6.1V on 6V range
Measured: 6.102019V low limit: 6.101861V high limit: 6.102221V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 6.09990539406424V on 6V range
Measured: 6.101946V low limit: 6.101767V high limit: 6.102127V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 6.09981078812848V on 6V range
Measured: 6.101843V low limit: 6.101672V high limit: 6.102032V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 6.09971618219272V on 6V range
Measured: 6.101762V low limit: 6.101577V high limit: 6.101937V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 6.09962157625696V on 6V range
Measured: 6.101652V low limit: 6.101483V high limit: 6.101843V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 6.09933775844968V on 6V range
Measured: 6.101390V low limit: 6.101199V high limit: 6.101559V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 6.09924315251392V on 6V range
Measured: 6.101291V low limit: 6.101104V high limit: 6.101464V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 6.09858091096361V on 6V range
Measured: 6.100634V low limit: 6.100442V high limit: 6.100802V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 6.09848630502785V on 6V range
Measured: 6.100523V low limit: 6.100347V high limit: 6.100707V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 6.09706721599146V on 6V range
Measured: 6.099117V low limit: 6.098928V high limit: 6.099288V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 6.0969726100557V on 6V range

Measured: 6.099015V low limit: 6.098833V high limit: 6.099193V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 6.09403982604715V on 6V range
Measured: 6.096095V low limit: 6.095900V high limit: 6.096260V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 6.09394522011139V on 6V range
Measured: 6.095987V low limit: 6.095805V high limit: 6.096165V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 6.08798504615854V on 6V range
Measured: 6.090037V low limit: 6.089844V high limit: 6.090204V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 6.08789044022278V on 6V range
Measured: 6.089930V low limit: 6.089749V high limit: 6.090109V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 6.07587548638132V on 6V range
Measured: 6.077925V low limit: 6.077731V high limit: 6.078091V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 6.07578088044556V on 6V range
Measured: 6.077812V low limit: 6.077637V high limit: 6.077997V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 6.05165636682689V on 6V range
Measured: 6.053695V low limit: 6.053507V high limit: 6.053867V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 6.05156176089113V on 6V range
Measured: 6.053590V low limit: 6.053412V high limit: 6.053772V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 6.00321812771801V on 6V range
Measured: 6.005243V low limit: 6.005057V high limit: 6.005417V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 6.00312352178225V on 6V range
Measured: 6.005142V low limit: 6.004963V high limit: 6.005323V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 5.90634164950027V on 6V range
Measured: 5.908344V low limit: 5.908159V high limit: 5.908519V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 5.90624704356451V on 6V range
Measured: 5.908231V low limit: 5.908064V high limit: 5.908424V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 5.71258869306477V on 6V range
Measured: 5.714533V low limit: 5.714361V high limit: 5.714721V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 5.71249408712902V on 6V range
Measured: 5.714430V low limit: 5.714267V high limit: 5.714627V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 5.32508278019379V on 6V range
Measured: 5.326928V low limit: 5.326766V high limit: 5.327126V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 5.32498817425803V on 6V range
Measured: 5.326850V low limit: 5.326672V high limit: 5.327032V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 4.55007095445182V on 6V range
Measured: 4.551754V low limit: 4.551577V high limit: 4.551937V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 4.54997634851606V on 6V range
Measured: 4.551656V low limit: 4.551482V high limit: 4.551842V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 3.00004730296788V on 6V range
Measured: 3.001382V low limit: 3.001198V high limit: 3.001558V

%PASS - Slot 17 channel 1 raw DAC codes linearity at 2.99995269703212V on 6V range
Measured: 3.001286V low limit: 3.001103V high limit: 3.001463V

%PASS - Slot 17 channel 1 raw DAC codes linearity at -.1V on 6V range
Measured: -0.09937776V low limit: -0.09956024V high limit: -0.09920024V

%PASS - Slot 17 channel 1 raw DAC codes maximum linearity error on 6V range
Measured: 2.190222E-05V high limit: 0.00018V

%PASS - Slot 17 channel 1 raw DAC code binary transition 0 to 1 on 6V range
Measured: 7.320800E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transition 1 to 2 on 6V range
Measured: 1.035929E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transition 2 to 3 on 6V range
Measured: 8.051899E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transition 3 to 4 on 6V range
Measured: 1.103360E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transition 7 to 8 on 6V range
Measured: 9.895200E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transition 15 to 16 on 6V range
Measured: 1.106860E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transition 31 to 32 on 6V range
Measured: 1.019299E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transition 63 to 64 on 6V range
Measured: 1.088920E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transition 127 to 128 on 6V range
Measured: 1.070969E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transition 255 to 256 on 6V range
Measured: 1.122190E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transition 511 to 512 on 6V range
Measured: 1.052140E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transition 1023 to 1024 on 6V range
Measured: 1.004849E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transition 2047 to 2048 on 6V range
Measured: 1.133570E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transition 4095 to 4096 on 6V range
Measured: 1.029800E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transition 8191 to 8192 on 6V range
Measured: 7.846200E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transition 16383 to 16384 on 6V range

Measured: 9.833900E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transition 32767 to 32768 on 6V range

Measured: 9.619399E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transitions maximum difference on 6V range

Measured: 1.133570E-04V high limit: 2.746044E-04V

%PASS - Slot 17 channel 1 raw DAC code binary transitions minimum difference on 6V range

Measured: 7.320800E-05V low limit: -8.539550E-05V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 3.05V on 3V range

Measured: 3.050170V low limit: 3.050093V high limit: 3.050273V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 3.04995269703212V on 3V range

Measured: 3.050128V low limit: 3.050046V high limit: 3.050226V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 3.04990539406424V on 3V range

Measured: 3.050084V low limit: 3.049998V high limit: 3.050178V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 3.04985809109636V on 3V range

Measured: 3.050039V low limit: 3.049951V high limit: 3.050131V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 3.04981078812848V on 3V range

Measured: 3.049994V low limit: 3.049904V high limit: 3.050084V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 3.04966887922484V on 3V range

Measured: 3.049856V low limit: 3.049762V high limit: 3.049942V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 3.04962157625696V on 3V range

Measured: 3.049804V low limit: 3.049714V high limit: 3.049894V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 3.0492904554818V on 3V range

Measured: 3.049478V low limit: 3.049383V high limit: 3.049563V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 3.04924315251392V on 3V range

Measured: 3.049426V low limit: 3.049336V high limit: 3.049516V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 3.04853360799573V on 3V range
Measured: 3.048720V low limit: 3.048626V high limit: 3.048806V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 3.04848630502785V on 3V range
Measured: 3.048670V low limit: 3.048579V high limit: 3.048759V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 3.04701991302358V on 3V range
Measured: 3.047208V low limit: 3.047113V high limit: 3.047293V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 3.0469726100557V on 3V range
Measured: 3.047154V low limit: 3.047065V high limit: 3.047245V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 3.04399252307927V on 3V range
Measured: 3.044180V low limit: 3.044085V high limit: 3.044265V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 3.04394522011139V on 3V range
Measured: 3.044127V low limit: 3.044038V high limit: 3.044218V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 3.03793774319066V on 3V range
Measured: 3.038122V low limit: 3.038030V high limit: 3.038210V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 3.03789044022278V on 3V range
Measured: 3.038069V low limit: 3.037982V high limit: 3.038162V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 3.02582818341344V on 3V range
Measured: 3.026008V low limit: 3.025919V high limit: 3.026099V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 3.02578088044556V on 3V range
Measured: 3.025960V low limit: 3.025872V high limit: 3.026052V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 3.00160906385901V on 3V range
Measured: 3.001788V low limit: 3.001698V high limit: 3.001878V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 3.00156176089113V on 3V range
Measured: 3.001738V low limit: 3.001651V high limit: 3.001831V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 2.95317082475013V on 3V range
Measured: 2.953343V low limit: 2.953256V high limit: 2.953436V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 2.95312352178225V on 3V range
Measured: 2.953296V low limit: 2.953209V high limit: 2.953389V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 2.85629434653239V on 3V range
Measured: 2.856461V low limit: 2.856372V high limit: 2.856552V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 2.85624704356451V on 3V range
Measured: 2.856409V low limit: 2.856325V high limit: 2.856505V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 2.66254139009689V on 3V range
Measured: 2.662694V low limit: 2.662603V high limit: 2.662783V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 2.66249408712902V on 3V range
Measured: 2.662654V low limit: 2.662556V high limit: 2.662736V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 2.27503547722591V on 3V range
Measured: 2.275163V low limit: 2.275067V high limit: 2.275247V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 2.27498817425803V on 3V range
Measured: 2.275122V low limit: 2.275019V high limit: 2.275199V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 1.50002365148394V on 3V range
Measured: 1.500085V low limit: 1.499993V high limit: 1.500173V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 1.49997634851606V on 3V range
Measured: 1.500043V low limit: 1.499946V high limit: 1.500126V

%PASS - Slot 17 channel 2 raw DAC codes linearity at -.05V on 3V range
Measured: -0.05007329V low limit: -5.015360E-02V high limit: -4.997360E-02V

%PASS - Slot 17 channel 2 raw DAC codes maximum linearity error on 3V range
Measured: 1.341468E-05V high limit: 0.00009V

%PASS - Slot 17 channel 2 raw DAC code binary transition 0 to 1 on 3V range
Measured: 4.203300E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transition 1 to 2 on 3V range

Measured: 0.000043609V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transition 2 to 3 on 3V range

Measured: 4.522899E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transition 3 to 4 on 3V range

Measured: 4.487800E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transition 7 to 8 on 3V range

Measured: 5.197200E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transition 15 to 16 on 3V range

Measured: 5.210299E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transition 31 to 32 on 3V range

Measured: 5.013300E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transition 63 to 64 on 3V range

Measured: 5.407300E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transition 127 to 128 on 3V range

Measured: 5.337300E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transition 255 to 256 on 3V range

Measured: 5.284800E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transition 511 to 512 on 3V range

Measured: 4.851299E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transition 1023 to 1024 on 3V range

Measured: 4.995800E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transition 2047 to 2048 on 3V range

Measured: 4.680599E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transition 4095 to 4096 on 3V range

Measured: 5.219099E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transition 8191 to 8192 on 3V range
Measured: 4.076300E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transition 16383 to 16384 on 3V range
Measured: 4.071900E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transition 32767 to 32768 on 3V range
Measured: 4.220799E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transitions maximum difference on 3V range
Measured: 5.407300E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transitions minimum difference on 3V range
Measured: 4.071900E-05V low limit: -4.269775E-05V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 6.1V on 6V range
Measured: 6.101231V low limit: 6.101055V high limit: 6.101415V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 6.09990539406424V on 6V range
Measured: 6.101146V low limit: 6.100960V high limit: 6.101320V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 6.09981078812848V on 6V range
Measured: 6.101049V low limit: 6.100865V high limit: 6.101225V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 6.09971618219272V on 6V range
Measured: 6.100959V low limit: 6.100771V high limit: 6.101131V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 6.09962157625696V on 6V range
Measured: 6.100862V low limit: 6.100676V high limit: 6.101036V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 6.09933775844968V on 6V range
Measured: 6.100590V low limit: 6.100392V high limit: 6.100752V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 6.09924315251392V on 6V range
Measured: 6.100483V low limit: 6.100298V high limit: 6.100658V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 6.09858091096361V on 6V range
Measured: 6.099828V low limit: 6.099635V high limit: 6.099995V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 6.09848630502785V on 6V range
Measured: 6.099724V low limit: 6.099541V high limit: 6.099901V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 6.09706721599146V on 6V range
Measured: 6.098317V low limit: 6.098122V high limit: 6.098482V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 6.0969726100557V on 6V range
Measured: 6.098206V low limit: 6.098027V high limit: 6.098387V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 6.09403982604715V on 6V range
Measured: 6.095281V low limit: 6.095094V high limit: 6.095454V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 6.09394522011139V on 6V range
Measured: 6.095181V low limit: 6.094999V high limit: 6.095359V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 6.08798504615854V on 6V range
Measured: 6.089219V low limit: 6.089038V high limit: 6.089398V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 6.08789044022278V on 6V range
Measured: 6.089115V low limit: 6.088944V high limit: 6.089304V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 6.07587548638132V on 6V range
Measured: 6.077111V low limit: 6.076927V high limit: 6.077287V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 6.07578088044556V on 6V range
Measured: 6.077008V low limit: 6.076833V high limit: 6.077193V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 6.05165636682689V on 6V range
Measured: 6.052883V low limit: 6.052705V high limit: 6.053065V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 6.05156176089113V on 6V range
Measured: 6.052785V low limit: 6.052610V high limit: 6.052970V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 6.00321812771801V on 6V range
Measured: 6.004435V low limit: 6.004261V high limit: 6.004621V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 6.00312352178225V on 6V range

Measured: 6.004334V low limit: 6.004166V high limit: 6.004526V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 5.90634164950027V on 6V range
Measured: 5.907544V low limit: 5.907372V high limit: 5.907732V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 5.90624704356451V on 6V range
Measured: 5.907452V low limit: 5.907278V high limit: 5.907638V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 5.71258869306477V on 6V range
Measured: 5.713767V low limit: 5.713596V high limit: 5.713956V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 5.71249408712902V on 6V range
Measured: 5.713663V low limit: 5.713501V high limit: 5.713861V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 5.32508278019379V on 6V range
Measured: 5.326213V low limit: 5.326042V high limit: 5.326402V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 5.32498817425803V on 6V range
Measured: 5.326126V low limit: 5.325947V high limit: 5.326307V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 4.55007095445182V on 6V range
Measured: 4.551112V low limit: 4.550934V high limit: 4.551294V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 4.54997634851606V on 6V range
Measured: 4.551030V low limit: 4.550840V high limit: 4.551200V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 3.00004730296788V on 6V range
Measured: 3.000886V low limit: 3.000719V high limit: 3.001079V

%PASS - Slot 17 channel 2 raw DAC codes linearity at 2.99995269703212V on 6V range
Measured: 3.000801V low limit: 3.000625V high limit: 3.000985V

%PASS - Slot 17 channel 2 raw DAC codes linearity at -.1V on 6V range
Measured: -0.09952004V low limit: -9.971019E-02V high limit: -9.935019E-02V

%PASS - Slot 17 channel 2 raw DAC codes maximum linearity error on 6V range
Measured: 1.781186E-05V high limit: 0.00018V

%PASS - Slot 17 channel 2 raw DAC code binary transition 0 to 1 on 6V range
Measured: 8.520400E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transition 1 to 2 on 6V range
Measured: 9.628099E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transition 2 to 3 on 6V range
Measured: 9.050199E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transition 3 to 4 on 6V range
Measured: 9.689500E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transition 7 to 8 on 6V range
Measured: 1.067450E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transition 15 to 16 on 6V range
Measured: 1.044690E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transition 31 to 32 on 6V range
Measured: 1.108610E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transition 63 to 64 on 6V range
Measured: 1.003099E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transition 127 to 128 on 6V range
Measured: 1.040749E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transition 255 to 256 on 6V range
Measured: 1.029800E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transition 511 to 512 on 6V range
Measured: 9.833900E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transition 1023 to 1024 on 6V range
Measured: 1.008349E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transition 2047 to 2048 on 6V range
Measured: 9.234100E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transition 4095 to 4096 on 6V range
Measured: 1.039870E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transition 8191 to 8192 on 6V range
Measured: 8.717399E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transition 16383 to 16384 on 6V range
Measured: 8.235800E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transition 32767 to 32768 on 6V range
Measured: 8.454799E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transitions maximum difference on 6V range
Measured: 1.108610E-04V high limit: 2.746044E-04V

%PASS - Slot 17 channel 2 raw DAC code binary transitions minimum difference on 6V range
Measured: 8.235800E-05V low limit: -8.539550E-05V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 3.05V on 3V range
Measured: 3.051233V low limit: 3.051148V high limit: 3.051328V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 3.04995269703212V on 3V range
Measured: 3.051191V low limit: 3.051101V high limit: 3.051281V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 3.04990539406424V on 3V range
Measured: 3.051144V low limit: 3.051054V high limit: 3.051234V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 3.04985809109636V on 3V range
Measured: 3.051098V low limit: 3.051006V high limit: 3.051186V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 3.04981078812848V on 3V range
Measured: 3.051047V low limit: 3.050959V high limit: 3.051139V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 3.04966887922484V on 3V range
Measured: 3.050911V low limit: 3.050817V high limit: 3.050997V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 3.04962157625696V on 3V range

Measured: 3.050858V low limit: 3.050770V high limit: 3.050950V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 3.0492904554818V on 3V range

Measured: 3.050531V low limit: 3.050439V high limit: 3.050619V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 3.04924315251392V on 3V range

Measured: 3.050480V low limit: 3.050391V high limit: 3.050571V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 3.04853360799573V on 3V range

Measured: 3.049776V low limit: 3.049682V high limit: 3.049862V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 3.04848630502785V on 3V range

Measured: 3.049723V low limit: 3.049634V high limit: 3.049814V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 3.04701991302358V on 3V range

Measured: 3.048262V low limit: 3.048167V high limit: 3.048347V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 3.0469726100557V on 3V range

Measured: 3.048207V low limit: 3.048120V high limit: 3.048300V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 3.04399252307927V on 3V range

Measured: 3.045231V low limit: 3.045139V high limit: 3.045319V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 3.04394522011139V on 3V range

Measured: 3.045183V low limit: 3.045092V high limit: 3.045272V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 3.03793774319066V on 3V range

Measured: 3.039174V low limit: 3.039083V high limit: 3.039263V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 3.03789044022278V on 3V range

Measured: 3.039126V low limit: 3.039036V high limit: 3.039216V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 3.02582818341344V on 3V range

Measured: 3.027063V low limit: 3.026971V high limit: 3.027151V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 3.02578088044556V on 3V range

Measured: 3.027015V low limit: 3.026923V high limit: 3.027103V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 3.00160906385901V on 3V range
Measured: 3.002839V low limit: 3.002746V high limit: 3.002926V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 3.00156176089113V on 3V range
Measured: 3.002780V low limit: 3.002699V high limit: 3.002879V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 2.95317082475013V on 3V range
Measured: 2.954382V low limit: 2.954296V high limit: 2.954476V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 2.95312352178225V on 3V range
Measured: 2.954333V low limit: 2.954249V high limit: 2.954429V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 2.85629434653239V on 3V range
Measured: 2.857481V low limit: 2.857397V high limit: 2.857577V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 2.85624704356451V on 3V range
Measured: 2.857434V low limit: 2.857349V high limit: 2.857529V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 2.66254139009689V on 3V range
Measured: 2.663687V low limit: 2.663597V high limit: 2.663777V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 2.66249408712902V on 3V range
Measured: 2.663642V low limit: 2.663550V high limit: 2.663730V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 2.27503547722591V on 3V range
Measured: 2.276094V low limit: 2.275999V high limit: 2.276179V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 2.27498817425803V on 3V range
Measured: 2.276052V low limit: 2.275952V high limit: 2.276132V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 1.50002365148394V on 3V range
Measured: 1.500897V low limit: 1.500803V high limit: 1.500983V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 1.49997634851606V on 3V range
Measured: 1.500848V low limit: 1.500756V high limit: 1.500936V

%PASS - Slot 17 channel 3 raw DAC codes linearity at -.05V on 3V range
Measured: -0.04950530V low limit: -4.958916E-02V high limit: -4.940916E-02V

%PASS - Slot 17 channel 3 raw DAC codes maximum linearity error on 3V range
Measured: 1.019777E-05V high limit: 0.00009V

%PASS - Slot 17 channel 3 raw DAC code binary transition 0 to 1 on 3V range
Measured: 4.150800E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition 1 to 2 on 3V range
Measured: 4.763699E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition 2 to 3 on 3V range
Measured: 4.601700E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition 3 to 4 on 3V range
Measured: 5.078999E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition 7 to 8 on 3V range
Measured: 5.297899E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition 15 to 16 on 3V range
Measured: 5.100899E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition 31 to 32 on 3V range
Measured: 5.345999E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition 63 to 64 on 3V range
Measured: 5.459899E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition 127 to 128 on 3V range
Measured: 4.838100E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition 255 to 256 on 3V range
Measured: 4.728700E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition 511 to 512 on 3V range
Measured: 4.833800E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition 1023 to 1024 on 3V range

Measured: 5.906499E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition 2047 to 2048 on 3V range
Measured: 4.938800E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition 4095 to 4096 on 3V range
Measured: 4.654200E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition 8191 to 8192 on 3V range
Measured: 4.430999E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition 16383 to 16384 on 3V range
Measured: 4.190199E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition 32767 to 32768 on 3V range
Measured: 4.820599E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transitions maximum difference on 3V range
Measured: 5.906499E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transitions minimum difference on 3V range
Measured: 4.150800E-05V low limit: -4.269775E-05V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 6.1V on 6V range
Measured: 6.103073V low limit: 6.102898V high limit: 6.103258V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 6.09990539406424V on 6V range
Measured: 6.102982V low limit: 6.102804V high limit: 6.103164V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 6.09981078812848V on 6V range
Measured: 6.102892V low limit: 6.102709V high limit: 6.103069V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 6.09971618219272V on 6V range
Measured: 6.102805V low limit: 6.102614V high limit: 6.102974V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 6.09962157625696V on 6V range
Measured: 6.102705V low limit: 6.102520V high limit: 6.102880V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 6.09933775844968V on 6V range
Measured: 6.102429V low limit: 6.102236V high limit: 6.102596V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 6.09924315251392V on 6V range
Measured: 6.102322V low limit: 6.102141V high limit: 6.102501V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 6.09858091096361V on 6V range
Measured: 6.101671V low limit: 6.101479V high limit: 6.101839V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 6.09848630502785V on 6V range
Measured: 6.101562V low limit: 6.101384V high limit: 6.101744V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 6.09706721599146V on 6V range
Measured: 6.100150V low limit: 6.099965V high limit: 6.100325V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 6.0969726100557V on 6V range
Measured: 6.100050V low limit: 6.099870V high limit: 6.100230V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 6.09403982604715V on 6V range
Measured: 6.097122V low limit: 6.096936V high limit: 6.097296V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 6.09394522011139V on 6V range
Measured: 6.097014V low limit: 6.096842V high limit: 6.097202V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 6.08798504615854V on 6V range
Measured: 6.091062V low limit: 6.090880V high limit: 6.091240V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 6.08789044022278V on 6V range
Measured: 6.090968V low limit: 6.090785V high limit: 6.091145V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 6.07587548638132V on 6V range
Measured: 6.078950V low limit: 6.078767V high limit: 6.079127V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 6.07578088044556V on 6V range
Measured: 6.078851V low limit: 6.078672V high limit: 6.079032V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 6.05165636682689V on 6V range
Measured: 6.054722V low limit: 6.054541V high limit: 6.054901V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 6.05156176089113V on 6V range
Measured: 6.054625V low limit: 6.054447V high limit: 6.054807V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 6.00321812771801V on 6V range
Measured: 6.006283V low limit: 6.006090V high limit: 6.006450V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 6.00312352178225V on 6V range
Measured: 6.006161V low limit: 6.005995V high limit: 6.006355V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 5.90634164950027V on 6V range
Measured: 5.909365V low limit: 5.909186V high limit: 5.909546V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 5.90624704356451V on 6V range
Measured: 5.909261V low limit: 5.909092V high limit: 5.909452V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 5.71258869306477V on 6V range
Measured: 5.715544V low limit: 5.715380V high limit: 5.715740V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 5.71249408712902V on 6V range
Measured: 5.715456V low limit: 5.715285V high limit: 5.715645V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 5.32508278019379V on 6V range
Measured: 5.327937V low limit: 5.327766V high limit: 5.328126V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 5.32498817425803V on 6V range
Measured: 5.327851V low limit: 5.327672V high limit: 5.328032V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 4.55007095445182V on 6V range
Measured: 4.552717V low limit: 4.552540V high limit: 4.552900V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 4.54997634851606V on 6V range
Measured: 4.552633V low limit: 4.552445V high limit: 4.552805V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 3.00004730296788V on 6V range
Measured: 3.002260V low limit: 3.002087V high limit: 3.002447V

%PASS - Slot 17 channel 3 raw DAC codes linearity at 2.99995269703212V on 6V range

Measured: 3.002167V low limit: 3.001992V high limit: 3.002352V

%PASS - Slot 17 channel 3 raw DAC codes linearity at -.1V on 6V range

Measured: -0.09863031V low limit: -9.881836E-02V high limit: -9.845836E-02V

%PASS - Slot 17 channel 3 raw DAC codes maximum linearity error on 6V range

Measured: 1.533020E-05V high limit: 0.00018V

%PASS - Slot 17 channel 3 raw DAC code binary transition 0 to 1 on 6V range

Measured: 9.089600E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition 1 to 2 on 6V range

Measured: 8.984499E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition 2 to 3 on 6V range

Measured: 8.774400E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition 3 to 4 on 6V range

Measured: 9.925799E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition 7 to 8 on 6V range

Measured: 1.066149E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition 15 to 16 on 6V range

Measured: 1.090660E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition 31 to 32 on 6V range

Measured: 1.000469E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition 63 to 64 on 6V range

Measured: 1.082779E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition 127 to 128 on 6V range

Measured: 9.387400E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition 255 to 256 on 6V range

Measured: 9.886399E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition 511 to 512 on 6V range
Measured: 9.733199E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition 1023 to 1024 on 6V range
Measured: 1.218949E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition 2047 to 2048 on 6V range
Measured: 1.044690E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition 4095 to 4096 on 6V range
Measured: 8.813699E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition 8191 to 8192 on 6V range
Measured: 8.581700E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition 16383 to 16384 on 6V range
Measured: 8.459099E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transition 32767 to 32768 on 6V range
Measured: 9.312900E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transitions maximum difference on 6V range
Measured: 1.218949E-04V high limit: 2.746044E-04V

%PASS - Slot 17 channel 3 raw DAC code binary transitions minimum difference on 6V range
Measured: 8.459099E-05V low limit: -8.539550E-05V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 3.05V on 3V range
Measured: 3.051093V low limit: 3.051007V high limit: 3.051187V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 3.04995269703212V on 3V range
Measured: 3.051049V low limit: 3.050959V high limit: 3.051139V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 3.04990539406424V on 3V range
Measured: 3.050995V low limit: 3.050912V high limit: 3.051092V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 3.04985809109636V on 3V range
Measured: 3.050953V low limit: 3.050865V high limit: 3.051045V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 3.04981078812848V on 3V range
Measured: 3.050906V low limit: 3.050817V high limit: 3.050997V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 3.04966887922484V on 3V range
Measured: 3.050760V low limit: 3.050675V high limit: 3.050855V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 3.04962157625696V on 3V range
Measured: 3.050719V low limit: 3.050628V high limit: 3.050808V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 3.0492904554818V on 3V range
Measured: 3.050384V low limit: 3.050297V high limit: 3.050477V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 3.04924315251392V on 3V range
Measured: 3.050339V low limit: 3.050250V high limit: 3.050430V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 3.04853360799573V on 3V range
Measured: 3.049625V low limit: 3.049540V high limit: 3.049720V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 3.04848630502785V on 3V range
Measured: 3.049583V low limit: 3.049493V high limit: 3.049673V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 3.04701991302358V on 3V range
Measured: 3.048116V low limit: 3.048026V high limit: 3.048206V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 3.0469726100557V on 3V range
Measured: 3.048069V low limit: 3.047979V high limit: 3.048159V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 3.04399252307927V on 3V range
Measured: 3.045087V low limit: 3.044998V high limit: 3.045178V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 3.04394522011139V on 3V range
Measured: 3.045040V low limit: 3.044951V high limit: 3.045131V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 3.03793774319066V on 3V range
Measured: 3.039029V low limit: 3.038942V high limit: 3.039122V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 3.03789044022278V on 3V range

Measured: 3.038985V low limit: 3.038895V high limit: 3.039075V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 3.02582818341344V on 3V range
Measured: 3.026919V low limit: 3.026831V high limit: 3.027011V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 3.02578088044556V on 3V range
Measured: 3.026874V low limit: 3.026784V high limit: 3.026964V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 3.00160906385901V on 3V range
Measured: 3.002698V low limit: 3.002608V high limit: 3.002788V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 3.00156176089113V on 3V range
Measured: 3.002647V low limit: 3.002561V high limit: 3.002741V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 2.95317082475013V on 3V range
Measured: 2.954248V low limit: 2.954162V high limit: 2.954342V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 2.95312352178225V on 3V range
Measured: 2.954205V low limit: 2.954115V high limit: 2.954295V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 2.85629434653239V on 3V range
Measured: 2.857357V low limit: 2.857271V high limit: 2.857451V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 2.85624704356451V on 3V range
Measured: 2.857321V low limit: 2.857224V high limit: 2.857404V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 2.66254139009689V on 3V range
Measured: 2.663579V low limit: 2.663488V high limit: 2.663668V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 2.66249408712902V on 3V range
Measured: 2.663545V low limit: 2.663441V high limit: 2.663621V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 2.27503547722591V on 3V range
Measured: 2.276020V low limit: 2.275923V high limit: 2.276103V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 2.27498817425803V on 3V range
Measured: 2.275989V low limit: 2.275876V high limit: 2.276056V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 1.50002365148394V on 3V range
Measured: 1.500883V low limit: 1.500792V high limit: 1.500972V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 1.49997634851606V on 3V range
Measured: 1.500841V low limit: 1.500745V high limit: 1.500925V

%PASS - Slot 17 channel 4 raw DAC codes linearity at -.05V on 3V range
Measured: -0.04939250V low limit: -4.946954E-02V high limit: -4.928954E-02V

%PASS - Slot 17 channel 4 raw DAC codes maximum linearity error on 3V range
Measured: 2.307256E-05V high limit: 0.00009V

%PASS - Slot 17 channel 4 raw DAC code binary transition 0 to 1 on 3V range
Measured: 4.417800E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition 1 to 2 on 3V range
Measured: 5.389799E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition 2 to 3 on 3V range
Measured: 4.194600E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition 3 to 4 on 3V range
Measured: 4.759299E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition 7 to 8 on 3V range
Measured: 4.163899E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition 15 to 16 on 3V range
Measured: 4.505400E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition 31 to 32 on 3V range
Measured: 4.181400E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition 63 to 64 on 3V range
Measured: 4.623600E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition 127 to 128 on 3V range
Measured: 4.746300E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition 255 to 256 on 3V range
Measured: 4.404700E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition 511 to 512 on 3V range
Measured: 4.553499E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition 1023 to 1024 on 3V range
Measured: 5.131499E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition 2047 to 2048 on 3V range
Measured: 4.282100E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition 4095 to 4096 on 3V range
Measured: 3.599100E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition 8191 to 8192 on 3V range
Measured: 3.323199E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition 16383 to 16384 on 3V range
Measured: 3.104299E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition 32767 to 32768 on 3V range
Measured: 4.286499E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transitions maximum difference on 3V range
Measured: 5.389799E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transitions minimum difference on 3V range
Measured: 3.104299E-05V low limit: -4.269775E-05V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 6.1V on 6V range
Measured: 6.101304V low limit: 6.101124V high limit: 6.101484V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 6.09990539406424V on 6V range
Measured: 6.101213V low limit: 6.101029V high limit: 6.101389V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 6.09981078812848V on 6V range

Measured: 6.101117V low limit: 6.100934V high limit: 6.101294V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 6.09971618219272V on 6V range
Measured: 6.101021V low limit: 6.100840V high limit: 6.101200V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 6.09962157625696V on 6V range
Measured: 6.100931V low limit: 6.100745V high limit: 6.101105V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 6.09933775844968V on 6V range
Measured: 6.100638V low limit: 6.100461V high limit: 6.100821V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 6.09924315251392V on 6V range
Measured: 6.100545V low limit: 6.100367V high limit: 6.100727V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 6.09858091096361V on 6V range
Measured: 6.099888V low limit: 6.099704V high limit: 6.100064V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 6.09848630502785V on 6V range
Measured: 6.099787V low limit: 6.099610V high limit: 6.099970V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 6.09706721599146V on 6V range
Measured: 6.098367V low limit: 6.098191V high limit: 6.098551V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 6.0969726100557V on 6V range
Measured: 6.098277V low limit: 6.098096V high limit: 6.098456V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 6.09403982604715V on 6V range
Measured: 6.095339V low limit: 6.095163V high limit: 6.095523V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 6.09394522011139V on 6V range
Measured: 6.095253V low limit: 6.095068V high limit: 6.095428V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 6.08798504615854V on 6V range
Measured: 6.089289V low limit: 6.089108V high limit: 6.089468V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 6.08789044022278V on 6V range
Measured: 6.089194V low limit: 6.089013V high limit: 6.089373V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 6.07587548638132V on 6V range
Measured: 6.077171V low limit: 6.076997V high limit: 6.077357V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 6.07578088044556V on 6V range
Measured: 6.077082V low limit: 6.076903V high limit: 6.077263V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 6.05165636682689V on 6V range
Measured: 6.052961V low limit: 6.052776V high limit: 6.053136V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 6.05156176089113V on 6V range
Measured: 6.052868V low limit: 6.052682V high limit: 6.053042V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 6.00321812771801V on 6V range
Measured: 6.004519V low limit: 6.004335V high limit: 6.004695V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 6.00312352178225V on 6V range
Measured: 6.004412V low limit: 6.004240V high limit: 6.004600V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 5.90634164950027V on 6V range
Measured: 5.907622V low limit: 5.907451V high limit: 5.907811V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 5.90624704356451V on 6V range
Measured: 5.907535V low limit: 5.907357V high limit: 5.907717V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 5.71258869306477V on 6V range
Measured: 5.713858V low limit: 5.713685V high limit: 5.714045V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 5.71249408712902V on 6V range
Measured: 5.713777V low limit: 5.713590V high limit: 5.713950V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 5.32508278019379V on 6V range
Measured: 5.326321V low limit: 5.326151V high limit: 5.326511V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 5.32498817425803V on 6V range
Measured: 5.326254V low limit: 5.326056V high limit: 5.326416V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 4.55007095445182V on 6V range
Measured: 4.551256V low limit: 4.551084V high limit: 4.551444V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 4.54997634851606V on 6V range
Measured: 4.551188V low limit: 4.550989V high limit: 4.551349V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 3.00004730296788V on 6V range
Measured: 3.001106V low limit: 3.000949V high limit: 3.001309V

%PASS - Slot 17 channel 4 raw DAC codes linearity at 2.99995269703212V on 6V range
Measured: 3.001026V low limit: 3.000855V high limit: 3.001215V

%PASS - Slot 17 channel 4 raw DAC codes linearity at -.1V on 6V range
Measured: -0.09912654V low limit: -9.931904E-02V high limit: -9.895904E-02V

%PASS - Slot 17 channel 4 raw DAC codes maximum linearity error on 6V range
Measured: 2.304195E-05V high limit: 0.00018V

%PASS - Slot 17 channel 4 raw DAC code binary transition 0 to 1 on 6V range
Measured: 9.036999E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition 1 to 2 on 6V range
Measured: 9.571300E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition 2 to 3 on 6V range
Measured: 9.654399E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition 3 to 4 on 6V range
Measured: 8.966999E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition 7 to 8 on 6V range
Measured: 9.330500E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition 15 to 16 on 6V range
Measured: 1.008789E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition 31 to 32 on 6V range
Measured: 9.019499E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition 63 to 64 on 6V range

Measured: 8.607999E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition 127 to 128 on 6V range

Measured: 9.571199E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition 255 to 256 on 6V range

Measured: 8.888200E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition 511 to 512 on 6V range

Measured: 9.317299E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition 1023 to 1024 on 6V range

Measured: 1.068780E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition 2047 to 2048 on 6V range

Measured: 8.642999E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition 4095 to 4096 on 6V range

Measured: 8.051899E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition 8191 to 8192 on 6V range

Measured: 6.756000E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition 16383 to 16384 on 6V range

Measured: 6.747100E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transition 32767 to 32768 on 6V range

Measured: 8.043200E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transitions maximum difference on 6V range

Measured: 1.068780E-04V high limit: 2.746044E-04V

%PASS - Slot 17 channel 4 raw DAC code binary transitions minimum difference on 6V range

Measured: 6.747100E-05V low limit: -8.539550E-05V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 3.05V on 3V range

Measured: 3.051094V low limit: 3.051001V high limit: 3.051181V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 3.04995269703212V on 3V range
Measured: 3.051043V low limit: 3.050954V high limit: 3.051134V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 3.04990539406424V on 3V range
Measured: 3.050996V low limit: 3.050906V high limit: 3.051086V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 3.04985809109636V on 3V range
Measured: 3.050947V low limit: 3.050859V high limit: 3.051039V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 3.04981078812848V on 3V range
Measured: 3.050900V low limit: 3.050812V high limit: 3.050992V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 3.04966887922484V on 3V range
Measured: 3.050758V low limit: 3.050670V high limit: 3.050850V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 3.04962157625696V on 3V range
Measured: 3.050714V low limit: 3.050623V high limit: 3.050803V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 3.0492904554818V on 3V range
Measured: 3.050380V low limit: 3.050291V high limit: 3.050471V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 3.04924315251392V on 3V range
Measured: 3.050333V low limit: 3.050244V high limit: 3.050424V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 3.04853360799573V on 3V range
Measured: 3.049622V low limit: 3.049534V high limit: 3.049714V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 3.04848630502785V on 3V range
Measured: 3.049577V low limit: 3.049487V high limit: 3.049667V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 3.04701991302358V on 3V range
Measured: 3.048108V low limit: 3.048021V high limit: 3.048201V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 3.0469726100557V on 3V range
Measured: 3.048063V low limit: 3.047973V high limit: 3.048153V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 3.04399252307927V on 3V range
Measured: 3.045077V low limit: 3.044993V high limit: 3.045173V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 3.04394522011139V on 3V range
Measured: 3.045034V low limit: 3.044946V high limit: 3.045126V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 3.03793774319066V on 3V range
Measured: 3.039024V low limit: 3.038937V high limit: 3.039117V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 3.03789044022278V on 3V range
Measured: 3.038982V low limit: 3.038890V high limit: 3.039070V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 3.02582818341344V on 3V range
Measured: 3.026911V low limit: 3.026826V high limit: 3.027006V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 3.02578088044556V on 3V range
Measured: 3.026868V low limit: 3.026779V high limit: 3.026959V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 3.00160906385901V on 3V range
Measured: 3.002689V low limit: 3.002605V high limit: 3.002785V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 3.00156176089113V on 3V range
Measured: 3.002645V low limit: 3.002557V high limit: 3.002737V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 2.95317082475013V on 3V range
Measured: 2.954244V low limit: 2.954161V high limit: 2.954341V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 2.95312352178225V on 3V range
Measured: 2.954206V low limit: 2.954114V high limit: 2.954294V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 2.85629434653239V on 3V range
Measured: 2.857360V low limit: 2.857273V high limit: 2.857453V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 2.85624704356451V on 3V range
Measured: 2.857324V low limit: 2.857226V high limit: 2.857406V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 2.66254139009689V on 3V range
Measured: 2.663593V low limit: 2.663499V high limit: 2.663679V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 2.66249408712902V on 3V range
Measured: 2.663550V low limit: 2.663451V high limit: 2.663631V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 2.27503547722591V on 3V range
Measured: 2.276047V low limit: 2.275949V high limit: 2.276129V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 2.27498817425803V on 3V range
Measured: 2.276009V low limit: 2.275901V high limit: 2.276081V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 1.50002365148394V on 3V range
Measured: 1.500941V low limit: 1.500849V high limit: 1.501029V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 1.49997634851606V on 3V range
Measured: 1.500896V low limit: 1.500802V high limit: 1.500982V

%PASS - Slot 17 channel 5 raw DAC codes linearity at -.05V on 3V range
Measured: -0.04927100V low limit: -4.934973E-02V high limit: -4.916973E-02V

%PASS - Slot 17 channel 5 raw DAC codes maximum linearity error on 3V range
Measured: 1.735048E-05V high limit: 0.00009V

%PASS - Slot 17 channel 5 raw DAC code binary transition 0 to 1 on 3V range
Measured: 5.135899E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition 1 to 2 on 3V range
Measured: 4.658600E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition 2 to 3 on 3V range
Measured: 4.921399E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition 3 to 4 on 3V range
Measured: 4.667400E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition 7 to 8 on 3V range
Measured: 4.417900E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition 15 to 16 on 3V range
Measured: 4.715599E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition 31 to 32 on 3V range
Measured: 4.426600E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition 63 to 64 on 3V range
Measured: 4.492300E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition 127 to 128 on 3V range
Measured: 4.303999E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition 255 to 256 on 3V range
Measured: 4.124499E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition 511 to 512 on 3V range
Measured: 4.264599E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition 1023 to 1024 on 3V range
Measured: 4.439699E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition 2047 to 2048 on 3V range
Measured: 3.839900E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition 4095 to 4096 on 3V range
Measured: 3.647300E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition 8191 to 8192 on 3V range
Measured: 4.317099E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition 16383 to 16384 on 3V range
Measured: 3.818000E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition 32767 to 32768 on 3V range
Measured: 4.487900E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transitions maximum difference on 3V range
Measured: 5.135899E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transitions minimum difference on 3V range

Measured: 3.647300E-05V low limit: -4.269775E-05V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 6.1V on 6V range

Measured: 6.101536V low limit: 6.101348V high limit: 6.101708V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 6.09990539406424V on 6V range

Measured: 6.101435V low limit: 6.101254V high limit: 6.101614V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 6.09981078812848V on 6V range

Measured: 6.101343V low limit: 6.101159V high limit: 6.101519V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 6.09971618219272V on 6V range

Measured: 6.101242V low limit: 6.101065V high limit: 6.101425V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 6.09962157625696V on 6V range

Measured: 6.101146V low limit: 6.100970V high limit: 6.101330V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 6.09933775844968V on 6V range

Measured: 6.100862V low limit: 6.100686V high limit: 6.101046V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 6.09924315251392V on 6V range

Measured: 6.100770V low limit: 6.100592V high limit: 6.100952V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 6.09858091096361V on 6V range

Measured: 6.100101V low limit: 6.099929V high limit: 6.100289V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 6.09848630502785V on 6V range

Measured: 6.100017V low limit: 6.099835V high limit: 6.100195V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 6.09706721599146V on 6V range

Measured: 6.098593V low limit: 6.098416V high limit: 6.098776V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 6.0969726100557V on 6V range

Measured: 6.098500V low limit: 6.098321V high limit: 6.098681V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 6.09403982604715V on 6V range

Measured: 6.095564V low limit: 6.095389V high limit: 6.095749V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 6.09394522011139V on 6V range
Measured: 6.095471V low limit: 6.095294V high limit: 6.095654V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 6.08798504615854V on 6V range
Measured: 6.089504V low limit: 6.089334V high limit: 6.089694V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 6.08789044022278V on 6V range
Measured: 6.089423V low limit: 6.089239V high limit: 6.089599V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 6.07587548638132V on 6V range
Measured: 6.077396V low limit: 6.077225V high limit: 6.077585V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 6.07578088044556V on 6V range
Measured: 6.077313V low limit: 6.077131V high limit: 6.077491V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 6.05165636682689V on 6V range
Measured: 6.053182V low limit: 6.053007V high limit: 6.053367V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 6.05156176089113V on 6V range
Measured: 6.053099V low limit: 6.052913V high limit: 6.053273V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 6.00321812771801V on 6V range
Measured: 6.004748V low limit: 6.004572V high limit: 6.004932V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 6.00312352178225V on 6V range
Measured: 6.004656V low limit: 6.004477V high limit: 6.004837V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 5.90634164950027V on 6V range
Measured: 5.907875V low limit: 5.907700V high limit: 5.908060V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 5.90624704356451V on 6V range
Measured: 5.907790V low limit: 5.907605V high limit: 5.907965V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 5.71258869306477V on 6V range
Measured: 5.714131V low limit: 5.713957V high limit: 5.714317V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 5.71249408712902V on 6V range
Measured: 5.714060V low limit: 5.713863V high limit: 5.714223V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 5.32508278019379V on 6V range
Measured: 5.326652V low limit: 5.326471V high limit: 5.326831V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 5.32498817425803V on 6V range
Measured: 5.326571V low limit: 5.326377V high limit: 5.326737V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 4.55007095445182V on 6V range
Measured: 4.551681V low limit: 4.551499V high limit: 4.551859V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 4.54997634851606V on 6V range
Measured: 4.551605V low limit: 4.551405V high limit: 4.551765V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 3.00004730296788V on 6V range
Measured: 3.001728V low limit: 3.001556V high limit: 3.001916V

%PASS - Slot 17 channel 5 raw DAC codes linearity at 2.99995269703212V on 6V range
Measured: 3.001639V low limit: 3.001461V high limit: 3.001821V

%PASS - Slot 17 channel 5 raw DAC codes linearity at -.1V on 6V range
Measured: -0.09815376V low limit: -9.833089E-02V high limit: -9.797089E-02V

%PASS - Slot 17 channel 5 raw DAC codes maximum linearity error on 6V range
Measured: 2.035353E-05V high limit: 0.00018V

%PASS - Slot 17 channel 5 raw DAC code binary transition 0 to 1 on 6V range
Measured: 1.007909E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition 1 to 2 on 6V range
Measured: 9.229700E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition 2 to 3 on 6V range
Measured: 1.010109E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition 3 to 4 on 6V range
Measured: 9.549299E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition 7 to 8 on 6V range

Measured: 9.207799E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition 15 to 16 on 6V range

Measured: 8.415300E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition 31 to 32 on 6V range

Measured: 9.273499E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition 63 to 64 on 6V range

Measured: 9.304100E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition 127 to 128 on 6V range

Measured: 8.148200E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition 255 to 256 on 6V range

Measured: 8.270799E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition 511 to 512 on 6V range

Measured: 8.279600E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition 1023 to 1024 on 6V range

Measured: 9.199100E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition 2047 to 2048 on 6V range

Measured: 8.428399E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition 4095 to 4096 on 6V range

Measured: 7.097399E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition 8191 to 8192 on 6V range

Measured: 8.121999E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition 16383 to 16384 on 6V range

Measured: 7.587800E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transition 32767 to 32768 on 6V range

Measured: 8.905699E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transitions maximum difference on 6V range
Measured: 1.010109E-04V high limit: 2.746044E-04V

%PASS - Slot 17 channel 5 raw DAC code binary transitions minimum difference on 6V range
Measured: 7.097399E-05V low limit: -8.539550E-05V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 3.05V on 3V range
Measured: 3.051547V low limit: 3.051465V high limit: 3.051645V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 3.04995269703212V on 3V range
Measured: 3.051497V low limit: 3.051418V high limit: 3.051598V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 3.04990539406424V on 3V range
Measured: 3.051450V low limit: 3.051370V high limit: 3.051550V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 3.04985809109636V on 3V range
Measured: 3.051406V low limit: 3.051323V high limit: 3.051503V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 3.04981078812848V on 3V range
Measured: 3.051362V low limit: 3.051276V high limit: 3.051456V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 3.04966887922484V on 3V range
Measured: 3.051221V low limit: 3.051134V high limit: 3.051314V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 3.04962157625696V on 3V range
Measured: 3.051171V low limit: 3.051086V high limit: 3.051266V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 3.0492904554818V on 3V range
Measured: 3.050842V low limit: 3.050755V high limit: 3.050935V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 3.04924315251392V on 3V range
Measured: 3.050794V low limit: 3.050708V high limit: 3.050888V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 3.04853360799573V on 3V range
Measured: 3.050087V low limit: 3.049998V high limit: 3.050178V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 3.04848630502785V on 3V range
Measured: 3.050038V low limit: 3.049951V high limit: 3.050131V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 3.04701991302358V on 3V range
Measured: 3.048571V low limit: 3.048484V high limit: 3.048664V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 3.0469726100557V on 3V range
Measured: 3.048525V low limit: 3.048437V high limit: 3.048617V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 3.04399252307927V on 3V range
Measured: 3.045546V low limit: 3.045457V high limit: 3.045637V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 3.04394522011139V on 3V range
Measured: 3.045498V low limit: 3.045409V high limit: 3.045589V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 3.03793774319066V on 3V range
Measured: 3.039495V low limit: 3.039401V high limit: 3.039581V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 3.03789044022278V on 3V range
Measured: 3.039446V low limit: 3.039354V high limit: 3.039534V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 3.02582818341344V on 3V range
Measured: 3.027385V low limit: 3.027291V high limit: 3.027471V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 3.02578088044556V on 3V range
Measured: 3.027333V low limit: 3.027243V high limit: 3.027423V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 3.00160906385901V on 3V range
Measured: 3.003162V low limit: 3.003069V high limit: 3.003249V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 3.00156176089113V on 3V range
Measured: 3.003116V low limit: 3.003022V high limit: 3.003202V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 2.95317082475013V on 3V range
Measured: 2.954724V low limit: 2.954626V high limit: 2.954806V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 2.95312352178225V on 3V range
Measured: 2.954667V low limit: 2.954579V high limit: 2.954759V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 2.85629434653239V on 3V range

Measured: 2.857835V low limit: 2.857740V high limit: 2.857920V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 2.85624704356451V on 3V range
Measured: 2.857790V low limit: 2.857693V high limit: 2.857873V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 2.66254139009689V on 3V range
Measured: 2.664064V low limit: 2.663968V high limit: 2.664148V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 2.66249408712902V on 3V range
Measured: 2.664020V low limit: 2.663921V high limit: 2.664101V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 2.27503547722591V on 3V range
Measured: 2.276524V low limit: 2.276425V high limit: 2.276605V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 2.27498817425803V on 3V range
Measured: 2.276485V low limit: 2.276377V high limit: 2.276557V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 1.50002365148394V on 3V range
Measured: 1.501426V low limit: 1.501337V high limit: 1.501517V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 1.49997634851606V on 3V range
Measured: 1.501388V low limit: 1.501290V high limit: 1.501470V

%PASS - Slot 17 channel 6 raw DAC codes linearity at -.05V on 3V range
Measured: -0.04875968V low limit: -4.883676E-02V high limit: -4.865676E-02V

%PASS - Slot 17 channel 6 raw DAC codes maximum linearity error on 3V range
Measured: 1.704309E-05V high limit: 0.00009V

%PASS - Slot 17 channel 6 raw DAC code binary transition 0 to 1 on 3V range
Measured: 5.022000E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition 1 to 2 on 3V range
Measured: 4.641100E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition 2 to 3 on 3V range
Measured: 4.461599E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition 3 to 4 on 3V range
Measured: 4.317199E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition 7 to 8 on 3V range
Measured: 4.925700E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition 15 to 16 on 3V range
Measured: 4.807500E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition 31 to 32 on 3V range
Measured: 4.890700E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition 63 to 64 on 3V range
Measured: 4.610399E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition 127 to 128 on 3V range
Measured: 4.820699E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition 255 to 256 on 3V range
Measured: 4.886400E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition 511 to 512 on 3V range
Measured: 5.210299E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition 1023 to 1024 on 3V range
Measured: 4.649900E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition 2047 to 2048 on 3V range
Measured: 5.709499E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition 4095 to 4096 on 3V range
Measured: 4.575499E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition 8191 to 8192 on 3V range
Measured: 4.448500E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition 16383 to 16384 on 3V range
Measured: 3.914299E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition 32767 to 32768 on 3V range
Measured: 3.813599E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transitions maximum difference on 3V range
Measured: 5.709499E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transitions minimum difference on 3V range
Measured: 3.813599E-05V low limit: -4.269775E-05V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 6.1V on 6V range
Measured: 6.102511V low limit: 6.102333V high limit: 6.102693V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 6.09990539406424V on 6V range
Measured: 6.102420V low limit: 6.102238V high limit: 6.102598V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 6.09981078812848V on 6V range
Measured: 6.102326V low limit: 6.102144V high limit: 6.102504V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 6.09971618219272V on 6V range
Measured: 6.102230V low limit: 6.102049V high limit: 6.102409V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 6.09962157625696V on 6V range
Measured: 6.102135V low limit: 6.101955V high limit: 6.102315V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 6.09933775844968V on 6V range
Measured: 6.101851V low limit: 6.101671V high limit: 6.102031V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 6.09924315251392V on 6V range
Measured: 6.101754V low limit: 6.101576V high limit: 6.101936V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 6.09858091096361V on 6V range
Measured: 6.101091V low limit: 6.100914V high limit: 6.101274V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 6.09848630502785V on 6V range
Measured: 6.100996V low limit: 6.100819V high limit: 6.101179V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 6.09706721599146V on 6V range

Measured: 6.099575V low limit: 6.099400V high limit: 6.099760V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 6.0969726100557V on 6V range

Measured: 6.099486V low limit: 6.099305V high limit: 6.099665V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 6.09403982604715V on 6V range

Measured: 6.096546V low limit: 6.096372V high limit: 6.096732V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 6.09394522011139V on 6V range

Measured: 6.096455V low limit: 6.096278V high limit: 6.096638V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 6.08798504615854V on 6V range

Measured: 6.090498V low limit: 6.090317V high limit: 6.090677V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 6.08789044022278V on 6V range

Measured: 6.090400V low limit: 6.090222V high limit: 6.090582V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 6.07587548638132V on 6V range

Measured: 6.078385V low limit: 6.078206V high limit: 6.078566V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 6.07578088044556V on 6V range

Measured: 6.078291V low limit: 6.078112V high limit: 6.078472V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 6.05165636682689V on 6V range

Measured: 6.054166V low limit: 6.053985V high limit: 6.054345V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 6.05156176089113V on 6V range

Measured: 6.054061V low limit: 6.053890V high limit: 6.054250V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 6.00321812771801V on 6V range

Measured: 6.005722V low limit: 6.005542V high limit: 6.005902V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 6.00312352178225V on 6V range

Measured: 6.005632V low limit: 6.005447V high limit: 6.005807V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 5.90634164950027V on 6V range

Measured: 5.908840V low limit: 5.908656V high limit: 5.909016V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 5.90624704356451V on 6V range
Measured: 5.908733V low limit: 5.908561V high limit: 5.908921V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 5.71258869306477V on 6V range
Measured: 5.715064V low limit: 5.714883V high limit: 5.715243V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 5.71249408712902V on 6V range
Measured: 5.714970V low limit: 5.714789V high limit: 5.715149V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 5.32508278019379V on 6V range
Measured: 5.327521V low limit: 5.327339V high limit: 5.327699V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 5.32498817425803V on 6V range
Measured: 5.327439V low limit: 5.327245V high limit: 5.327605V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 4.55007095445182V on 6V range
Measured: 4.552428V low limit: 4.552251V high limit: 4.552611V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 4.54997634851606V on 6V range
Measured: 4.552357V low limit: 4.552156V high limit: 4.552516V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 3.00004730296788V on 6V range
Measured: 3.002244V low limit: 3.002074V high limit: 3.002434V

%PASS - Slot 17 channel 6 raw DAC codes linearity at 2.99995269703212V on 6V range
Measured: 3.002165V low limit: 3.001979V high limit: 3.002339V

%PASS - Slot 17 channel 6 raw DAC codes linearity at -.1V on 6V range
Measured: -0.09810459V low limit: -0.09827981V high limit: -0.09791981V

%PASS - Slot 17 channel 6 raw DAC codes maximum linearity error on 6V range
Measured: 2.109729E-05V high limit: 0.00018V

%PASS - Slot 17 channel 6 raw DAC code binary transition 0 to 1 on 6V range
Measured: 9.076399E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition 1 to 2 on 6V range
Measured: 9.431100E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition 2 to 3 on 6V range
Measured: 9.575599E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition 3 to 4 on 6V range
Measured: 9.501200E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition 7 to 8 on 6V range
Measured: 9.728800E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition 15 to 16 on 6V range
Measured: 9.571299E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition 31 to 32 on 6V range
Measured: 8.835700E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition 63 to 64 on 6V range
Measured: 9.107100E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition 127 to 128 on 6V range
Measured: 9.851499E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition 255 to 256 on 6V range
Measured: 9.404900E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition 511 to 512 on 6V range
Measured: 1.045130E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition 1023 to 1024 on 6V range
Measured: 8.932000E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition 2047 to 2048 on 6V range
Measured: 1.079279E-04V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition 4095 to 4096 on 6V range
Measured: 9.404900E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition 8191 to 8192 on 6V range

Measured: 8.130799E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition 16383 to 16384 on 6V range

Measured: 7.114900E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transition 32767 to 32768 on 6V range

Measured: 7.881199E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transitions maximum difference on 6V range

Measured: 1.079279E-04V high limit: 2.746044E-04V

%PASS - Slot 17 channel 6 raw DAC code binary transitions minimum difference on 6V range

Measured: 7.114900E-05V low limit: -8.539550E-05V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 3.05V on 3V range

Measured: 3.050980V low limit: 3.050878V high limit: 3.051058V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 3.04995269703212V on 3V range

Measured: 3.050930V low limit: 3.050831V high limit: 3.051011V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 3.04990539406424V on 3V range

Measured: 3.050886V low limit: 3.050783V high limit: 3.050963V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 3.04985809109636V on 3V range

Measured: 3.050834V low limit: 3.050736V high limit: 3.050916V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 3.04981078812848V on 3V range

Measured: 3.050788V low limit: 3.050689V high limit: 3.050869V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 3.04966887922484V on 3V range

Measured: 3.050642V low limit: 3.050547V high limit: 3.050727V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 3.04962157625696V on 3V range

Measured: 3.050597V low limit: 3.050500V high limit: 3.050680V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 3.0492904554818V on 3V range

Measured: 3.050262V low limit: 3.050169V high limit: 3.050349V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 3.04924315251392V on 3V range
Measured: 3.050213V low limit: 3.050121V high limit: 3.050301V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 3.04853360799573V on 3V range
Measured: 3.049499V low limit: 3.049412V high limit: 3.049592V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 3.04848630502785V on 3V range
Measured: 3.049454V low limit: 3.049365V high limit: 3.049545V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 3.04701991302358V on 3V range
Measured: 3.047984V low limit: 3.047899V high limit: 3.048079V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 3.0469726100557V on 3V range
Measured: 3.047939V low limit: 3.047851V high limit: 3.048031V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 3.04399252307927V on 3V range
Measured: 3.044956V low limit: 3.044872V high limit: 3.045052V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 3.04394522011139V on 3V range
Measured: 3.044909V low limit: 3.044825V high limit: 3.045005V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 3.03793774319066V on 3V range
Measured: 3.038900V low limit: 3.038818V high limit: 3.038998V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 3.03789044022278V on 3V range
Measured: 3.038855V low limit: 3.038771V high limit: 3.038951V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 3.02582818341344V on 3V range
Measured: 3.026792V low limit: 3.026712V high limit: 3.026892V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 3.02578088044556V on 3V range
Measured: 3.026749V low limit: 3.026664V high limit: 3.026844V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 3.00160906385901V on 3V range
Measured: 3.002582V low limit: 3.002498V high limit: 3.002678V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 3.00156176089113V on 3V range
Measured: 3.002535V low limit: 3.002451V high limit: 3.002631V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 2.95317082475013V on 3V range
Measured: 2.954155V low limit: 2.954071V high limit: 2.954251V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 2.95312352178225V on 3V range
Measured: 2.954105V low limit: 2.954024V high limit: 2.954204V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 2.85629434653239V on 3V range
Measured: 2.857300V low limit: 2.857217V high limit: 2.857397V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 2.85624704356451V on 3V range
Measured: 2.857257V low limit: 2.857170V high limit: 2.857350V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 2.66254139009689V on 3V range
Measured: 2.663591V low limit: 2.663510V high limit: 2.663690V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 2.66249408712902V on 3V range
Measured: 2.663557V low limit: 2.663462V high limit: 2.663642V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 2.27503547722591V on 3V range
Measured: 2.276186V low limit: 2.276094V high limit: 2.276274V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 2.27498817425803V on 3V range
Measured: 2.276147V low limit: 2.276047V high limit: 2.276227V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 1.50002365148394V on 3V range
Measured: 1.501360V low limit: 1.501263V high limit: 1.501443V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 1.49997634851606V on 3V range
Measured: 1.501318V low limit: 1.501215V high limit: 1.501395V

%PASS - Slot 17 channel 7 raw DAC codes linearity at -.05V on 3V range
Measured: -0.04832062V low limit: -4.839950E-02V high limit: -4.821950E-02V

%PASS - Slot 17 channel 7 raw DAC codes maximum linearity error on 3V range
Measured: 1.308624E-05V high limit: 0.00009V

%PASS - Slot 17 channel 7 raw DAC code binary transition 0 to 1 on 3V range

Measured: 4.982600E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transition 1 to 2 on 3V range

Measured: 4.378399E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transition 2 to 3 on 3V range

Measured: 5.214700E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transition 3 to 4 on 3V range

Measured: 4.645499E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transition 7 to 8 on 3V range

Measured: 4.553599E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transition 15 to 16 on 3V range

Measured: 4.934399E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transition 31 to 32 on 3V range

Measured: 4.557900E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transition 63 to 64 on 3V range

Measured: 4.549100E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transition 127 to 128 on 3V range

Measured: 4.772499E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transition 255 to 256 on 3V range

Measured: 4.544800E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transition 511 to 512 on 3V range

Measured: 4.207600E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transition 1023 to 1024 on 3V range

Measured: 4.689299E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transition 2047 to 2048 on 3V range

Measured: 4.986999E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transition 4095 to 4096 on 3V range
Measured: 4.247099E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transition 8191 to 8192 on 3V range
Measured: 3.467700E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transition 16383 to 16384 on 3V range
Measured: 3.887999E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transition 32767 to 32768 on 3V range
Measured: 4.282100E-05V low limit: -4.269775E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transitions maximum difference on 3V range
Measured: 5.214700E-05V high limit: 1.373022E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transitions minimum difference on 3V range
Measured: 3.467700E-05V low limit: -4.269775E-05V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 6.1V on 6V range
Measured: 6.100895V low limit: 6.100712V high limit: 6.101072V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 6.09990539406424V on 6V range
Measured: 6.100802V low limit: 6.100617V high limit: 6.100977V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 6.09981078812848V on 6V range
Measured: 6.100709V low limit: 6.100523V high limit: 6.100883V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 6.09971618219272V on 6V range
Measured: 6.100609V low limit: 6.100428V high limit: 6.100788V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 6.09962157625696V on 6V range
Measured: 6.100516V low limit: 6.100334V high limit: 6.100694V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 6.09933775844968V on 6V range
Measured: 6.100228V low limit: 6.100050V high limit: 6.100410V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 6.09924315251392V on 6V range
Measured: 6.100138V low limit: 6.099955V high limit: 6.100315V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 6.09858091096361V on 6V range
Measured: 6.099473V low limit: 6.099293V high limit: 6.099653V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 6.09848630502785V on 6V range
Measured: 6.099377V low limit: 6.099199V high limit: 6.099559V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 6.09706721599146V on 6V range
Measured: 6.097954V low limit: 6.097780V high limit: 6.098140V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 6.0969726100557V on 6V range
Measured: 6.097868V low limit: 6.097685V high limit: 6.098045V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 6.09403982604715V on 6V range
Measured: 6.094926V low limit: 6.094754V high limit: 6.095114V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 6.09394522011139V on 6V range
Measured: 6.094836V low limit: 6.094659V high limit: 6.095019V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 6.08798504615854V on 6V range
Measured: 6.088871V low limit: 6.088701V high limit: 6.089061V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 6.08789044022278V on 6V range
Measured: 6.088780V low limit: 6.088606V high limit: 6.088966V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 6.07587548638132V on 6V range
Measured: 6.076767V low limit: 6.076595V high limit: 6.076955V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 6.07578088044556V on 6V range
Measured: 6.076675V low limit: 6.076500V high limit: 6.076860V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 6.05165636682689V on 6V range
Measured: 6.052552V low limit: 6.052383V high limit: 6.052743V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 6.05156176089113V on 6V range
Measured: 6.052470V low limit: 6.052288V high limit: 6.052648V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 6.00321812771801V on 6V range

Measured: 6.004132V low limit: 6.003958V high limit: 6.004318V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 6.00312352178225V on 6V range
Measured: 6.004041V low limit: 6.003864V high limit: 6.004224V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 5.90634164950027V on 6V range
Measured: 5.907288V low limit: 5.907110V high limit: 5.907470V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 5.90624704356451V on 6V range
Measured: 5.907188V low limit: 5.907016V high limit: 5.907376V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 5.71258869306477V on 6V range
Measured: 5.713592V low limit: 5.713414V high limit: 5.713774V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 5.71249408712902V on 6V range
Measured: 5.713502V low limit: 5.713320V high limit: 5.713680V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 5.32508278019379V on 6V range
Measured: 5.326194V low limit: 5.326022V high limit: 5.326382V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 5.32498817425803V on 6V range
Measured: 5.326126V low limit: 5.325927V high limit: 5.326287V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 4.55007095445182V on 6V range
Measured: 4.551424V low limit: 4.551237V high limit: 4.551597V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 4.54997634851606V on 6V range
Measured: 4.551348V low limit: 4.551142V high limit: 4.551502V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 3.00004730296788V on 6V range
Measured: 3.001863V low limit: 3.001667V high limit: 3.002027V

%PASS - Slot 17 channel 7 raw DAC codes linearity at 2.99995269703212V on 6V range
Measured: 3.001777V low limit: 3.001573V high limit: 3.001933V

%PASS - Slot 17 channel 7 raw DAC codes linearity at -.1V on 6V range
Measured: -0.09731997V low limit: -9.747143E-02V high limit: -9.711143E-02V

%PASS - Slot 17 channel 7 raw DAC codes maximum linearity error on 6V range
Measured: 2.854101E-05V high limit: 0.00018V

%PASS - Slot 17 channel 7 raw DAC code binary transition 0 to 1 on 6V range
Measured: 9.317199E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transition 1 to 2 on 6V range
Measured: 9.374200E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transition 2 to 3 on 6V range
Measured: 9.947800E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transition 3 to 4 on 6V range
Measured: 9.286599E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transition 7 to 8 on 6V range
Measured: 9.037100E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transition 15 to 16 on 6V range
Measured: 9.597499E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transition 31 to 32 on 6V range
Measured: 8.529100E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transition 63 to 64 on 6V range
Measured: 9.045899E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transition 127 to 128 on 6V range
Measured: 9.098299E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transition 255 to 256 on 6V range
Measured: 9.238400E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transition 511 to 512 on 6V range
Measured: 8.279600E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transition 1023 to 1024 on 6V range
Measured: 9.102800E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transition 2047 to 2048 on 6V range
Measured: 9.943400E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transition 4095 to 4096 on 6V range
Measured: 9.002000E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transition 8191 to 8192 on 6V range
Measured: 6.830400E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transition 16383 to 16384 on 6V range
Measured: 7.565900E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transition 32767 to 32768 on 6V range
Measured: 8.572900E-05V low limit: -8.539550E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transitions maximum difference on 6V range
Measured: 9.947800E-05V high limit: 2.746044E-04V

%PASS - Slot 17 channel 7 raw DAC code binary transitions minimum difference on 6V range
Measured: 6.830400E-05V low limit: -8.539550E-05V

%JOB_END - ****PASSED**** CTO_DIB External Verification of slot 17 (C320E91) at
4:42:16 PM

slot18_excal

%JOB_START - Beginning CUB External Calibration test on slot 18 at 4:16:22 PM on
2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

%PASS - CALCUB Vforce external calibration test, -1 volts.

Measured: -0.9996 low limit: -1.200 high limit: -0.7999

%PASS - CALCUB Vforce external calibration test, 0 volts.

Measured: 0.0004275 low limit: -0.2000 high limit: 0.2000

%PASS - CALCUB Vforce external calibration test, 0 volts.

Measured: 0.000005412 low limit: -0.2000 high limit: 0.2000

%PASS - CALCUB Vforce external calibration test, 2 volts.

Measured: 2.000 low limit: 1.799 high limit: 2.200

%PASS - CALCUB Vforce external calibration test, 3 volts.

Measured: 3.000 low limit: 2.799 high limit: 3.200

%PASS - CALCUB Vforce external calibration test, 5 volts.

Measured: 5.000 low limit: 4.799 high limit: 5.200

%PASS - CALCUB Vforce external calibration test, 7 volts.

Measured: 7.000 low limit: 6.799 high limit: 7.200

%PASS - CALCUB Vforce external calibration test, 9 volts.

Measured: 9.000 low limit: 8.799 high limit: 9.200

%PASS - CALCUB Vforce external calibration test, 10 volts.

Measured: 10.000 low limit: 9.799 high limit: 10.200

%PASS - CALCUB Vforce external calibration test, 24 volts.

Measured: 24.000 low limit: 23.799 high limit: 24.200

%PASS - CALCUB Vforce external calibration test, -2 volts.

Measured: -1.999 low limit: -2.200 high limit: -1.799

%PASS - CALCUB Vforce external calibration test, -5 volts.

Measured: -4.999 low limit: -5.200 high limit: -4.799

%PASS - CALCUB Vforce external calibration test, -7 volts.

Measured: -6.999 low limit: -7.200 high limit: -6.799

%PASS - CALCUB Vforce external calibration test, -10 volts.

Measured: -9.999 low limit: -10.200 high limit: -9.799

%PASS - CALCUB Vforce external calibration test, -24 volts.

Measured: -23.999 low limit: -24.200 high limit: -23.799

%PASS - CALCUB Vforce external calibration test, -9 volts.

Measured: -8.999 low limit: -9.200 high limit: -8.799

%PASS - CALCUB Vforce external calibration test, 1 volts.

Measured: 1.000 low limit: 0.7999 high limit: 1.200

%PASS - CALCUB Vforce external calibration test, 19 volts.

Measured: 19.000 low limit: 18.799 high limit: 19.200

%PASS - CALCUB Vforce external calibration test, 21 volts.

Measured: 21.000 low limit: 20.799 high limit: 21.200

%PASS - CALCUB Vforce external calibration test, 4 volts.

Measured: 4.000 low limit: 3.799 high limit: 4.200

%PASS - CALCUB Vforce external calibration test, 6 volts.

Measured: 6.000 low limit: 5.799 high limit: 6.200

%PASS - CALCUB Vforce external calibration test, 20 volts.

Measured: 20.000 low limit: 19.799 high limit: 20.200

%PASS - CALCUB Vforce external calibration test, -19 volts.

Measured: -18.999 low limit: -19.200 high limit: -18.799

%PASS - CALCUB Vforce external calibration test, -20 volts.
Measured: -19.999 low limit: -20.200 high limit: -19.799

%PASS - CALCUB Vforce external calibration test, -21 volts.
Measured: -20.999 low limit: -21.200 high limit: -20.799

%PASS - CALCUB Vforce external calibration test, 23 volts.
Measured: 23.000 low limit: 22.799 high limit: 23.200

%PASS - CALCUB Vforce external calibration test, 22 volts.
Measured: 22.000 low limit: 21.799 high limit: 22.200

%PASS - CALCUB Vforce external calibration test, 18 volts.
Measured: 18.000 low limit: 17.799 high limit: 18.200

%PASS - CALCUB Vforce external calibration test, 17 volts.
Measured: 17.000 low limit: 16.799 high limit: 17.200

%PASS - CALCUB Vforce external calibration test, 16 volts.
Measured: 16.000 low limit: 15.799 high limit: 16.200

%PASS - CALCUB Vforce external calibration test, 15 volts.
Measured: 15.000 low limit: 14.799 high limit: 15.200

%PASS - CALCUB Vforce external calibration test, 14 volts.
Measured: 14.000 low limit: 13.799 high limit: 14.200

%PASS - CALCUB Vforce external calibration test, 13 volts.
Measured: 13.000 low limit: 12.799 high limit: 13.200

%PASS - CALCUB Vforce external calibration test, 12 volts.
Measured: 12.000 low limit: 11.799 high limit: 12.200

%PASS - CALCUB Vforce external calibration test, 11 volts.
Measured: 11.000 low limit: 10.799 high limit: 11.200

%PASS - CALCUB Vforce external calibration test, 8 volts.
Measured: 8.000 low limit: 7.799 high limit: 8.200

%PASS - CALCUB Vforce external calibration test, -3 volts.
Measured: -2.999 low limit: -3.200 high limit: -2.799

%PASS - CALCUB Vforce external calibration test, -4 volts.
Measured: -3.999 low limit: -4.200 high limit: -3.799

%PASS - CALCUB Vforce external calibration test, -6 volts.
Measured: -5.999 low limit: -6.200 high limit: -5.799

%PASS - CALCUB Vforce external calibration test, -8 volts.
Measured: -7.999 low limit: -8.200 high limit: -7.799

%PASS - CALCUB Vforce external calibration test, -11 volts.
Measured: -10.999 low limit: -11.200 high limit: -10.799

%PASS - CALCUB Vforce external calibration test, -12 volts.
Measured: -11.999 low limit: -12.200 high limit: -11.799

%PASS - CALCUB Vforce external calibration test, -13 volts.
Measured: -12.999 low limit: -13.200 high limit: -12.799

%PASS - CALCUB Vforce external calibration test, -14 volts.
Measured: -13.999 low limit: -14.200 high limit: -13.799

%PASS - CALCUB Vforce external calibration test, -15 volts.
Measured: -14.999 low limit: -15.200 high limit: -14.799

%PASS - CALCUB Vforce external calibration test, -16 volts.
Measured: -15.999 low limit: -16.200 high limit: -15.799

%PASS - CALCUB Vforce external calibration test, -17 volts.
Measured: -16.999 low limit: -17.200 high limit: -16.799

%PASS - CALCUB Vforce external calibration test, -18 volts.
Measured: -17.999 low limit: -18.200 high limit: -17.799

%PASS - CALCUB Vforce external calibration test, -22 volts.

Measured: -21.999 low limit: -22.200 high limit: -21.799

%PASS - CALCUB Vforce external calibration test, -23 volts.

Measured: -22.999 low limit: -23.200 high limit: -22.799

%PASS - CALCUB IForce external calibration test, 200 na.

Measured: 0.000001999 low limit: 1.998E-06 high limit: 2.001E-06

%PASS - CALCUB IForce external calibration test, 200 na.

Measured: -0.000002000 low limit: -2.001E-06 high limit: -1.998E-06

%PASS - CALCUB IForce external calibration test, 2 ua.

Measured: 0.00001999 low limit: 1.998E-05 high limit: 2.001E-05

%PASS - CALCUB IForce external calibration test, 2 ua.

Measured: -0.00002000 low limit: -2.001E-05 high limit: -1.998E-05

%PASS - CALCUB IForce external calibration test, 20 ua.

Measured: 0.0001999 low limit: 1.998E-04 high limit: 2.001E-04

%PASS - CALCUB IForce external calibration test, 20 ua.

Measured: -0.0001999 low limit: -2.001E-04 high limit: -1.998E-04

%PASS - CALCUB IForce external calibration test, 200 ua.

Measured: 0.001999 low limit: 1.998E-03 high limit: 2.001E-03

%PASS - CALCUB IForce external calibration test, 200 ua.

Measured: -0.001999 low limit: -2.001E-03 high limit: -1.998E-03

%PASS - CALCUB IForce external calibration test, 2 ma.

Measured: 0.02000 low limit: 1.998E-02 high limit: 2.001E-02

%PASS - CALCUB IForce external calibration test, 2 ma.

Measured: -0.02000 low limit: -2.001E-02 high limit: -1.998E-02

%PASS - CALCUB IForce external calibration test, 20 ma.

Measured: 0.09999 low limit: 9.990E-02 high limit: 0.1000

%PASS - CALCUB IForce external calibration test, 20 ma.
Measured: -0.1000 low limit: -0.1000 high limit: -9.990E-02

%PASS - CALCUB IForce external calibration test, 200 ma.
Measured: 0.1999 low limit: 0.1994 high limit: 0.2005

%PASS - CALCUB IForce external calibration test, 200 ma.
Measured: -0.2000 low limit: -0.2005 high limit: -0.1994

%PASS - Flash readback error of force DAC record 0
Measured: 0.0004577 expected: 0.0004577

%PASS - Flash readback error of external measurement record 0
Measured: 0.0004275 expected: 0.0004275

%PASS - Flash readback error of internal measurement record 0
Measured: -5.698E-03 expected: -5.698E-03

%PASS - Flash readback error of voltage flag record 0
Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 1
Measured: 2.000 expected: 2.000

%PASS - Flash readback error of external measurement record 1
Measured: 2.000 expected: 2.000

%PASS - Flash readback error of internal measurement record 1
Measured: 1.989 expected: 1.989

%PASS - Flash readback error of voltage flag record 1
Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 2
Measured: 5.001 expected: 5.001

%PASS - Flash readback error of external measurement record 2

Measured: 5.000 expected: 5.000

%PASS - Flash readback error of internal measurement record 2

Measured: 4.982 expected: 4.982

%PASS - Flash readback error of voltage flag record 2

Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 3

Measured: 7.001 expected: 7.001

%PASS - Flash readback error of external measurement record 3

Measured: 7.000 expected: 7.000

%PASS - Flash readback error of internal measurement record 3

Measured: 6.978 expected: 6.978

%PASS - Flash readback error of voltage flag record 3

Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 4

Measured: 10.001 expected: 10.001

%PASS - Flash readback error of external measurement record 4

Measured: 10.000 expected: 10.000

%PASS - Flash readback error of internal measurement record 4

Measured: 9.949 expected: 9.949

%PASS - Flash readback error of voltage flag record 4

Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 5

Measured: 24.003 expected: 24.003

%PASS - Flash readback error of external measurement record 5

Measured: 24.000 expected: 24.000

%PASS - Flash readback error of internal measurement record 5
Measured: 23.918 expected: 23.918

%PASS - Flash readback error of voltage flag record 5
Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 6
Measured: -2.000 expected: -2.000

%PASS - Flash readback error of external measurement record 6
Measured: -1.999 expected: -1.999

%PASS - Flash readback error of internal measurement record 6
Measured: -2.001 expected: -2.001

%PASS - Flash readback error of voltage flag record 6
Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 7
Measured: -5.000 expected: -5.000

%PASS - Flash readback error of external measurement record 7
Measured: -4.999 expected: -4.999

%PASS - Flash readback error of internal measurement record 7
Measured: -4.994 expected: -4.994

%PASS - Flash readback error of voltage flag record 7
Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 8
Measured: -7.001 expected: -7.001

%PASS - Flash readback error of external measurement record 8
Measured: -6.999 expected: -6.999

%PASS - Flash readback error of internal measurement record 8
Measured: -6.989 expected: -6.989

%PASS - Flash readback error of voltage flag record 8

Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 9

Measured: -10.001 expected: -10.001

%PASS - Flash readback error of external measurement record 9

Measured: -9.999 expected: -9.999

%PASS - Flash readback error of internal measurement record 9

Measured: -10.001 expected: -10.001

%PASS - Flash readback error of voltage flag record 9

Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 10

Measured: -24.003 expected: -24.003

%PASS - Flash readback error of external measurement record 10

Measured: -23.999 expected: -23.999

%PASS - Flash readback error of internal measurement record 10

Measured: -23.968 expected: -23.968

%PASS - Flash readback error of voltage flag record 10

Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 11

Measured: 9999 expected: 9999

%PASS - Flash readback error of external measurement record 11

Measured: 0 expected: 0

%PASS - Flash readback error of internal measurement record 11

Measured: -3.092E-03 expected: -3.092E-03

%PASS - Flash readback error of voltage flag record 11

Measured: 0 expected: 0

%PASS - Flash readback error of force DAC record 12

Measured: 0.8186 expected: 0.8186

%PASS - Flash readback error of external measurement record 12

Measured: 0.2 expected: 0.2

%PASS - Flash readback error of internal measurement record 12

Measured: 0.1962 expected: 0.1962

%PASS - Flash readback error of voltage flag record 12

Measured: 0 expected: 0

%PASS - Flash readback error of force DAC record 13

Measured: 9999 expected: 9999

%PASS - Flash readback error of external measurement record 13

Measured: 0 expected: 0

%PASS - Flash readback error of internal measurement record 13

Measured: -3.113E-04 expected: -3.113E-04

%PASS - Flash readback error of voltage flag record 13

Measured: 0 expected: 0

%PASS - Flash readback error of force DAC record 14

Measured: 2.192 expected: 2.192

%PASS - Flash readback error of external measurement record 14

Measured: 0.02 expected: 0.02

%PASS - Flash readback error of internal measurement record 14

Measured: 1.970E-02 expected: 1.970E-02

%PASS - Flash readback error of voltage flag record 14

Measured: 0 expected: 0

%PASS - Flash readback error of force DAC record 15

Measured: 9999 expected: 9999

%PASS - Flash readback error of external measurement record 15

Measured: 0 expected: 0

%PASS - Flash readback error of internal measurement record 15

Measured: -3.143E-05 expected: -3.143E-05

%PASS - Flash readback error of voltage flag record 15

Measured: 0 expected: 0

%PASS - Flash readback error of force DAC record 16

Measured: 4.002 expected: 4.002

%PASS - Flash readback error of external measurement record 16

Measured: 0.002 expected: 0.002

%PASS - Flash readback error of internal measurement record 16

Measured: 1.964E-03 expected: 1.964E-03

%PASS - Flash readback error of voltage flag record 16

Measured: 0 expected: 0

%PASS - Flash readback error of force DAC record 17

Measured: 9999 expected: 9999

%PASS - Flash readback error of external measurement record 17

Measured: 0 expected: 0

%PASS - Flash readback error of internal measurement record 17

Measured: -3.114E-06 expected: -3.114E-06

%PASS - Flash readback error of voltage flag record 17

Measured: 0 expected: 0

%PASS - Flash readback error of force DAC record 18

Measured: 3.960 expected: 3.960

%PASS - Flash readback error of external measurement record 18
Measured: 0.0002 expected: 0.0002

%PASS - Flash readback error of internal measurement record 18
Measured: 1.964E-04 expected: 1.964E-04

%PASS - Flash readback error of voltage flag record 18
Measured: 0 expected: 0

%PASS - Flash readback error of force DAC record 19
Measured: 9999 expected: 9999

%PASS - Flash readback error of external measurement record 19
Measured: 0 expected: 0

%PASS - Flash readback error of internal measurement record 19
Measured: -3.162E-07 expected: -3.162E-07

%PASS - Flash readback error of voltage flag record 19
Measured: 0 expected: 0

%PASS - Flash readback error of force DAC record 20
Measured: 3.957 expected: 3.957

%PASS - Flash readback error of external measurement record 20
Measured: 0.00002 expected: 0.00002

%PASS - Flash readback error of internal measurement record 20
Measured: 1.964E-05 expected: 1.964E-05

%PASS - Flash readback error of voltage flag record 20
Measured: 0 expected: 0

%PASS - Flash readback error of force DAC record 21
Measured: 9999 expected: 9999

%PASS - Flash readback error of external measurement record 21

Measured: 0 expected: 0

%PASS - Flash readback error of internal measurement record 21

Measured: -3.128E-08 expected: -3.128E-08

%PASS - Flash readback error of voltage flag record 21

Measured: 0 expected: 0

%PASS - Flash readback error of force DAC record 22

Measured: 3.954 expected: 3.954

%PASS - Flash readback error of external measurement record 22

Measured: 0.000002 expected: 0.000002

%PASS - Flash readback error of internal measurement record 22

Measured: 1.964E-06 expected: 1.964E-06

%PASS - Flash readback error of voltage flag record 22

Measured: 0 expected: 0

%PASS - Flash readback error of force DAC record 23

Measured: 9999 expected: 9999

%PASS - Flash readback error of external measurement record 23

Measured: 0 expected: 0

%PASS - Flash readback error of internal measurement record 23

Measured: -3.052E-09 expected: -3.052E-09

%PASS - Flash readback error of voltage flag record 23

Measured: 0 expected: 0

%PASS - Flash readback error of force DAC record 24

Measured: 3.945 expected: 3.945

%PASS - Flash readback error of external measurement record 24

Measured: 0.0000002 expected: 0.0000002

%PASS - Flash readback error of internal measurement record 24
Measured: 1.964E-07 expected: 1.964E-07

%PASS - Flash readback error of voltage flag record 24
Measured: 0 expected: 0

%PASS - Flash readback error of force DAC record 25
Measured: 3.000 expected: 3.000

%PASS - Flash readback error of external measurement record 25
Measured: 3.000 expected: 3.000

%PASS - Flash readback error of internal measurement record 25
Measured: 2.987 expected: 2.987

%PASS - Flash readback error of voltage flag record 25
Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 26
Measured: -0.9999 expected: -0.9999

%PASS - Flash readback error of external measurement record 26
Measured: -0.9996 expected: -0.9996

%PASS - Flash readback error of internal measurement record 26
Measured: -1.003 expected: -1.003

%PASS - Flash readback error of voltage flag record 26
Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 27
Measured: 9.002 expected: 9.002

%PASS - Flash readback error of external measurement record 27
Measured: 9.000 expected: 9.000

%PASS - Flash readback error of internal measurement record 27
Measured: 8.973 expected: 8.973

%PASS - Flash readback error of voltage flag record 27

Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 28

Measured: 9999 expected: 9999

%PASS - Flash readback error of external measurement record 28

Measured: 0.000005412 expected: 0.000005412

%PASS - Flash readback error of internal measurement record 28

Measured: -2.520E-02 expected: -2.520E-02

%PASS - Flash readback error of voltage flag record 28

Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 29

Measured: -9.001 expected: -9.001

%PASS - Flash readback error of external measurement record 29

Measured: -8.999 expected: -8.999

%PASS - Flash readback error of internal measurement record 29

Measured: -8.985 expected: -8.985

%PASS - Flash readback error of voltage flag record 29

Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 30

Measured: 1.000 expected: 1.000

%PASS - Flash readback error of external measurement record 30

Measured: 1.000 expected: 1.000

%PASS - Flash readback error of internal measurement record 30

Measured: 0.9915 expected: 0.9915

%PASS - Flash readback error of voltage flag record 30

Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 31

Measured: 19.002 expected: 19.002

%PASS - Flash readback error of external measurement record 31

Measured: 19.000 expected: 19.000

%PASS - Flash readback error of internal measurement record 31

Measured: 18.931 expected: 18.931

%PASS - Flash readback error of voltage flag record 31

Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 32

Measured: 21.002 expected: 21.002

%PASS - Flash readback error of external measurement record 32

Measured: 21.000 expected: 21.000

%PASS - Flash readback error of internal measurement record 32

Measured: 20.924 expected: 20.924

%PASS - Flash readback error of voltage flag record 32

Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 33

Measured: 4.001 expected: 4.001

%PASS - Flash readback error of external measurement record 33

Measured: 4.000 expected: 4.000

%PASS - Flash readback error of internal measurement record 33

Measured: 3.985 expected: 3.985

%PASS - Flash readback error of voltage flag record 33

Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 34

Measured: 6.001 expected: 6.001

%PASS - Flash readback error of external measurement record 34

Measured: 6.000 expected: 6.000

%PASS - Flash readback error of internal measurement record 34

Measured: 5.980 expected: 5.980

%PASS - Flash readback error of voltage flag record 34

Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 35

Measured: 20.002 expected: 20.002

%PASS - Flash readback error of external measurement record 35

Measured: 20.000 expected: 20.000

%PASS - Flash readback error of internal measurement record 35

Measured: 19.926 expected: 19.926

%PASS - Flash readback error of voltage flag record 35

Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 36

Measured: -19.002 expected: -19.002

%PASS - Flash readback error of external measurement record 36

Measured: -18.999 expected: -18.999

%PASS - Flash readback error of internal measurement record 36

Measured: -18.980 expected: -18.980

%PASS - Flash readback error of voltage flag record 36

Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 37

Measured: -20.002 expected: -20.002

%PASS - Flash readback error of external measurement record 37
Measured: -19.999 expected: -19.999

%PASS - Flash readback error of internal measurement record 37
Measured: -19.978 expected: -19.978

%PASS - Flash readback error of voltage flag record 37
Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 38
Measured: -21.002 expected: -21.002

%PASS - Flash readback error of external measurement record 38
Measured: -20.999 expected: -20.999

%PASS - Flash readback error of internal measurement record 38
Measured: -20.974 expected: -20.974

%PASS - Flash readback error of voltage flag record 38
Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 39
Measured: 23.002 expected: 23.002

%PASS - Flash readback error of external measurement record 39
Measured: 23.000 expected: 23.000

%PASS - Flash readback error of internal measurement record 39
Measured: 22.920 expected: 22.920

%PASS - Flash readback error of voltage flag record 39
Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 40
Measured: 22.002 expected: 22.002

%PASS - Flash readback error of external measurement record 40

Measured: 22.000 expected: 22.000

%PASS - Flash readback error of internal measurement record 40

Measured: 21.922 expected: 21.922

%PASS - Flash readback error of voltage flag record 40

Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 41

Measured: 18.002 expected: 18.002

%PASS - Flash readback error of external measurement record 41

Measured: 18.000 expected: 18.000

%PASS - Flash readback error of internal measurement record 41

Measured: 17.933 expected: 17.933

%PASS - Flash readback error of voltage flag record 41

Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 42

Measured: 17.002 expected: 17.002

%PASS - Flash readback error of external measurement record 42

Measured: 17.000 expected: 17.000

%PASS - Flash readback error of internal measurement record 42

Measured: 16.935 expected: 16.935

%PASS - Flash readback error of voltage flag record 42

Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 43

Measured: 16.001 expected: 16.001

%PASS - Flash readback error of external measurement record 43

Measured: 16.000 expected: 16.000

%PASS - Flash readback error of internal measurement record 43
Measured: 15.937 expected: 15.937

%PASS - Flash readback error of voltage flag record 43
Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 44
Measured: 15.002 expected: 15.002

%PASS - Flash readback error of external measurement record 44
Measured: 15.000 expected: 15.000

%PASS - Flash readback error of internal measurement record 44
Measured: 14.939 expected: 14.939

%PASS - Flash readback error of voltage flag record 44
Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 45
Measured: 14.001 expected: 14.001

%PASS - Flash readback error of external measurement record 45
Measured: 14.000 expected: 14.000

%PASS - Flash readback error of internal measurement record 45
Measured: 13.942 expected: 13.942

%PASS - Flash readback error of voltage flag record 45
Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 46
Measured: 13.001 expected: 13.001

%PASS - Flash readback error of external measurement record 46
Measured: 13.000 expected: 13.000

%PASS - Flash readback error of internal measurement record 46
Measured: 12.944 expected: 12.944

%PASS - Flash readback error of voltage flag record 46

Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 47

Measured: 12.001 expected: 12.001

%PASS - Flash readback error of external measurement record 47

Measured: 12.000 expected: 12.000

%PASS - Flash readback error of internal measurement record 47

Measured: 11.945 expected: 11.945

%PASS - Flash readback error of voltage flag record 47

Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 48

Measured: 11.001 expected: 11.001

%PASS - Flash readback error of external measurement record 48

Measured: 11.000 expected: 11.000

%PASS - Flash readback error of internal measurement record 48

Measured: 10.948 expected: 10.948

%PASS - Flash readback error of voltage flag record 48

Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 49

Measured: 8.002 expected: 8.002

%PASS - Flash readback error of external measurement record 49

Measured: 8.000 expected: 8.000

%PASS - Flash readback error of internal measurement record 49

Measured: 7.975 expected: 7.975

%PASS - Flash readback error of voltage flag record 49

Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 50

Measured: -3.000 expected: -3.000

%PASS - Flash readback error of external measurement record 50

Measured: -2.999 expected: -2.999

%PASS - Flash readback error of internal measurement record 50

Measured: -2.998 expected: -2.998

%PASS - Flash readback error of voltage flag record 50

Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 51

Measured: -4.000 expected: -4.000

%PASS - Flash readback error of external measurement record 51

Measured: -3.999 expected: -3.999

%PASS - Flash readback error of internal measurement record 51

Measured: -3.996 expected: -3.996

%PASS - Flash readback error of voltage flag record 51

Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 52

Measured: -6.001 expected: -6.001

%PASS - Flash readback error of external measurement record 52

Measured: -5.999 expected: -5.999

%PASS - Flash readback error of internal measurement record 52

Measured: -5.992 expected: -5.992

%PASS - Flash readback error of voltage flag record 52

Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 53

Measured: -8.001 expected: -8.001

%PASS - Flash readback error of external measurement record 53

Measured: -7.999 expected: -7.999

%PASS - Flash readback error of internal measurement record 53

Measured: -7.987 expected: -7.987

%PASS - Flash readback error of voltage flag record 53

Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 54

Measured: -11.001 expected: -11.001

%PASS - Flash readback error of external measurement record 54

Measured: -10.999 expected: -10.999

%PASS - Flash readback error of internal measurement record 54

Measured: -10.997 expected: -10.997

%PASS - Flash readback error of voltage flag record 54

Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 55

Measured: -12.001 expected: -12.001

%PASS - Flash readback error of external measurement record 55

Measured: -11.999 expected: -11.999

%PASS - Flash readback error of internal measurement record 55

Measured: -11.995 expected: -11.995

%PASS - Flash readback error of voltage flag record 55

Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 56

Measured: -13.001 expected: -13.001

%PASS - Flash readback error of external measurement record 56
Measured: -12.999 expected: -12.999

%PASS - Flash readback error of internal measurement record 56
Measured: -12.993 expected: -12.993

%PASS - Flash readback error of voltage flag record 56
Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 57
Measured: -14.002 expected: -14.002

%PASS - Flash readback error of external measurement record 57
Measured: -13.999 expected: -13.999

%PASS - Flash readback error of internal measurement record 57
Measured: -13.991 expected: -13.991

%PASS - Flash readback error of voltage flag record 57
Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 58
Measured: -15.002 expected: -15.002

%PASS - Flash readback error of external measurement record 58
Measured: -14.999 expected: -14.999

%PASS - Flash readback error of internal measurement record 58
Measured: -14.988 expected: -14.988

%PASS - Flash readback error of voltage flag record 58
Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 59
Measured: -16.002 expected: -16.002

%PASS - Flash readback error of external measurement record 59

Measured: -15.999 expected: -15.999

%PASS - Flash readback error of internal measurement record 59

Measured: -15.986 expected: -15.986

%PASS - Flash readback error of voltage flag record 59

Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 60

Measured: -17.002 expected: -17.002

%PASS - Flash readback error of external measurement record 60

Measured: -16.999 expected: -16.999

%PASS - Flash readback error of internal measurement record 60

Measured: -16.984 expected: -16.984

%PASS - Flash readback error of voltage flag record 60

Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 61

Measured: -18.002 expected: -18.002

%PASS - Flash readback error of external measurement record 61

Measured: -17.999 expected: -17.999

%PASS - Flash readback error of internal measurement record 61

Measured: -17.983 expected: -17.983

%PASS - Flash readback error of voltage flag record 61

Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 62

Measured: -22.002 expected: -22.002

%PASS - Flash readback error of external measurement record 62

Measured: -21.999 expected: -21.999

%PASS - Flash readback error of internal measurement record 62
Measured: -21.971 expected: -21.971

%PASS - Flash readback error of voltage flag record 62
Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 63
Measured: -23.003 expected: -23.003

%PASS - Flash readback error of external measurement record 63
Measured: -22.999 expected: -22.999

%PASS - Flash readback error of internal measurement record 63
Measured: -22.970 expected: -22.970

%PASS - Flash readback error of voltage flag record 63
Measured: 1 expected: 1

%PASS - Flash readback error of force DAC record 64
Measured: 9999 expected: 9999

%PASS - Flash readback error of external measurement record 64
Measured: 9999 expected: 9999

%PASS - Flash readback error of internal measurement record 64
Measured: 9999 expected: 9999

%PASS - Flash readback error of voltage flag record 64
Measured: 9999 expected: 9999

%PASS - Flash readback error of force DAC record 65
Measured: 9999 expected: 9999

%PASS - Flash readback error of external measurement record 65
Measured: 9999 expected: 9999

%PASS - Flash readback error of internal measurement record 65
Measured: 9999 expected: 9999

%PASS - Flash readback error of voltage flag record 65

Measured: 9999 expected: 9999

%PASS - Flash readback error of force DAC record 66

Measured: 9999 expected: 9999

%PASS - Flash readback error of external measurement record 66

Measured: 9999 expected: 9999

%PASS - Flash readback error of internal measurement record 66

Measured: 9999 expected: 9999

%PASS - Flash readback error of voltage flag record 66

Measured: 9999 expected: 9999

%PASS - Flash readback error of force DAC record 67

Measured: 9999 expected: 9999

%PASS - Flash readback error of external measurement record 67

Measured: 9999 expected: 9999

%PASS - Flash readback error of internal measurement record 67

Measured: 9999 expected: 9999

%PASS - Flash readback error of voltage flag record 67

Measured: 9999 expected: 9999

%PASS - Flash readback error of force DAC record 68

Measured: 9999 expected: 9999

%PASS - Flash readback error of external measurement record 68

Measured: 9999 expected: 9999

%PASS - Flash readback error of internal measurement record 68

Measured: 9999 expected: 9999

%PASS - Flash readback error of voltage flag record 68

Measured: 9999 expected: 9999

%PASS - Flash readback error of force DAC record 69

Measured: 9999 expected: 9999

%PASS - Flash readback error of external measurement record 69

Measured: 9999 expected: 9999

%PASS - Flash readback error of internal measurement record 69

Measured: 9999 expected: 9999

%PASS - Flash readback error of voltage flag record 69

Measured: 9999 expected: 9999

%PASS - Flash readback error of force DAC record 70

Measured: 9999 expected: 9999

%PASS - Flash readback error of external measurement record 70

Measured: 9999 expected: 9999

%PASS - Flash readback error of internal measurement record 70

Measured: 9999 expected: 9999

%PASS - Flash readback error of voltage flag record 70

Measured: 9999 expected: 9999

%PASS - Flash readback error of force DAC record 71

Measured: 9999 expected: 9999

%PASS - Flash readback error of external measurement record 71

Measured: 9999 expected: 9999

%PASS - Flash readback error of internal measurement record 71

Measured: 9999 expected: 9999

%PASS - Flash readback error of voltage flag record 71

Measured: 9999 expected: 9999

%PASS - Flash readback error of force DAC record 72

Measured: 9999 expected: 9999

%PASS - Flash readback error of external measurement record 72

Measured: 9999 expected: 9999

%PASS - Flash readback error of internal measurement record 72

Measured: 9999 expected: 9999

%PASS - Flash readback error of voltage flag record 72

Measured: 9999 expected: 9999

%PASS - Flash readback error of force DAC record 73

Measured: 9999 expected: 9999

%PASS - Flash readback error of external measurement record 73

Measured: 9999 expected: 9999

%PASS - Flash readback error of internal measurement record 73

Measured: 9999 expected: 9999

%PASS - Flash readback error of voltage flag record 73

Measured: 9999 expected: 9999

%PASS - Flash readback error of force DAC record 74

Measured: 9999 expected: 9999

%PASS - Flash readback error of external measurement record 74

Measured: 9999 expected: 9999

%PASS - Flash readback error of internal measurement record 74

Measured: 9999 expected: 9999

%PASS - Flash readback error of voltage flag record 74

Measured: 9999 expected: 9999

%JOB_END - ****PASSED**** CUB External Calibration of slot 18 (1FC1CA) at 4:27:39 PM

slot18_expv

%JOB_START - Beginning CUB External Verification test on slot 18 at 4:42:48 PM on
2/29/2020

Workbook Rev V7.30.12_0835_MOUT IG-XL Version: 3.40.13 DIB #
23BA64 Rev 1137A

- Performing source and measure voltage verification...

%PASS - CALCUB test of source voltage at -24V

Measured: -23.999878 low limit: -24.005417 high limit: -23.994582

%PASS - CALCUB test of voltage measure at -24V

Measured: -23.999925 low limit: -24.002930 high limit: -23.996827

%PASS - CALCUB test of source voltage at -23V

Measured: -22.999837 low limit: -23.005217 high limit: -22.994782

%PASS - CALCUB test of voltage measure at -23V

Measured: -22.999792 low limit: -23.002889 high limit: -22.996785

%PASS - CALCUB test of source voltage at -22V

Measured: -21.999642 low limit: -22.005017 high limit: -21.994982

%PASS - CALCUB test of voltage measure at -22V

Measured: -21.999664 low limit: -22.002693 high limit: -21.996590

%PASS - CALCUB test of source voltage at -21V

Measured: -20.999882 low limit: -21.004817 high limit: -20.995182

%PASS - CALCUB test of voltage measure at -21V

Measured: -20.999842 low limit: -21.002934 high limit: -20.996830

%PASS - CALCUB test of source voltage at -20V

Measured: -20.000196 low limit: -20.004617 high limit: -19.995382

%PASS - CALCUB test of voltage measure at -20V

Measured: -20.000070 low limit: -20.003248 high limit: -19.997144

%PASS - CALCUB test of source voltage at -19V
Measured: -18.999948 low limit: -19.004417 high limit: -18.995582

%PASS - CALCUB test of voltage measure at -19V
Measured: -18.999890 low limit: -19.003000 high limit: -18.996896

%PASS - CALCUB test of source voltage at -18V
Measured: -17.999837 low limit: -18.004217 high limit: -17.995782

%PASS - CALCUB test of voltage measure at -18V
Measured: -17.999837 low limit: -18.002889 high limit: -17.996785

%PASS - CALCUB test of source voltage at -17V
Measured: -16.999646 low limit: -17.004017 high limit: -16.995982

%PASS - CALCUB test of voltage measure at -17V
Measured: -16.999679 low limit: -17.002698 high limit: -16.996594

%PASS - CALCUB test of source voltage at -16V
Measured: -16.000235 low limit: -16.003817 high limit: -15.996182

%PASS - CALCUB test of voltage measure at -16V
Measured: -16.000525 low limit: -16.003287 high limit: -15.997183

%PASS - CALCUB test of source voltage at -15V
Measured: -15.000112 low limit: -15.003617 high limit: -14.996382

%PASS - CALCUB test of voltage measure at -15V
Measured: -15.000016 low limit: -15.003163 high limit: -14.997060

%PASS - CALCUB test of source voltage at -14V
Measured: -13.999908 low limit: -14.003417 high limit: -13.996582

%PASS - CALCUB test of voltage measure at -14V
Measured: -13.999927 low limit: -14.002959 high limit: -13.996856

%PASS - CALCUB test of source voltage at -13V
Measured: -12.999735 low limit: -13.003217 high limit: -12.996782

%PASS - CALCUB test of voltage measure at -13V
Measured: -13.000023 low limit: -13.002787 high limit: -12.996683

%PASS - CALCUB test of source voltage at -12V
Measured: -11.999537 low limit: -12.003017 high limit: -11.996982

%PASS - CALCUB test of voltage measure at -12V
Measured: -11.999477 low limit: -12.002588 high limit: -11.996485

%PASS - CALCUB test of source voltage at -11V
Measured: -11.000194 low limit: -11.002817 high limit: -10.997182

%PASS - CALCUB test of voltage measure at -11V
Measured: -11.000259 low limit: -11.003246 high limit: -10.997142

%PASS - CALCUB test of source voltage at -10V
Measured: -9.999962 low limit: -10.002617 high limit: -9.997382

%PASS - CALCUB test of voltage measure at -10V
Measured: -10.000091 low limit: -10.003014 high limit: -9.996911

%PASS - CALCUB test of source voltage at -9V
Measured: -9.000105 low limit: -9.002417 high limit: -8.997582

%PASS - CALCUB test of voltage measure at -9V
Measured: -9.000020 low limit: -9.000868 high limit: -8.999342

%PASS - CALCUB test of source voltage at -8V
Measured: -8.000034 low limit: -8.002217 high limit: -7.997782

%PASS - CALCUB test of voltage measure at -8V
Measured: -7.999912 low limit: -8.000797 high limit: -7.999271

%PASS - CALCUB test of source voltage at -7V
Measured: -7.000274 low limit: -7.002017 high limit: -6.997982

%PASS - CALCUB test of voltage measure at -7V

Measured: -7.000561 low limit: -7.001037 high limit: -6.999511

%PASS - CALCUB test of source voltage at -6V

Measured: -6.000172 low limit: -6.001817 high limit: -5.998182

%PASS - CALCUB test of voltage measure at -6V

Measured: -6.000184 low limit: -6.000935 high limit: -5.999409

%PASS - CALCUB test of source voltage at -5V

Measured: -5.000049 low limit: -5.001617 high limit: -4.998382

%PASS - CALCUB test of voltage measure at -5V

Measured: -4.999953 low limit: -5.000812 high limit: -4.999286

%PASS - CALCUB test of source voltage at -4V

Measured: -3.999932 low limit: -4.001417 high limit: -3.998582

%PASS - CALCUB test of voltage measure at -4V

Measured: -3.999987 low limit: -4.000695 high limit: -3.999169

%PASS - CALCUB test of source voltage at -3V

Measured: -2.999852 low limit: -3.001217 high limit: -2.998782

%PASS - CALCUB test of voltage measure at -3V

Measured: -2.999863 low limit: -3.000615 high limit: -2.999089

%PASS - CALCUB test of source voltage at -2V

Measured: -2.000157 low limit: -2.001017 high limit: -1.998982

%PASS - CALCUB test of voltage measure at -2V

Measured: -2.000199 low limit: -2.000920 high limit: -1.999394

%PASS - CALCUB test of source voltage at -2V at 200mA

Measured: -1.999600 low limit: -2.001235 high limit: -1.998764

%PASS - CALCUB test of source voltage at -1V

Measured: -1.000074 low limit: -1.000817 high limit: -0.9991820

%PASS - CALCUB test of voltage measure at -1V

Measured: -1.000084 low limit: -1.000837 high limit: -0.9993112

%PASS - CALCUB test of source voltage at 0V

Measured: 0.000001276210 low limit: -6.179903E-04 high limit: 6.179903E-04

%PASS - CALCUB test of voltage measure at 0V

Measured: -1.676558E-05 low limit: -7.616865E-04 high limit: 7.642389E-04

%PASS - CALCUB test of source voltage at 1V

Measured: 1.000122 low limit: 0.9991820 high limit: 1.000817

%PASS - CALCUB test of voltage measure at 1V

Measured: 1.000114 low limit: 0.9993597 high limit: 1.000885

%PASS - CALCUB test of source voltage at 2V

Measured: 1.999758 low limit: 1.998982 high limit: 2.001017

%PASS - CALCUB test of voltage measure at 2V

Measured: 1.999751 low limit: 1.998995 high limit: 2.000521

%PASS - CALCUB test of source voltage at 2V at 200mA

Measured: 1.999246 low limit: 1.998764 high limit: 2.001235

%PASS - CALCUB test of source voltage at 3V

Measured: 2.999835 low limit: 2.998782 high limit: 3.001217

%PASS - CALCUB test of voltage measure at 3V

Measured: 2.999780 low limit: 2.999072 high limit: 3.000598

%PASS - CALCUB test of source voltage at 3V with DGS perturbed high

Measured: 3.175300 low limit: 3.1 high limit: 3.3

%PASS - CALCUB test of voltage measure at 3V with DGS perturbed high

Measured: 2.999794 low limit: 2.999072 high limit: 3.000598

%PASS - CALCUB test of source voltage at 3V with DGS perturbed low

Measured: 2.779827 low limit: 2.7 high limit: 2.9

%PASS - CALCUB test of voltage measure at 3V with DGS perturbed low

Measured: 2.999774 low limit: 2.999072 high limit: 3.000598

%PASS - CALCUB test of source voltage at 4V

Measured: 3.999845 low limit: 3.998582 high limit: 4.001417

%PASS - CALCUB test of voltage measure at 4V

Measured: 3.999883 low limit: 3.999082 high limit: 4.000608

%PASS - CALCUB test of source voltage at 5V

Measured: 4.999922 low limit: 4.998382 high limit: 5.001617

%PASS - CALCUB test of voltage measure at 5V

Measured: 4.999959 low limit: 4.999159 high limit: 5.000685

%PASS - CALCUB test of source voltage at 6V

Measured: 6.000011 low limit: 5.998182 high limit: 6.001817

%PASS - CALCUB test of voltage measure at 6V

Measured: 5.999994 low limit: 5.999248 high limit: 6.000774

%PASS - CALCUB test of source voltage at 7V

Measured: 6.999676 low limit: 6.997982 high limit: 7.002017

%PASS - CALCUB test of voltage measure at 7V

Measured: 6.999730 low limit: 6.998913 high limit: 7.000439

%PASS - CALCUB test of source voltage at 8V

Measured: 7.999780 low limit: 7.997782 high limit: 8.002217

%PASS - CALCUB test of voltage measure at 8V

Measured: 7.999942 low limit: 7.999017 high limit: 8.000543

%PASS - CALCUB test of source voltage at 9V

Measured: 8.999832 low limit: 8.997582 high limit: 9.002417

%PASS - CALCUB test of voltage measure at 9V

Measured: 8.999788 low limit: 8.999069 high limit: 9.000595

%PASS - CALCUB test of source voltage at 10V

Measured: 10.000013 low limit: 9.997382 high limit: 10.002617

%PASS - CALCUB test of voltage measure at 10V

Measured: 9.999858 low limit: 9.996961 high limit: 10.003065

%PASS - CALCUB test of source voltage at 11V

Measured: 10.999861 low limit: 10.997182 high limit: 11.002817

%PASS - CALCUB test of voltage measure at 11V

Measured: 10.999850 low limit: 10.996809 high limit: 11.002913

%PASS - CALCUB test of source voltage at 12V

Measured: 12.000010 low limit: 11.996982 high limit: 12.003017

%PASS - CALCUB test of voltage measure at 12V

Measured: 12.000072 low limit: 11.996958 high limit: 12.003062

%PASS - CALCUB test of source voltage at 13V

Measured: 12.999791 low limit: 12.996782 high limit: 13.003217

%PASS - CALCUB test of voltage measure at 13V

Measured: 12.999753 low limit: 12.996739 high limit: 13.002843

%PASS - CALCUB test of source voltage at 14V

Measured: 14.000056 low limit: 13.996582 high limit: 14.003417

%PASS - CALCUB test of voltage measure at 14V

Measured: 14.000046 low limit: 13.997004 high limit: 14.003108

%PASS - CALCUB test of source voltage at 15V

Measured: 15.000291 low limit: 14.996382 high limit: 15.003617

%PASS - CALCUB test of voltage measure at 15V

Measured: 15.000313 low limit: 14.997239 high limit: 15.003343

%PASS - CALCUB test of source voltage at 16V

Measured: 15.999634 low limit: 15.996182 high limit: 16.003817

%PASS - CALCUB test of voltage measure at 16V

Measured: 15.999510 low limit: 15.996582 high limit: 16.002686

%PASS - CALCUB test of source voltage at 17V

Measured: 16.999907 low limit: 16.995982 high limit: 17.004017

%PASS - CALCUB test of voltage measure at 17V

Measured: 16.999933 low limit: 16.996855 high limit: 17.002959

%PASS - CALCUB test of source voltage at 18V

Measured: 18.000092 low limit: 17.995782 high limit: 18.004217

%PASS - CALCUB test of voltage measure at 18V

Measured: 18.000062 low limit: 17.997041 high limit: 18.003144

%PASS - CALCUB test of source voltage at 19V

Measured: 19.000262 low limit: 18.995582 high limit: 19.004417

%PASS - CALCUB test of voltage measure at 19V

Measured: 19.000270 low limit: 18.997210 high limit: 19.003314

%PASS - CALCUB test of source voltage at 20V

Measured: 19.999681 low limit: 19.995382 high limit: 20.004617

%PASS - CALCUB test of voltage measure at 20V

Measured: 19.999709 low limit: 19.996630 high limit: 20.002733

%PASS - CALCUB test of source voltage at 21V

Measured: 20.999793 low limit: 20.995182 high limit: 21.004817

%PASS - CALCUB test of voltage measure at 21V

Measured: 20.999926 low limit: 20.996742 high limit: 21.002845

%PASS - CALCUB test of source voltage at 22V

Measured: 22.000004 low limit: 21.994982 high limit: 22.005017

%PASS - CALCUB test of voltage measure at 22V

Measured: 22.000078 low limit: 21.996952 high limit: 22.003056

%PASS - CALCUB test of source voltage at 23V

Measured: 22.999886 low limit: 22.994782 high limit: 23.005217

%PASS - CALCUB test of voltage measure at 23V

Measured: 22.999911 low limit: 22.996834 high limit: 23.002938

%PASS - CALCUB test of source voltage at 24V

Measured: 23.999995 low limit: 23.994582 high limit: 24.005417

%PASS - CALCUB test of voltage measure at 24V

Measured: 24.000038 low limit: 23.996944 high limit: 24.003047

- Performing current measure verification...

%PASS - CALCUB test of current measure at -0.00002548366158mA with 2Mohm source impedance

Measured: -2.5508E-05 low limit: -2.6704E-05 high limit: -2.4262E-05

%PASS - CALCUB test of current measure at 0.00002422752727mA with 2Mohm source impedance

Measured: 2.3756E-05 low limit: 2.3006E-05 high limit: 2.5448E-05

%PASS - CALCUB test of current measure at -0.00005047085305mA with 2Mohm source impedance

Measured: -5.0110E-05 low limit: -5.1691E-05 high limit: -4.9250E-05

%PASS - CALCUB test of current measure at 0.00004921368797mA with 2Mohm source impedance

Measured: 4.8718E-05 low limit: 4.7992E-05 high limit: 5.0434E-05

%PASS - CALCUB test of current measure at -0.0001002280658mA with 2Mohm source impedance

Measured: -9.9908E-05 low limit: -1.0144E-04 high limit: -9.9007E-05

%PASS - CALCUB test of current measure at 0.00009897440529mA with 2Mohm source

impedance

Measured: 9.8495E-05 low limit: 9.7753E-05 high limit: 1.0019E-04

%PASS - CALCUB test of current measure at -0.0001993688244mA with 2Mohm source
impedance

Measured: -1.9928E-04 low limit: -2.0058E-04 high limit: -1.9814E-04

%PASS - CALCUB test of current measure at 0.0001983213693mA with 2Mohm source
impedance

Measured: 1.9783E-04 low limit: 1.9710E-04 high limit: 1.9954E-04

%PASS - CALCUB test of current measure at -0.000497972916mA with 2Mohm source
impedance

Measured: -4.9779E-04 low limit: -4.9919E-04 high limit: -4.9675E-04

%PASS - CALCUB test of current measure at 0.0004966864513mA with 2Mohm source
impedance

Measured: 4.9635E-04 low limit: 4.9546E-04 high limit: 4.9790E-04

%PASS - CALCUB test of current measure at -0.001014915948mA with 2Mohm source
impedance

Measured: -1.0149E-03 low limit: -1.0161E-03 high limit: -1.0136E-03

%PASS - CALCUB test of current measure at 0.001013980504mA with 2Mohm source
impedance

Measured: 1.0133E-03 low limit: 1.0127E-03 high limit: 1.0152E-03

%PASS - CALCUB test of current measure at -0.002029113771mA with 2Mohm source
impedance

Measured: -2.0292E-03 low limit: -2.0303E-03 high limit: -2.0278E-03

%PASS - CALCUB test of current measure at 0.002028013788mA with 2Mohm source
impedance

Measured: 2.0277E-03 low limit: 2.0267E-03 high limit: 2.0292E-03

%PASS - CALCUB test of current measure at -0.0002114576756mA with 200Kohm source
impedance

Measured: -2.1258E-04 low limit: -2.2366E-04 high limit: -1.9925E-04

%PASS - CALCUB test of current measure at 0.000201153447mA with 200Kohm source impedance

Measured: 1.9775E-04 low limit: 1.8894E-04 high limit: 2.1336E-04

%PASS - CALCUB test of current measure at -0.000419549541mA with 200Kohm source impedance

Measured: -4.1637E-04 low limit: -4.3175E-04 high limit: -4.0734E-04

%PASS - CALCUB test of current measure at 0.0004093332113mA with 200Kohm source impedance

Measured: 4.0564E-04 low limit: 3.9712E-04 high limit: 4.2154E-04

%PASS - CALCUB test of current measure at -0.0009964767101mA with 200Kohm source impedance

Measured: -9.9337E-04 low limit: -1.0086E-03 high limit: -9.8426E-04

%PASS - CALCUB test of current measure at 0.0009853322591mA with 200Kohm source impedance

Measured: 9.8102E-04 low limit: 9.7312E-04 high limit: 9.9753E-04

%PASS - CALCUB test of current measure at -0.001985028143mA with 200Kohm source impedance

Measured: -1.9826E-03 low limit: -1.9972E-03 high limit: -1.9728E-03

%PASS - CALCUB test of current measure at 0.001973055025mA with 200Kohm source impedance

Measured: 1.9697E-03 low limit: 1.9608E-03 high limit: 1.9852E-03

%PASS - CALCUB test of current measure at -0.004955216372mA with 200Kohm source impedance

Measured: -4.9535E-03 low limit: -4.9674E-03 high limit: -4.9430E-03

%PASS - CALCUB test of current measure at 0.004942761701mA with 200Kohm source impedance

Measured: 4.9411E-03 low limit: 4.9305E-03 high limit: 4.9549E-03

%PASS - CALCUB test of current measure at -0.01012722727mA with 200Kohm source

impedance

Measured: $-1.0126\text{E-}02$ low limit: $-1.0139\text{E-}02$ high limit: $-1.0115\text{E-}02$

%PASS - CALCUB test of current measure at 0.01011265792mA with 200Kohm source
impedance

Measured: $1.0111\text{E-}02$ low limit: $1.0100\text{E-}02$ high limit: $1.0124\text{E-}02$

%PASS - CALCUB test of current measure at -0.02024631448mA with 200Kohm source
impedance

Measured: $-2.0246\text{E-}02$ low limit: $-2.0258\text{E-}02$ high limit: $-2.0234\text{E-}02$

%PASS - CALCUB test of current measure at 0.02023381292mA with 200Kohm source
impedance

Measured: $2.0232\text{E-}02$ low limit: $2.0221\text{E-}02$ high limit: $2.0246\text{E-}02$

%PASS - CALCUB test of current measure at -0.002039698438mA with 20Kohm source
impedance

Measured: $-2.0443\text{E-}03$ low limit: $-2.1617\text{E-}03$ high limit: $-1.9176\text{E-}03$

%PASS - CALCUB test of current measure at 0.001941747724mA with 20Kohm source
impedance

Measured: $1.9130\text{E-}03$ low limit: $1.8196\text{E-}03$ high limit: $2.0638\text{E-}03$

%PASS - CALCUB test of current measure at -0.004048069435mA with 20Kohm source
impedance

Measured: $-4.0138\text{E-}03$ low limit: $-4.1701\text{E-}03$ high limit: $-3.9259\text{E-}03$

%PASS - CALCUB test of current measure at 0.003950605807mA with 20Kohm source
impedance

Measured: $3.9176\text{E-}03$ low limit: $3.8285\text{E-}03$ high limit: $4.0726\text{E-}03$

%PASS - CALCUB test of current measure at -0.009789018529mA with 20Kohm source
impedance

Measured: $-9.7576\text{E-}03$ low limit: $-9.9110\text{E-}03$ high limit: $-9.6669\text{E-}03$

%PASS - CALCUB test of current measure at 0.009669876165mA with 20Kohm source
impedance

Measured: $9.6384\text{E-}03$ low limit: $9.5478\text{E-}03$ high limit: $9.7919\text{E-}03$

%PASS - CALCUB test of current measure at -0.01950039596mA with 20Kohm source impedance

Measured: -1.9472E-02 low limit: -0.019622 high limit: -0.019378

%PASS - CALCUB test of current measure at 0.01938077179mA with 20Kohm source impedance

Measured: 1.9351E-02 low limit: 0.019258 high limit: 0.019502

%PASS - CALCUB test of current measure at -0.04867521518mA with 20Kohm source impedance

Measured: -4.8659E-02 low limit: -0.048797 high limit: -0.048553

%PASS - CALCUB test of current measure at 0.0485542238mA with 20Kohm source impedance

Measured: 4.8539E-02 low limit: 0.048432 high limit: 0.048676

%PASS - CALCUB test of current measure at -0.1012077053mA with 20Kohm source impedance

Measured: -0.10119 low limit: -0.10132 high limit: -0.10108

%PASS - CALCUB test of current measure at 0.1010602734mA with 20Kohm source impedance

Measured: 0.10105 low limit: 0.10093 high limit: 0.10118

%PASS - CALCUB test of current measure at -0.202330552mA with 20Kohm source impedance

Measured: -0.20233 low limit: -0.20245 high limit: -0.20220

%PASS - CALCUB test of current measure at 0.2022046305mA with 20Kohm source impedance

Measured: 0.20220 low limit: 0.20208 high limit: 0.20232

%PASS - CALCUB test of current measure at -0.01782096917mA with 2Kohm source impedance

Measured: -1.7891E-02 low limit: -1.9041E-02 high limit: -1.6600E-02

%PASS - CALCUB test of current measure at 0.016958604mA with 2Kohm source impedance

Measured: 1.6617E-02 low limit: 1.5737E-02 high limit: 1.8179E-02

%PASS - CALCUB test of current measure at -0.03536608021mA with 2Kohm source impedance

Measured: -3.5443E-02 low limit: -3.6586E-02 high limit: -3.4145E-02

%PASS - CALCUB test of current measure at 0.03450422212mA with 2Kohm source impedance

Measured: 0.034136 low limit: 3.3283E-02 high limit: 3.5724E-02

%PASS - CALCUB test of current measure at -0.09737856141mA with 2Kohm source impedance

Measured: -9.7102E-02 low limit: -9.8599E-02 high limit: -9.6157E-02

%PASS - CALCUB test of current measure at 0.09618902999mA with 2Kohm source impedance

Measured: 9.5826E-02 low limit: 9.4968E-02 high limit: 9.7409E-02

%PASS - CALCUB test of current measure at -0.1939793202mA with 2Kohm source impedance

Measured: -0.19375 low limit: -0.19520 high limit: -0.19275

%PASS - CALCUB test of current measure at 0.1927923472mA with 2Kohm source impedance

Measured: 0.19253 low limit: 0.19157 high limit: 0.19401

%PASS - CALCUB test of current measure at -0.4842023936mA with 2Kohm source impedance

Measured: -0.48402 low limit: -0.48542 high limit: -0.48298

%PASS - CALCUB test of current measure at 0.4830183636mA with 2Kohm source impedance

Measured: 0.48285 low limit: 0.48179 high limit: 0.48423

%PASS - CALCUB test of current measure at -1.011165939mA with 2Kohm source impedance

Measured: -1.0110 low limit: -1.0123 high limit: -1.0099

%PASS - CALCUB test of current measure at 1.009677022mA with 2Kohm source impedance

Measured: 1.0096 low limit: 1.0084 high limit: 1.0108

%PASS - CALCUB test of current measure at -2.021541464mA with 2Kohm source impedance

Measured: -2.0215 low limit: -2.0227 high limit: -2.0203

%PASS - CALCUB test of current measure at 2.020243174mA with 2Kohm source impedance

Measured: 2.0202 low limit: 2.0190 high limit: 2.0214

%PASS - CALCUB test of current measure at -0.171468053mA with 200ohm source impedance

Measured: -0.17202 low limit: -0.18367 high limit: -0.15926

%PASS - CALCUB test of current measure at 0.1632159639mA with 200ohm source impedance

Measured: 0.15971 low limit: 0.15100 high limit: 0.17542

%PASS - CALCUB test of current measure at -0.3402761835mA with 200ohm source impedance
Measured: -0.34097 low limit: -0.35248 high limit: -0.32806

%PASS - CALCUB test of current measure at 0.3320683975mA with 200ohm source impedance
Measured: 0.32853 low limit: 0.31986 high limit: 0.34427

%PASS - CALCUB test of current measure at -0.9636700715mA with 200ohm source impedance
Measured: -0.96081 low limit: -0.97587 high limit: -0.95146

%PASS - CALCUB test of current measure at 0.9518613767mA with 200ohm source impedance
Measured: 0.94873 low limit: 0.93965 high limit: 0.96406

%PASS - CALCUB test of current measure at -1.919697093mA with 200ohm source impedance
Measured: -1.9166 low limit: -1.9319 high limit: -1.9074

%PASS - CALCUB test of current measure at 1.908073662mA with 200ohm source impedance
Measured: 1.9054 low limit: 1.8958 high limit: 1.9202

%PASS - CALCUB test of current measure at -4.79174343mA with 200ohm source impedance
Measured: -4.7902 low limit: -4.8039 high limit: -4.7795

%PASS - CALCUB test of current measure at 4.780734432mA with 200ohm source impedance
Measured: 4.7792 low limit: 4.7685 high limit: 4.7929

%PASS - CALCUB test of current measure at -10.00020327mA with 200ohm source impedance
Measured: -10.0000 low limit: -10.0124 high limit: -9.9879

%PASS - CALCUB test of current measure at 9.987844876mA with 200ohm source impedance
Measured: 9.9873 low limit: 9.9756 high limit: 10.0000

%PASS - CALCUB test of current measure at -19.99269184mA with 200ohm source impedance
Measured: -19.9926 low limit: -20.0048 high limit: -19.9804

%PASS - CALCUB test of current measure at 19.98432363mA with 200ohm source impedance
Measured: 19.9840 low limit: 19.9721 high limit: 19.9965

%PASS - CALCUB test of current measure at -1.602397955mA with 20ohm source impedance
Measured: -1.6031 low limit: -1.7244 high limit: -1.4803

%PASS - CALCUB test of current measure at 1.525122261mA with 20ohm source impedance
Measured: 1.4924 low limit: 1.4030 high limit: 1.6471

%PASS - CALCUB test of current measure at -3.179995032mA with 20ohm source impedance
Measured: -3.1854 low limit: -3.3020 high limit: -3.0579

%PASS - CALCUB test of current measure at 3.105214954mA with 20ohm source impedance
Measured: 3.0722 low limit: 2.9831 high limit: 3.2272

%PASS - CALCUB test of current measure at -8.749024907mA with 20ohm source impedance
Measured: -8.7200 low limit: -8.8710 high limit: -8.6269

%PASS - CALCUB test of current measure at 8.658026542mA with 20ohm source impedance
Measured: 8.6293 low limit: 8.5359 high limit: 8.7801

%PASS - CALCUB test of current measure at -17.4295739mA with 20ohm source impedance
Measured: -17.4041 low limit: -17.5516 high limit: -17.3074

%PASS - CALCUB test of current measure at 17.35516851mA with 20ohm source impedance
Measured: 17.3241 low limit: 17.2330 high limit: 17.4772

%PASS - CALCUB test of current measure at -43.50618675mA with 20ohm source impedance
Measured: -43.4896 low limit: -43.6282 high limit: -43.3841

%PASS - CALCUB test of current measure at 43.48349427mA with 20ohm source impedance
Measured: 43.4696 low limit: 43.3614 high limit: 43.6055

%PASS - CALCUB test of current measure at -90.94504982mA with 20ohm source impedance
Measured: -90.9399 low limit: -91.0671 high limit: -90.8229

%PASS - CALCUB test of current measure at 90.99900747mA with 20ohm source impedance
Measured: 90.9920 low limit: 90.8769 high limit: 91.1210

%PASS - CALCUB test of current measure at -9.779457719mA with 2ohm source impedance
Measured: -9.8399 low limit: -11.0001 high limit: -8.5587

%PASS - CALCUB test of current measure at 9.381630106mA with 2ohm source impedance

Measured: 9.0967 low limit: 8.1608 high limit: 10.6023

%PASS - CALCUB test of current measure at -19.41007809mA with 2ohm source impedance

Measured: -19.5524 low limit: -20.6308 high limit: -18.1893

%PASS - CALCUB test of current measure at 19.09936493mA with 2ohm source impedance

Measured: 18.8262 low limit: 17.8786 high limit: 20.3201

%PASS - CALCUB test of current measure at -47.72286575mA with 2ohm source impedance

Measured: -47.5026 low limit: -48.9436 high limit: -46.5021

%PASS - CALCUB test of current measure at 47.63299649mA with 2ohm source impedance

Measured: 47.3450 low limit: 46.4122 high limit: 48.8537

%PASS - CALCUB test of current measure at -95.40254132mA with 2ohm source impedance

Measured: -95.2625 low limit: -96.6232 high limit: -94.1818

%PASS - CALCUB test of current measure at 95.82896918mA with 2ohm source impedance

Measured: 95.5693 low limit: 94.6082 high limit: 97.0497

%PASS - CALCUB test of current measure at -238.2950944mA with 2ohm source impedance

Measured: -238.3596 low limit: -239.5158 high limit: -237.0743

%PASS - CALCUB test of current measure at 239.9776467mA with 2ohm source impedance

Measured: 240.0065 low limit: 238.7569 high limit: 241.1983

- Performing 100Mhz clock accuracy verification...

%PASS - CALCUB Clock 100 External Performance Verification test.

Measured: 99999295.6 low limit: 99996000 high limit: 100004000

%JOB_END - ****PASSED**** CUB External Verification of slot 18 (1FC1CA) at

4:47:58 PM